YOSHIKI FUJIWARA

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RESEARCH INTEREST

Deep Learning Accelerators / Domain Specific Architectures / Algorithm-Hardware Co-design

EDUCATION

The University of Tokyo *April 2021 - Present*
M.S. in Computer Science. GPA: -
Graduate School of Information Science and Technology
Advisor: Shinya Takamaeda-Yamazaki

The University of Tokyo *April 2017 - March 2021*
Bachelor of Science in Computer Science. GPA: 3.9/4.0. (Note that **my major GPA is 4.0/4.0**.)
Department of Information Science, Faculty of Science
Advisor: Shinya Takamaeda-Yamazaki

PUBLICATION

1. **Fujiwara, Y.** & Shinya, T.
   "ASBNN: Acceleration of Bayesian Convolutional Neural Networks by Algorithm-hardware Co-design"
   Full paper accepted in Application-Specific Systems, Architectures and Processors (ASAP) 2021.

PUBLICATION & TALK (JAPANESE)

1. **Fujiwara, Y.** & Shinya, T.
   "Acceleration of Bayesian Convolutional Neural Networks by Algorithm-hardware Co-design"
   Summer United Workshops on Parallel, Distributed and Cooperative Processing (SWoPP) 2021.
2. **Fujiwara, Y.** & Okada, S. & Ito, Y. & Yoshikura, M. & Kusumi, R., Mitsunaga, T.
   "Realization and Improvement of DX for Municipal Activities based on Digital Business Models"
   The 83rd National Convention of Information Processing Society of Japan.

RESEARCH JOBS

The University of Tokyo *April 2021 - Present*
Research Assistant

- **Algorithm/Hardware Co-design for Bayesian Neural Networks**: In this project, I focused on algorithm/hardware co-design for Bayesian Convolutional Neural Networks. I found a bottleneck of the computations and proposed a new approximation method for the computation. To support the approximation algorithm efficiently on hardware, I proposed a novel hardware design.

The University of Tokyo & Toyo University *February 2019 - Present*
Research Assistant

- **Malicious Information Sharing Systems**: As a research assistant, I started a project related with computer security. I built a system that can share malicious information among companies and automatically include them in the network configuration by using Software Designed Networks and STIX format.

TEACHING JOBS

The University of Tokyo *April 2021 - Present*
Teaching Assistant

- **Hardware Laboratory**: I support a class for undergraduates covering circuit design using breadboards and implementation of important circuits, such as FPU and UART, using Verilog HDL.

The University of Tokyo & Toyo University *February 2019 - Present*
Teaching Assistant

- **Industrial Control Systems**: As a teaching assistant, I am involved in ICS security. Our team provides lectures and hands-on training to deepen the understanding of ICS security. In hands-on training, we attack pump systems that mimic factory systems connected to the Internet. The security lectures and training were provided not only for the university but also for the electric power companies in various countries through the Ministry of Economy, Trade and Industry in Japan. I was involved in the construction of the hands-on training and technical support for the lecture. My name is written in the following link in 2021.
  2021: `https://www.meti.go.jp/english/press/2021/0315_001.html`,
  2019: `https://www.meti.go.jp/english/press/2019/0912_002.html`

INTERNSHIP EXPERIENCE

NTT Data *September 2019*
Development

- I participated in a project to create a system that uses the newly introduced national system called "my number." My contribution to the project was to propose a safe system for handling the "my number" information and involve in its development.

Amazon Web Services Japan *August 2019*
Solution Architect

- I participated in a project to manage a large web page with huge traffic using AWS and find the optimal configuration and modification for their requirements. My contribution to the project was to propose the system configuration and created a mock for the proposal.

QUALIFICATION & AWARD

Applied Information Technology Engineer *December 2020*

- Japanese qualification that qualifies that I have applied knowledge and skills as an IT engineer.

Sugaku Koshien Final Round *September 2016*

- The Japanese event for selecting top high school students in mathematics. I was in the final round (top 50).

GRANT PROGRAM

Education Network for Practical Information Technologies (enPiT) *March 2021*

- Japanese program that qualifies the students who have enough knowledge about "Big Data Analysis", "Security", "Embedded Systems", and "System Designs."

NICT Quantum Camp *March 2021*

- NICT's program to foster quantum information specialists

Deloitte & The University of Tokyo SiSOC Cyber Security Training *September 2018*

- A Course to learn the basic of the cyber security through competition called CTF. I was top 10 of the competition.

TECHNICAL SKILLS

GitHub	https://github.com/yoshi-ki
My Coding Experience	https://github.com/yoshi-ki/BACHELOR
Tech Blog	https://yoshi-ki.medium.com
Frequently Used Language	C, C++, Python, Verilog HDL
Frequently Used Software Tools	PyTorch, Vitis HLS

ENGLISH

TOEFL iBT: 100 (My Best Score: 102)