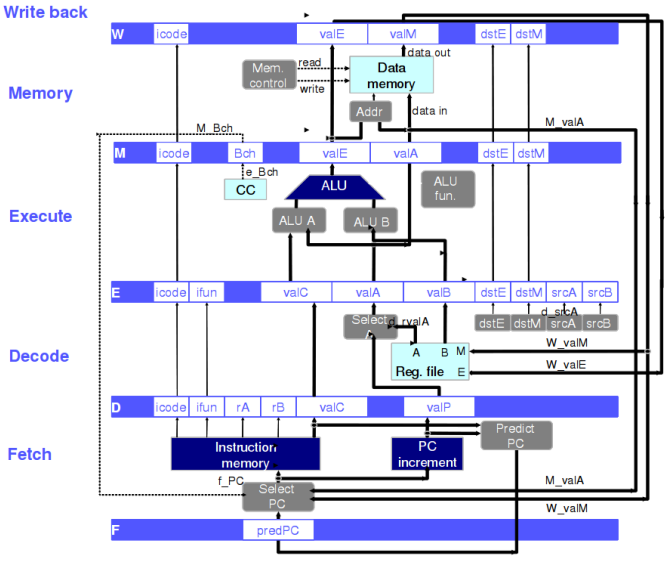
Y86-64 Instructions Encoding

Byte	0		1		2	3	4	5	6	7	8	9
halt	0	0										
nop	1	0										
rrmovq **rA**, **rB**	2	0	**rA**	**rB**								
cmovXX **rA**, **rB**	2	fn	**rA**	**rB**								
irmovq **V**, **rB**	3	0	F	**rB**	**V**							
rmmovq **rA**, **D**(**rB**)	4	0	**rA**	**rB**	**D**							
mrmovq **D**(**rB**), **rA**	5	0	**rA**	**rB**	**D**							
OPq **rA**, **rB**	6	fn	**rA**	**rB**								
jXX **Dest**	7	fn	**Dest**									
call **Dest**	8	0	**Dest**									
ret	9	0										
pushq **rA**	A	0	**rA**	F								
popq **rA**	B	0	**rA**	F								

Y86-64 ISA Reference

Instruction	Semantics	Example
rrmovq %rs, %rd	r[rd] ← r[rs]	rrmovq %rax, %rbx
cmovXX %rs, %rd	r[rd] ← r[rs] if last ALU result XX 0 (XX is le/l/e/ne/ge/g)	cmovle %rax, %rbx
irmovq $i, %rd	r[rd] ← i	irmovq $100, %rax
rmmovq %rs, D(%rd)	m[D + r[rd]] ← r[rs]	rmmovq %rax, 100(%rbx)
mrmovq D(%rs), %rd	r[rd] ← m[D + r[rs]]	mrmovq 100(%rbx), %rax
addq %rs, %rd	r[rd] ← r[rd] + r[rs]	addq %rax, %rbx
subq %rs, %rd	r[rd] ← r[rd] - r[rs]	subq %rax, %rbx
andq %rs, %rd	r[rd] ← r[rd] & r[rs]	andq %rax, %rbx
xorq %rs, %rd	r[rd] ← r[rd] ⊕ r[rs]	xorq %rax, %rbx
jmp D	goto D	jmp foo
jXX D	goto D if last ALU result XX 0 (XX is le/l/e/ne/ge/g)	jle foo
call D	pushq PC; jmp D	call foo
ret	popq PC	ret
pushq %rs	m[r[rsp] - 8] ← r[rs]; r[rsp] = r[rsp] - 8	pushq %rax
popq %rd	r[rd] ← m[r[rsp]]; r[rsp] = r[rsp] + 8	popq %rax



Register Number	Register Name
0	%rax
1	%rcx
2	%rdx
3	%rbx
4	%rsp
5	%rbp
6	%rsi
7	%rdi
8	%r8
9	%r9
A (10)	%r10
B (11)	%r11
C (12)	%r12
D (13)	%r13
E (14)	%r14
F (15)	NO REGISTER