# Mathison

Core Commands

(LOAD|STORE)[NEXT][BIG] var (ACC|TEMP)

UNREAD var

MAP map

JUMP label

BRANCH label0 label1 label2 label3

LOADI imm (ACC|TEMP)

Other Primitive Commands

SLLs vard imm

SRLs vard imm

SLL2s vard imm

SRL2s vard imm

NOTs vard

COMPs vard

ZEROs vard imm

ADDIs vard imm

SUBIs vard imm

Higher Level Commands

ADD vard var0 var1

SUB vard var0 var1

BOL vard var0 var1 map

ADDs vard var0

SUBs vard var0

BOLs vard var0 map

SLL vard var0 imm

SRL vard var0 imm

SLT var0 var1

MULT vard var0 var1

DIV vard varr var0 var1

RECP vard varr var0

# Core Commands

Load Unread Bit

LOAD var ACC

LOAD var TEMP

LOADBIG var ACC

LOADBIG var TEMP

Load the next smallest or largest unread bit of var into ACC or TEMP

If out of bounds, jump to the next *oob* label

Load Next Unread Bit

LOADNEXT var ACC

LOADNEXT var TEMP

LOADNEXTBIG var ACC

LOADNEXTBIG var TEMP

Load the next smallest or largest unread bit of var into ACC or TEMP and mark it as read

If out of bounds, jump to the next *oob* label

Store Unread Bit

STORE var ACC

STORE var TEMP

STOREBIG var ACC

STOREBIG var TEMP

Store the value of ACC or TEMP in the next smallest or largest unread bit of var

If out of bounds, jump to the next *oob* label

Store Next Unread Bit

STORENEXT var ACC

STORENEXT var TEMP

STORENEXTBIG var ACC

STORENEXTBIG var TEMP

Store the value of ACC or TEMP in the next smallest or largest unread bit of var and mark it as read

If out of bounds, jump to the next *oob* label

Un-Read Variable

UNREAD var

Mark all bits of var as unread

Map Memory

MAP map

Change the value of ACC and/or TEMP based on a map function

A map is a series of a comma-seperated mappings within braces

Each mapping is a colon-seperated pair of states

A state can be an ACC value (0, 1, 2, or 3) or an “x”-seperated ACC value and TEMP value (0 or 1), depending on neighboring commands

{<acc>x<temp>:<acc>, <acc>x<temp>:<acc>, ...} if directly after loading into TEMP

{<acc>:<acc>x<temp>, <acc>:<acc>x<temp>, ...} if directly before storing TEMP

{<acc>x<temp>:<acc>x<temp>, <acc>x<temp>:<acc>x<temp>, ...} if directly between loading a bit into TEMP and storing TEMP into the same bit

{<acc>:<acc>, <acc>:<acc>, …} otherwise

If a possible ACC value or ACC and TEMP value pair isn’t specified, it defaults to not changing the value of the ACC or TEMP

Unconditional Branch

JUMP label

Go to the label

Conditional Branch

BRANCH label0 label1 label2 label3

Go to the label corresponding to the value of ACC

Null can be used in place of a label

Load Immediate

LOADI imm ACC

LOADI imm TEMP

Set ACC or TEMP to imm (0, 1, 2, or 3 for ACC and 0 or 1 for TEMP)

# Other Primitive Commands

Stationary Logical Shift

SLLs vard imm

SRLs vard imm

Shift vard 1 bit to the left or right

Replaces the vacated bit with imm (0 or 1)

ACC will contain the displaced bit (0 or 1)

Stationary Double Logical Shift

SLL2s vard imm

SRL2s vard imm

Shift vard 2 bits to the left or right

Replaces the vacated bits with imm (0, 1, 2, or 3)

ACC will contain the displaced bits (0, 1, 2, or 3)

Stationary Logical Negation

NOTs vard

Set vard to its logical negation

The ACC is preserved

Stationary Two’s Complement

COMPs vard

Set vard to its logical negation

ACC will contain 1 if vard was zero; or 0 otherwise

Stationary Logical Set

ZEROs vard imm

Set all bits vard to imm (0 or 1)

The ACC is preserved

Stationary Add and Subtract Immediate

ADDIs vard imm

SUBIs vard imm

Increase or decrease vard by imm (0, 1, 2, or 3)

ACC will contain 1 if the operation overflowed; or 0 otherwise

# Higher Level Commands

Add and Subtract

ADD vard var0 var1

SUB vard var0 var1

Add or subtract var0 and var1 and store the result in vard

ACC will contain 1 if the operation overflowed; or 0 otherwise

Boolean Operation

BOL vard var0 var1 map

Apply a bitwise operation to var0 and var1 using a map function and store the result in vard

{<bit0>x<bit1>:<bitd>, <bit0>x<bit1>:<bitd>, ...}

Stationary Add and Subtract

ADDs vard var0

SUBs vard var0

Add var0 to vard or subtract var0 from vard

ACC will contain 1 if the operation overflowed; or 0 otherwise

Stationary Boolean Operation

BOLs vard var0 map

Apply a bitwise operation to var0 and vard using a map function and store the result in vard

{<bit0>x<bitd>:<bitd>, <bit0>x<bitd>:<bitd>, ...}

Logical Shift

SLL vard var0 imm

SRL vard var0 imm

Shift var0 1 bit to the left or right and store the result in vard

Replaces the vacated bit with imm (0 or 1)

ACC will contain the displaced bit (0 or 1)

Comparison Operator

SLT var0 var1

Test if var0 is less than var1

ACC will contain 1 if var0 is less than var1, 0 if they’re equal, or 3 if var0 is greater than var1

Multiply

MULT vard var0 var1

Multiply var0 by var1 and add the product to vard

var1 is set to zero

Divide

DIV vard varr var0 var1

Divide var0 by var1 and store the quotient in vard and the remainder in varr

Reciprocal

RECP vard varr var0

Find the reciprocal or var0 and store the fractional part of the quotient in vard and the fractional part of the remainder in varr

# Future

Test if equal to zero (or other immediates?)

Variable bit shifting?

Data structures