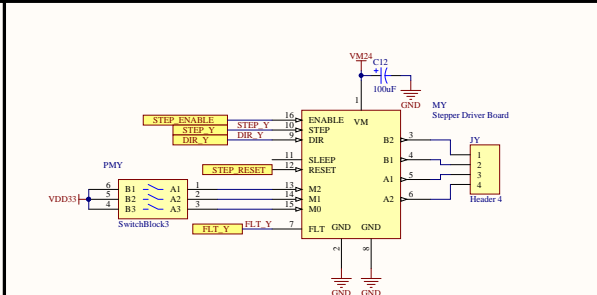
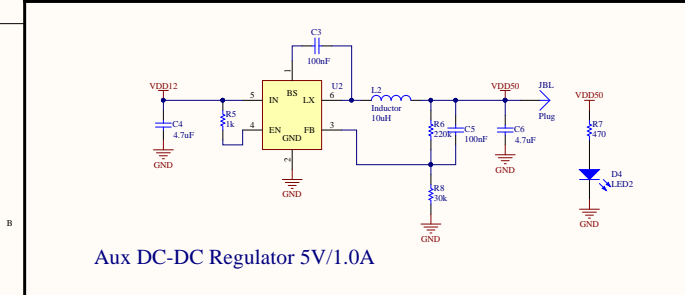
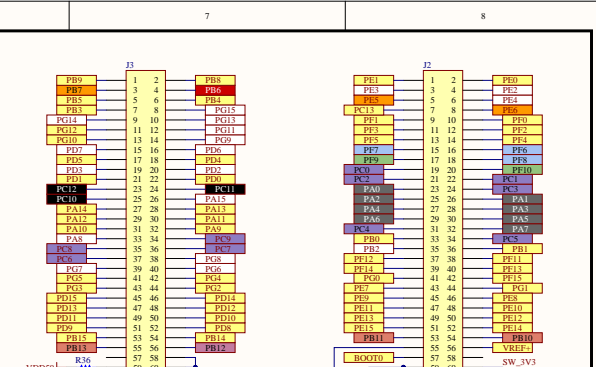
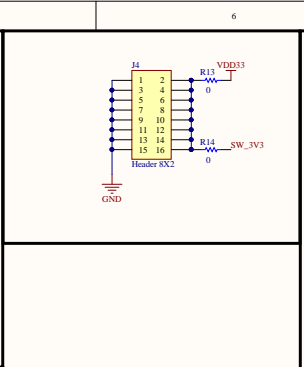
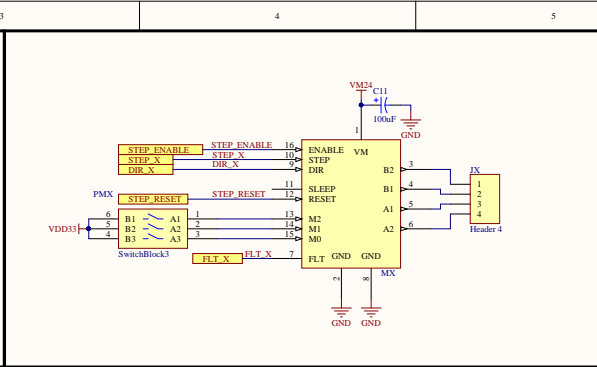
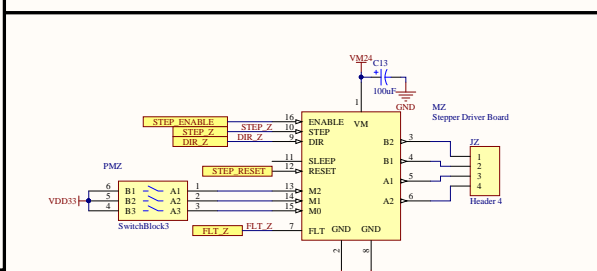
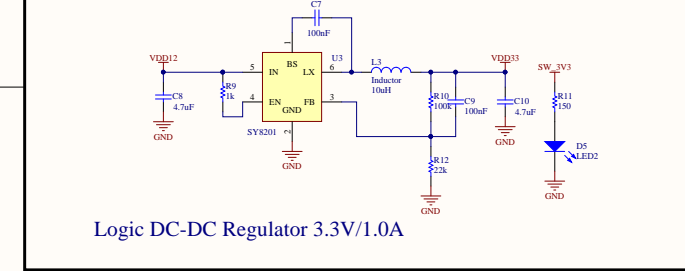


[illegible]

--	--

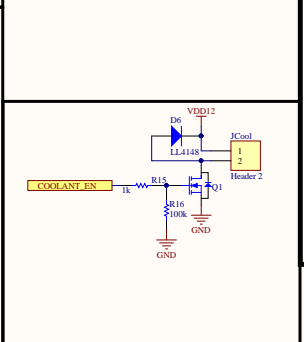
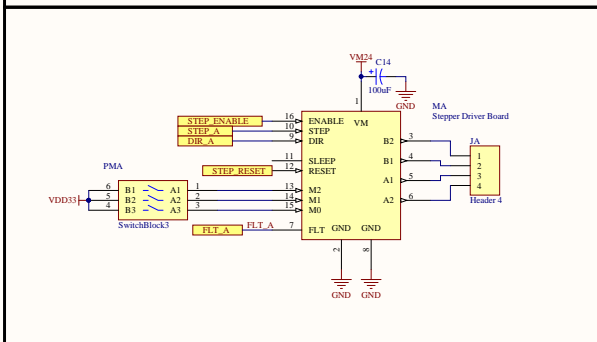
<sup>c</sup> STEP = Internal Pull-down ~100 k

DIR = Internal Pull-down ~ 100 k  
MODEx = Internal Pull-down ~ 100 k  
ENABLE = Low active, Internal PD ~ 100 k  
RESET = Low active, Internal PD ~ 100 k  
SLEEP = Low active, PU in carrier board  
FLT = Open drain

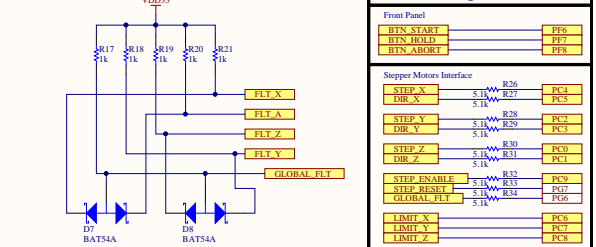
All ENABLE inputs are tied together ( $\sim 100/4 = 25\text{ k}$ )  
All RESET inputs are tied together ( $\sim 25\text{ k}$ )

To move motors:

<sup>D</sup> | ENABLE = 0 & RESET = 1



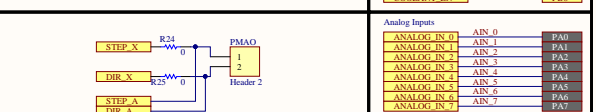
VDF032	Assignments
--------	-------------



## Fault Detection



Stepper Enable/Reset



Stepper A <=> Stepper X

<p>Stepper A <math>\Rightarrow</math> Stepper B</p> <p>DE5 TIM0.CH1</p>	<p>LED 0 LED 1</p> <p>LED 1</p>	<p>LED 1</p> <p>PF9 PF10</p>
---	---------------------------------	------------------------------

PWM Outputs	
PWM_OUT_1	PES
PWM_OUT_2	PE6

PWM_OUT_3	PWM_OUT_3	PB7
-----------	-----------	-----

PF9 - LED0  
PF10 - LED1

SPI3_MISO	SPI3_MOSI	PC11
SPI3_CS	SPI3_CS	PA15

PA15 Pull up for JTAG

PA0 .. PA7 - ADC Channels 0 .. 7	
----------------------------------	--

PA8 - TIM1 Ch1 / RCC MCO1

PA15 - SPI1/3 NSS

								<table><tr><td colspan="3">Title</td></tr><tr><td>Size A2</td><td colspan="2">Number</td><td colspan="2">Revision</td></tr><tr><td>Date: 28/07/2020</td><td colspan="2"></td><td>Sheet</td><td>of</td></tr><tr><td>File: D:\Design\NC\Altunijaddon_board.Sch</td><td colspan="2">Drawn By:</td><td colspan="2"></td></tr></table>		Title			Size A2	Number		Revision		Date: 28/07/2020			Sheet	of	File: D:\Design\NC\Altunijaddon_board.Sch	Drawn By:			
Title																											
Size A2	Number		Revision																								
Date: 28/07/2020			Sheet	of																							
File: D:\Design\NC\Altunijaddon_board.Sch	Drawn By:																										
1	2	3	4	5	6	7	8																				