



Embedded Systems Workshop

Session

4



Hello!

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Attendance

place qr code



AGENDA :



Input VS Output

Pull Up VS Pull down



input VS output



In our course we will use Microcontroller AVR Atmega32.

It has 32 DIO pins grouped as following:

1- PORTA has 8 DIO Pins from A0 to A7

2- PORTB has 8 DIO Pins from B0 to B8

3- PORTC has 8 DIO Pins from C0 to C8

4- PORTD has 8 DIO Pins from D0 to D8

PORT B

PB0	C	1	40	D	PA0
PB1	C	2	39	D	PA1
PB2	C	3	38	D	PA2
PB3	C	4	37	D	PA3
PB4	C	5	36	D	PA4
PB5	C	6	35	D	PA5
PB6	C	7	34	D	PA6
PB7	C	8	33	D	PA7
RESET	C	9	32	D	AREF
VCC	C	10	31	D	AGND
GND	C	11	ATmega	16/32	30 D AVCC
XTAL2	C	12	29	D	PC7
XTAL1	C	13	28	D	PC6
PD0	C	14	27	D	PC5
PD1	C	15	26	D	PC4
PD2	C	16	25	D	PC3
PD3	C	17	24	D	PC2
PD4	C	18	23	D	PC1
PD5	C	19	22	D	PC0
PD6	C	20	21	D	PD7

PORT A

PORT D

PORT C

Each port has 8 pins the pins of these four ports can be used as general purpose input/output (-GPIO-)

Each I/O Could be input or output (multiplexed direction). This could be controlled through the I/O port direction register

PORT B

PB0	C	1	40	D	PA0
PB1	C	2	39	D	PA1
PB2	C	3	38	D	PA2
PB3	C	4	37	D	PA3
PB4	C	5	36	D	PA4
PB5	C	6	35	D	PA5
PB6	C	7	34	D	PA6
PB7	C	8	33	D	PA7
RESET	C	9	32	D	AREF
VCC	C	10	ATmega		31 D AGND
GND	C	11	16/32		30 D AVCC
XTAL2	C	12			29 D PC7
XTAL1	C	13			28 D PC6
PD0	C	14			27 D PC5
PD1	C	15			26 D PC4
PD2	C	16			25 D PC3
PD3	C	17			24 D PC2
PD4	C	18			23 D PC1
PD5	C	19			22 D PC0
PD6	C	20			21 D PD7

PORT A

PORT D

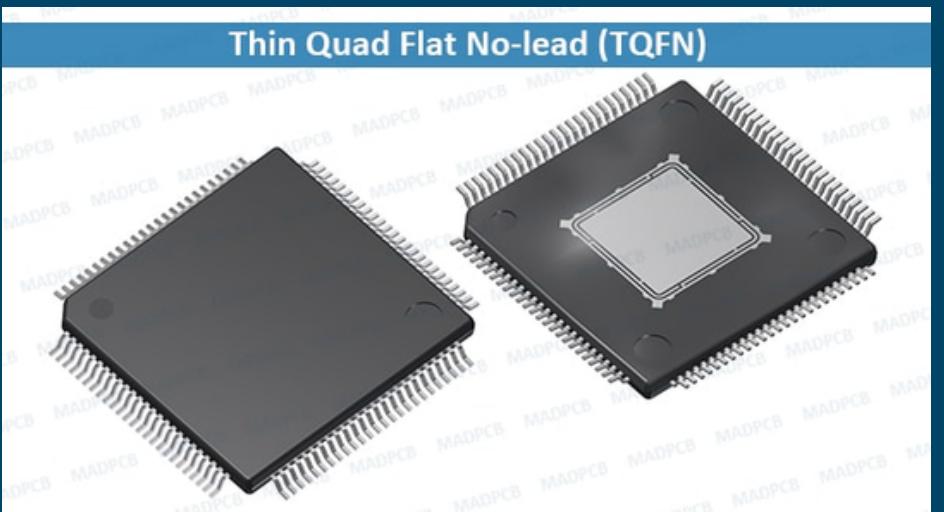
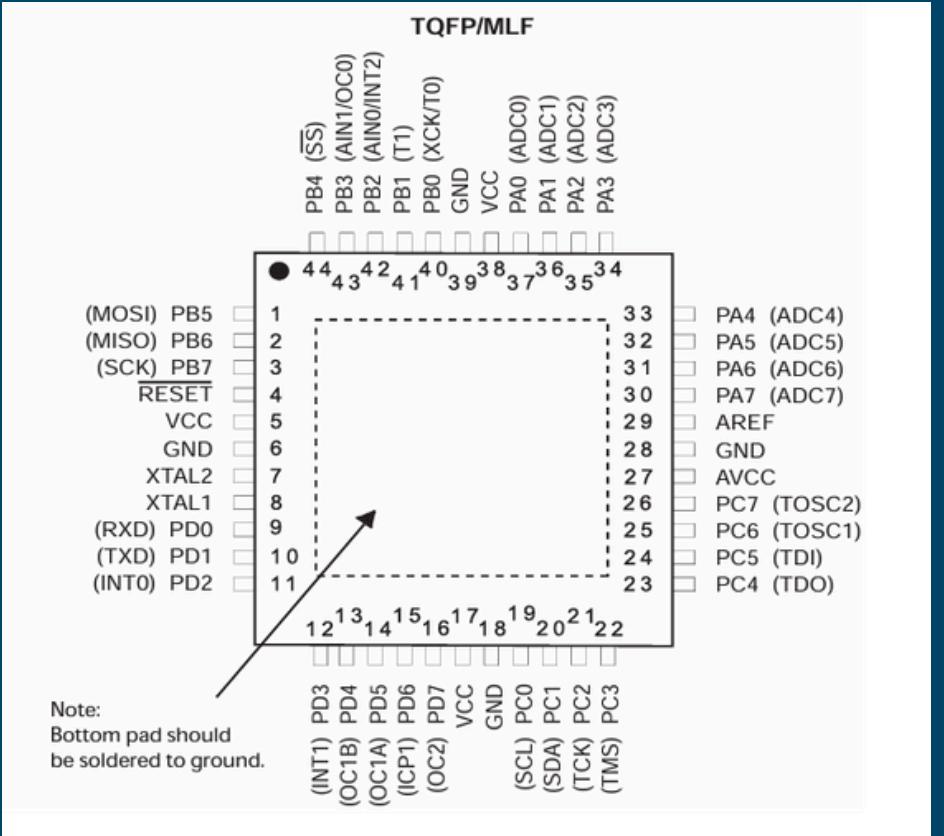
PORT C



Each MC pin could be assigned to different I/O functions. Pin Could be configured for digital I/O, analog input, Timer I/O, Serial I/O, For example PDO could be digital I/O or UART serial input (Multiplexed Functionality)

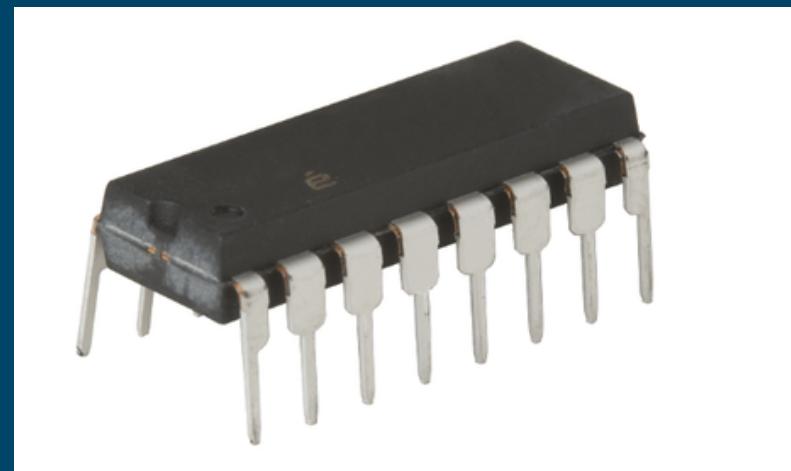
		PDIP	
(XCK/T0)	PB0	1	40 PA0 (ADC0)
(T1)	PB1	2	39 PA1 (ADC1)
(INT2/AIN0)	PB2	3	38 PA2 (ADC2)
(OC0/AIN1)	PB3	4	37 PA3 (ADC3)
(SS)	PB4	5	36 PA4 (ADC4)
(MOSI)	PB5	6	35 PA5 (ADC5)
(MISO)	PB6	7	34 PA6 (ADC6)
(SCK)	PB7	8	33 PA7 (ADC7)
	<u>RESET</u>	9	32 AREF
	VCC	10	31 GND
	GND	11	30 AVCC
	XTAL2	12	29 PC7 (TOSC2)
	XTAL1	13	28 PC6 (TOSC1)
	(RXD) PD0	14	27 PC5 (TDI)
	(TXD) PD1	15	26 PC4 (TDO)
	(INT0) PD2	16	25 PC3 (TMS)
	(INT1) PD3	17	24 PC2 (TCK)
	(OC1B) PD4	18	23 PC1 (SDA)
	(OC1A) PD5	19	22 PC0 (SCL)
	(ICP1) PD6	20	21 PD7 (OC2)

Packaging



PDIP	
(XCK/T0)	PB0
(T1)	PB1
(INT2/AIN0)	PB2
(OC0/AIN1)	PB3
(SS)	PB4
(MOSI)	PB5
(MISO)	PB6
(SCK)	PB7
RESET	
VCC	
GND	
XTAL2	
XTAL1	
(RXD)	PD0
(TXD)	PD1
(INT0)	PD2
(INT1)	PD3
(OC1B)	PD4
(OC1A)	PD5
(ICP1)	PD6
(OC2)	PD7
VCC	
GND	
(SCL)	PC0
(SDA)	PC1
(TCK)	PC2
(TMS)	PC3
PA0 (ADC0)	40
PA1 (ADC1)	39
PA2 (ADC2)	38
PA3 (ADC3)	37
PA4 (ADC4)	36
PA5 (ADC5)	35
PA6 (ADC6)	34
PA7 (ADC7)	33
AREF	32
GND	31
AVCC	30
PC7 (TOSC2)	29
PC6 (TOSC1)	28
PC5 (TDI)	27
PC4 (TDO)	26
PC3 (TMS)	25
PC2 (TCK)	24
PC1 (SDA)	23
PC0 (SCL)	22
PD7 (OC2)	21

PDIP PLASTIC DUAL IN LINE PACKAGE



Each Port is controlled by 3 registers

DDR_x(Data Direction Register)

This register used to decide the direction of the pins , i.e
Whether the pins will act as input pins or as output pins

PORT_x(Output Register)

This register is used to set the logic on the output pins

HIGH OR LOW

PIN_x (Input Register)

This register is used to read the logic level on the port
input pins



Note : x could be A,B,C, OR D depending on which
port registers are being addressed



Each Port is controlled by 3 registers

**Port A Data Register –
PORTA**

Bit	7	6	5	4	3	2	1	0	PORTA
Read/Write	R/W								
Initial Value	0	0	0	0	0	0	0	0	

**Port A Data Direction
Register – DDRA**

Bit	7	6	5	4	3	2	1	0	DDRA
Read/Write	R/W								
Initial Value	0	0	0	0	0	0	0	0	

**Port A Input Pins
Address – PINA**

Bit	7	6	5	4	3	2	1	0	PINA
Read/Write	R	R	R	R	R	R	R	R	
Initial Value	N/A								



I/O Pin Programming Steps In AVR MC

1-Decide which pin is input and which pin is output

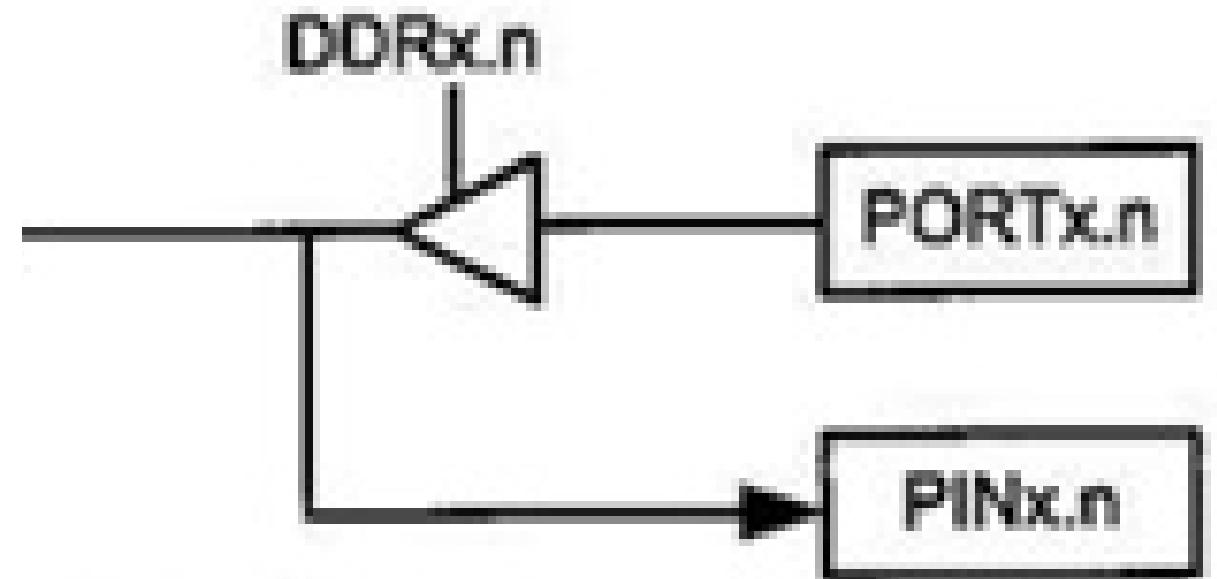
2-Configure the port direction use register DDRx

1 for Output.

0 for Input.

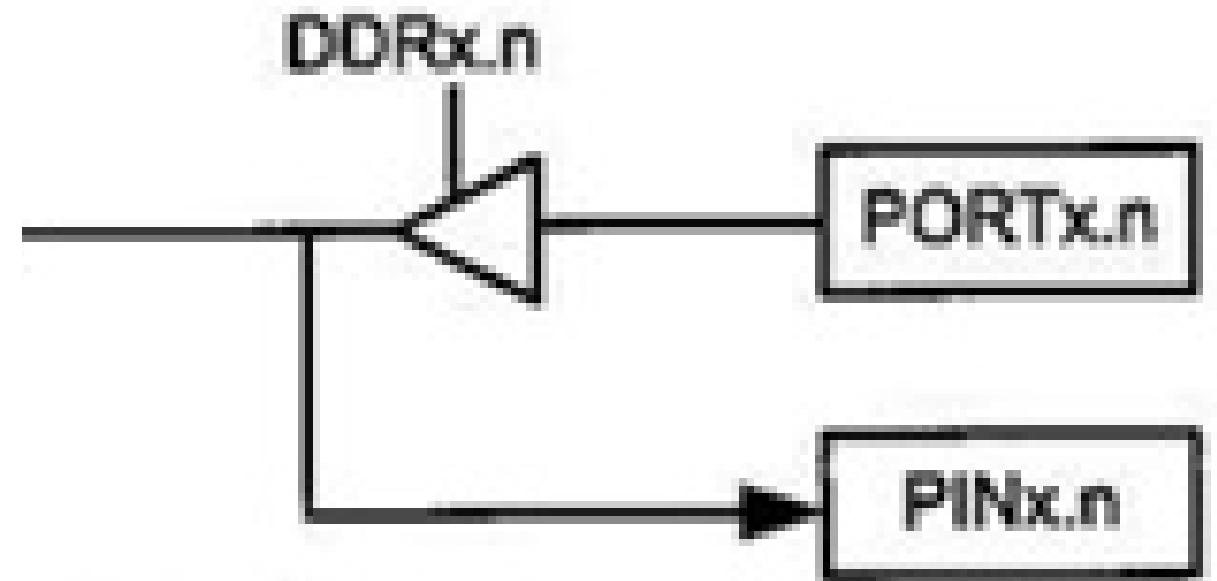
3- In Read (input case) Use register PINx

4-In write (Output case) Use register PORTx



I/O Pin Programming Steps In AVR MC

For each output pin we can perform read/write operation on this pin .We can write on it using PORTx register and read it through PINx or PORTx register .



For each Input pin we can perform read operation only through the PINx register



Pull Up VS Pull Down



Pull Up VS Pull Down

What is Pull-up resistor ?

Why we need it ?

How to use this feature ?

What is Pull-down resistor ?

Why we need it ?

How to use this feature ?



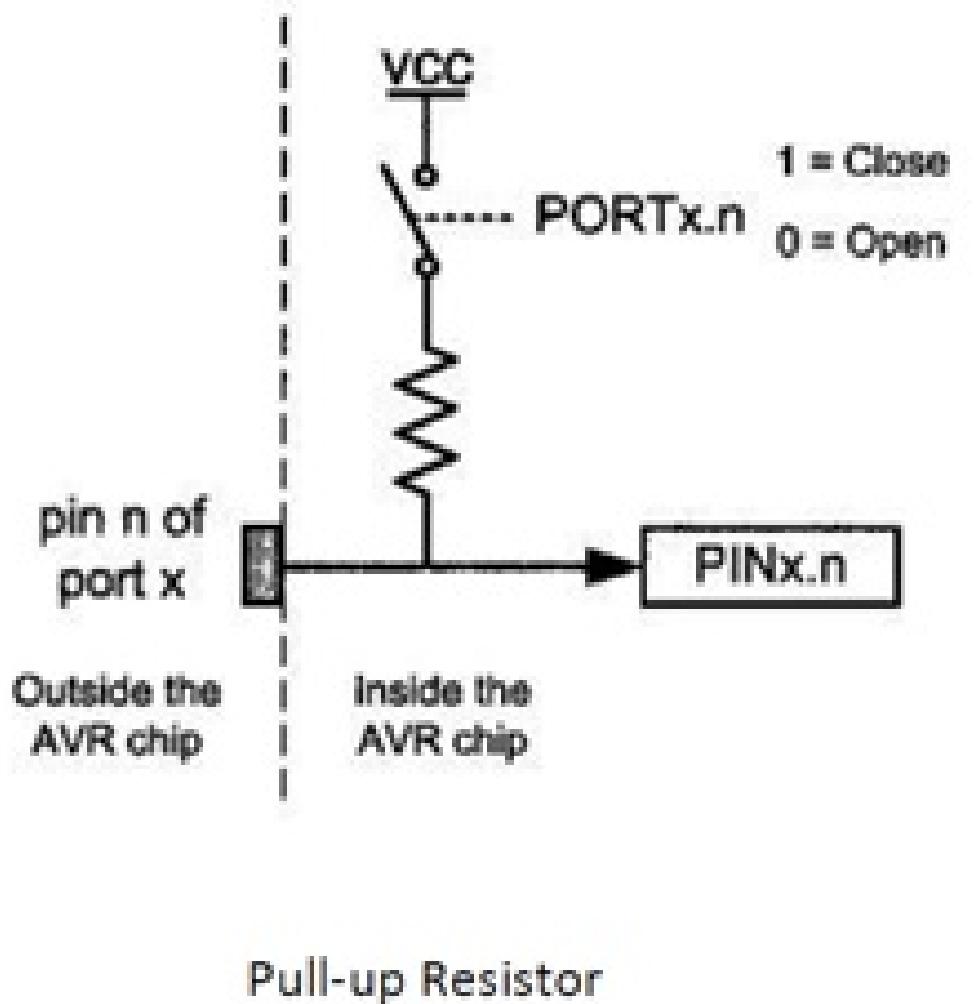
Internal Pull Up resistor

In case you set any PIN as input you can activate the internal pull up resistor by setting the corresponding bit in Portx register.

For each Input pin we can perform read operation only through the PINx register.

This feature is supported by all pins in the Atmega16/32 MC.

There is no internal Pull-down resistor in Atmega16/32 MC pins but it could be supported in other MC



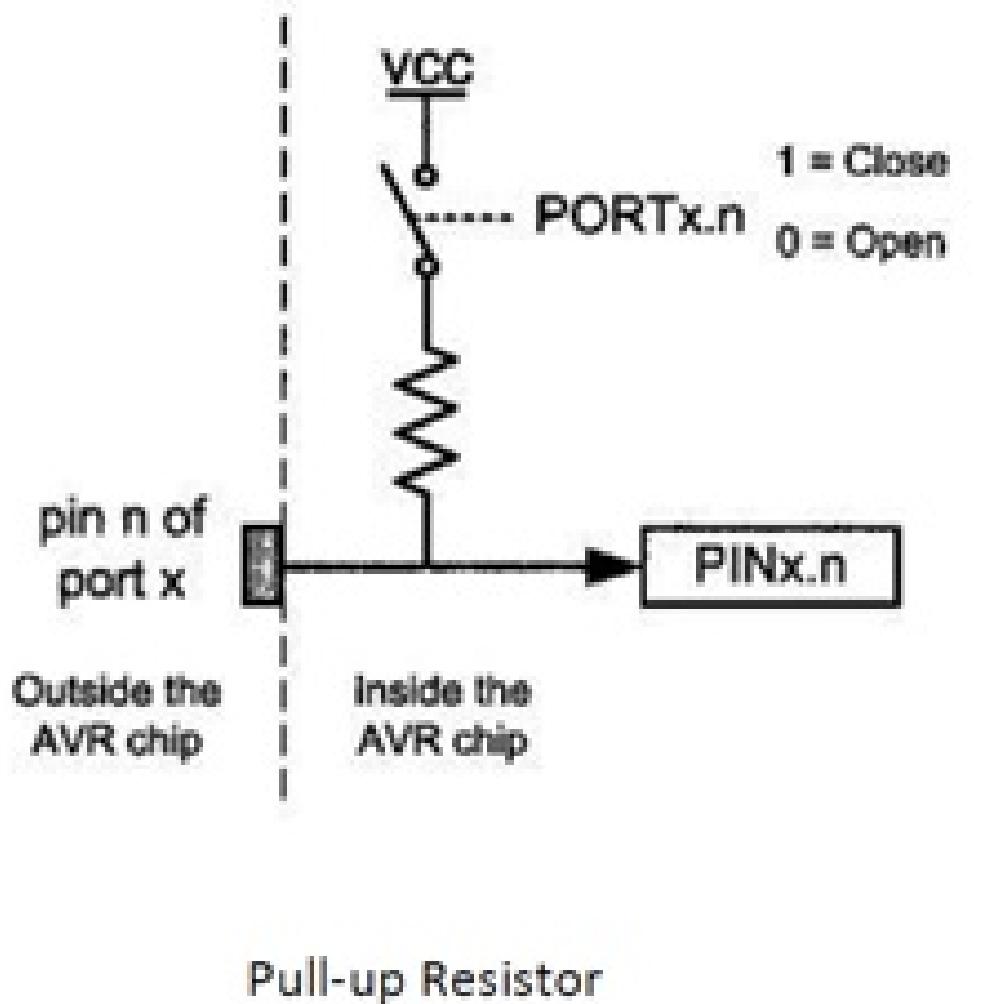
Internal Pull Up resistor

Example :

Setup pin 2 in PORTB as input and activate the internal pull up resistor

DDRB&=~(1<<2);

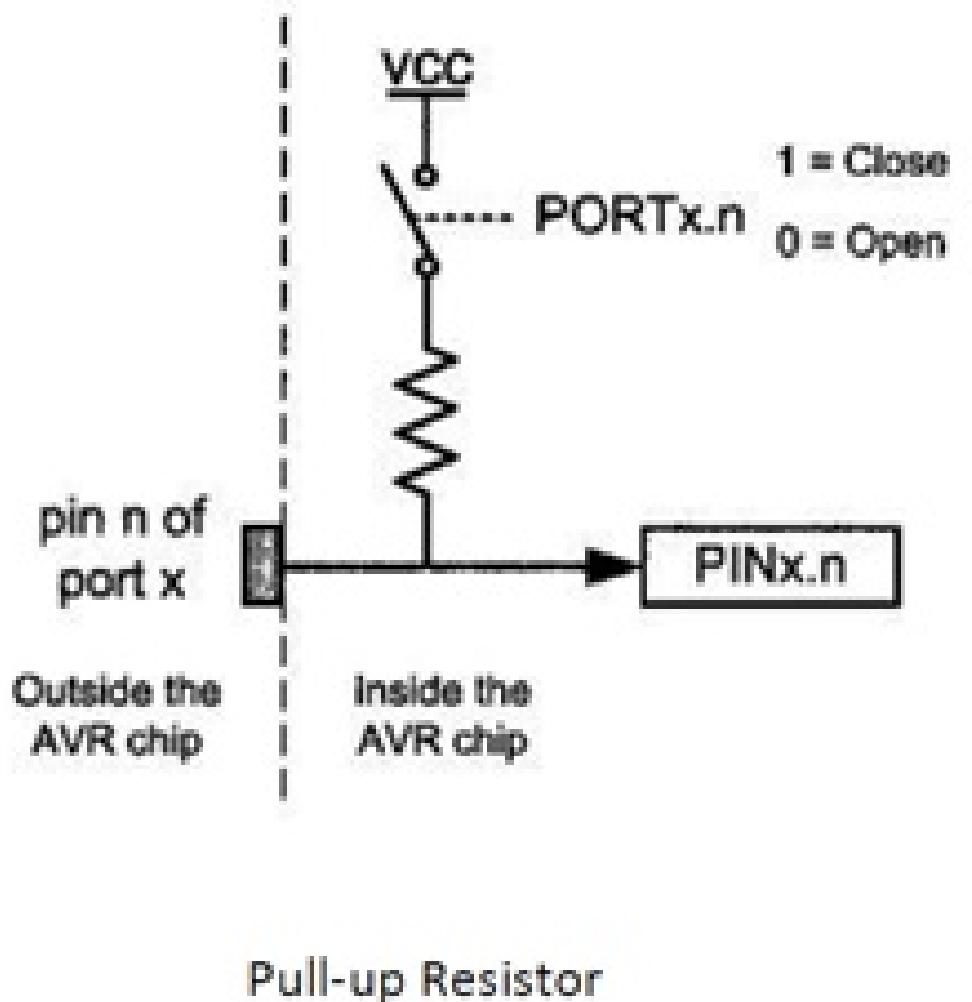
PORTB|=1<<2;



Internal Pull Up resistor

Lets Summarize our cases :

A PORT can have four different states depending upon the state of the bits corresponding to this pin in the data direction register (DDRx) and a data register (PORTx) of that specific port.



4 Cases

DDxn	PORTxn	I/O	PULL-UP	Comment
0	0	I	NO	TRI-STATE (HIGH-Z)
0	1	I	YES	Pxn will source current if pulled low externally
1	0	0	NO	output low (sink)
1	1	0	NO	output high (source)



Internal Pull Up resistor

This is internal PULL-UP resistor to all the pins can be disabled by writing one to the PUD BIT IN THE SFIOR register When this is done the pins are in tri state(high impedance states) if DDRxn =0 and PORTXn=1

Special FunctionIO
Register – **SFIOR**

Bit	7	6	5	4	3	2	1	0	SFIOR
Read/Write	R/W	R/W	R/W	R	R/W	R/W	R/W	R/W	
Initial Value	0	0	0	0	0	0	0	0	

Bit-2 (PUD) : Pull up disable
when this bit is written to one , the pull-ups in the I/O ports are disabled even if the DDxn and PORTXn Registers are configured to enable the PULL-ups





Basics



How to set values in registers ?

Set all pins of Port A AS OUTPUT

DDRA = 255;

DDRA= 0xFF;

DDRA=0b1111111;



How to set values in registers ?

set all Pins in PORTA

PORTA=255;

PORTA= 0xFF;

PORTA=0b11111111;



How to set values in registers ?

Clear all pins in PORTA

PORTA=0;

PORTA= 0x00;

PORTA=0b00000000;



How to deal with a specific pin and conserving other pins ?

Set Specified bit in register

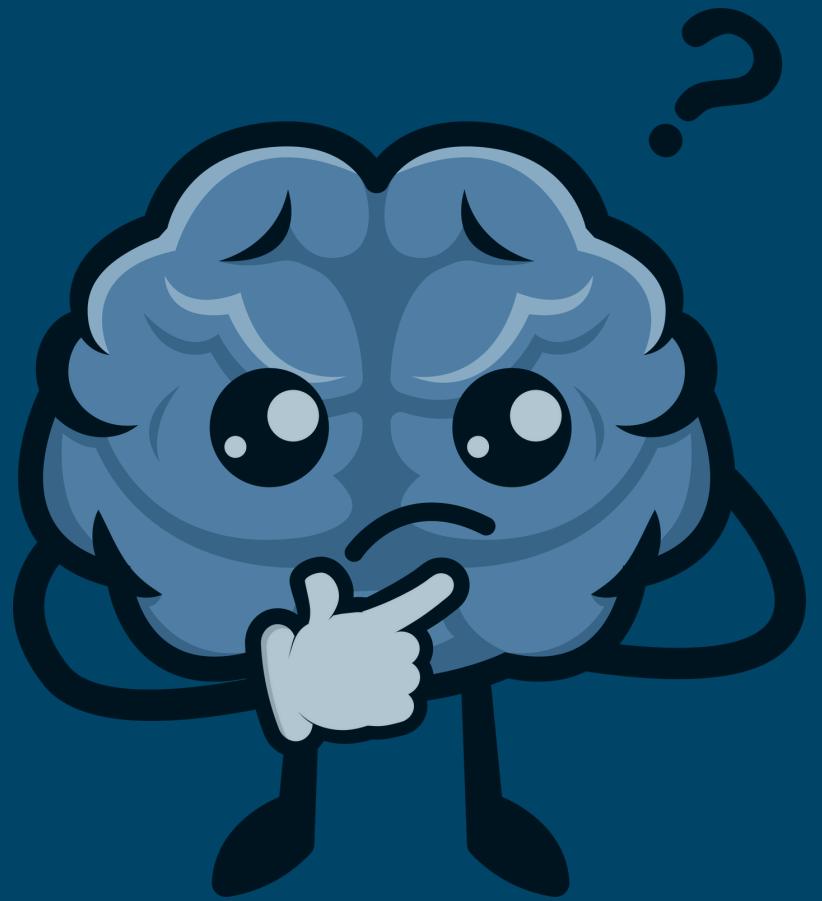
Make OR operation on the old register value with one shift left by the pin number For example if we want to set pin number 5 in PORTA

PORTA|= (1<<5);

Clear(reset) specified bit in register

Make AND operation on the old register value with the 1 's complement value of one shift left bt the pin number For example if we want to clear pin number 3 in PORTB

PORTB&=~ (1<<3);



How to deal with a specific pin and conserving other pins ?

Toggle or Flip specified bit in register

Make XOR operation on the register old value with one shift left by the pin number for example if we want to toggle pin number 2 in PORTC

PORTC^=(1<<2);



How to deal with a specific pin and conserving other pins ?

Check if a specified bit is set (one)

Make AND operation on the register value with one shift left by the pin number For example check if pin 2 in PORTD is set

```
if( PIND&(1<<2) )  
{  
/*CODE*/  
}
```



How to deal with a specific pin and conserving other pins ?

Check if a specified bit is cleared (reset or zero)

Just revert the previous condition For example check if pin 2 IN PORTD is cleared

```
if( !(PIND &(1<<2)) {  
/*CODE*/  
}
```



Know More about your MC (DATA SHEET)

Sink current vs Source current

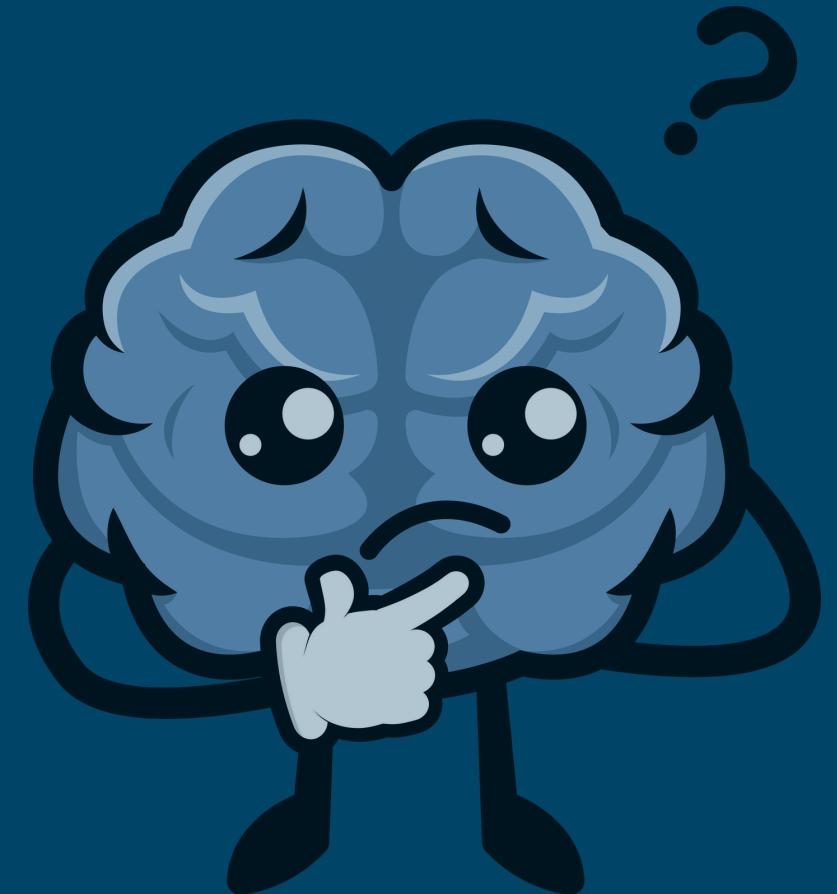
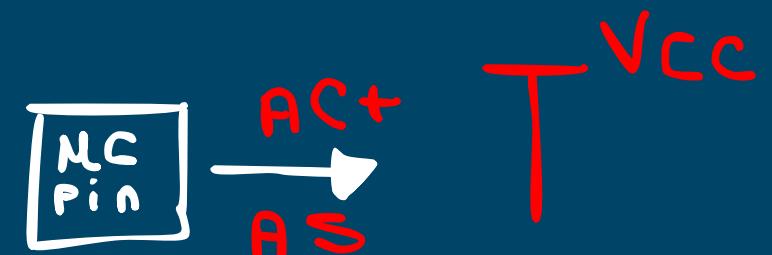
Sink current : PIN LOW

the sink value is the amount of current that can flow into the pin (and therefore into the MC) safely.

Source current : PIN HIGH

The SOURCE current is the amount of the current that can be pulled out of the pin safely.

It is very important that you try to limit the amount of current flowing into and out of your pin (typically using series resistance)





Applications



Output pins

LED AND 7-Segment

LCD display

Motors

Buzzer

Signal to other MC

Output to PC through PC Serial Port

Input pins

Switches (Push button ,keypad and etc..)

analog/digital sensors

Signal from another MC

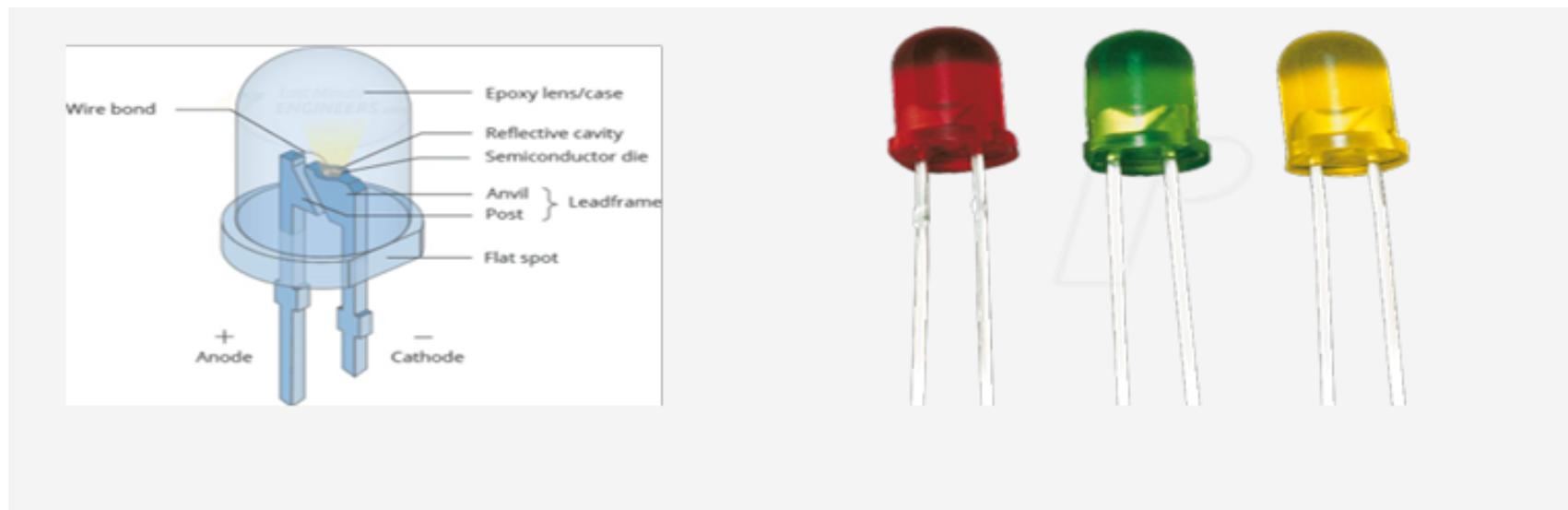
input from PC through PC Serial Port



Interfacing

LED :

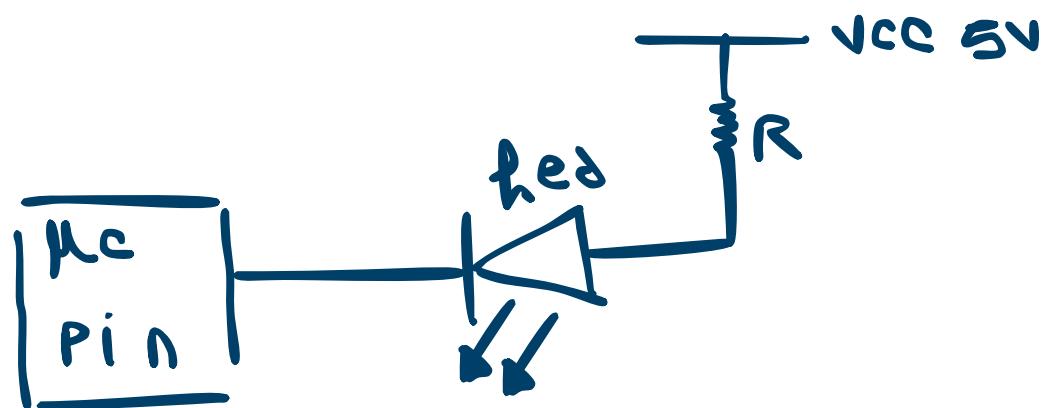
Light Emitting Diode is an electrical element that emits light by supplying a voltage difference between its terminals



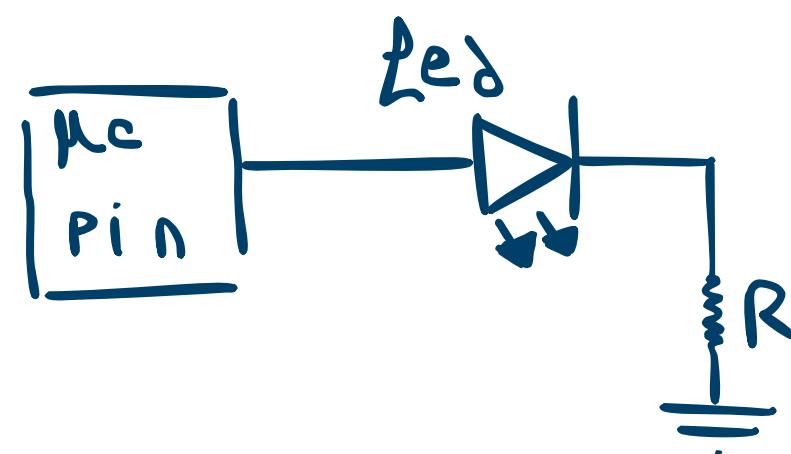
Interfacing

LED :

Negative Logic



Positive Logic



R= 220 : 350 Ω



Interfacing

Mechanical Switch :

Mechanical Switch is an electrical component that can connect or break an electrical circuit.

Open



Switch (open)

Switch state

Close



Switch (closed)

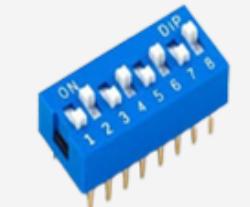
Tactile switch



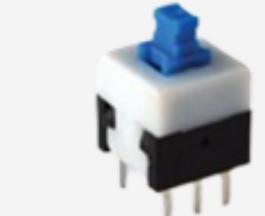
Paddle switch



DIP switch



Push Button



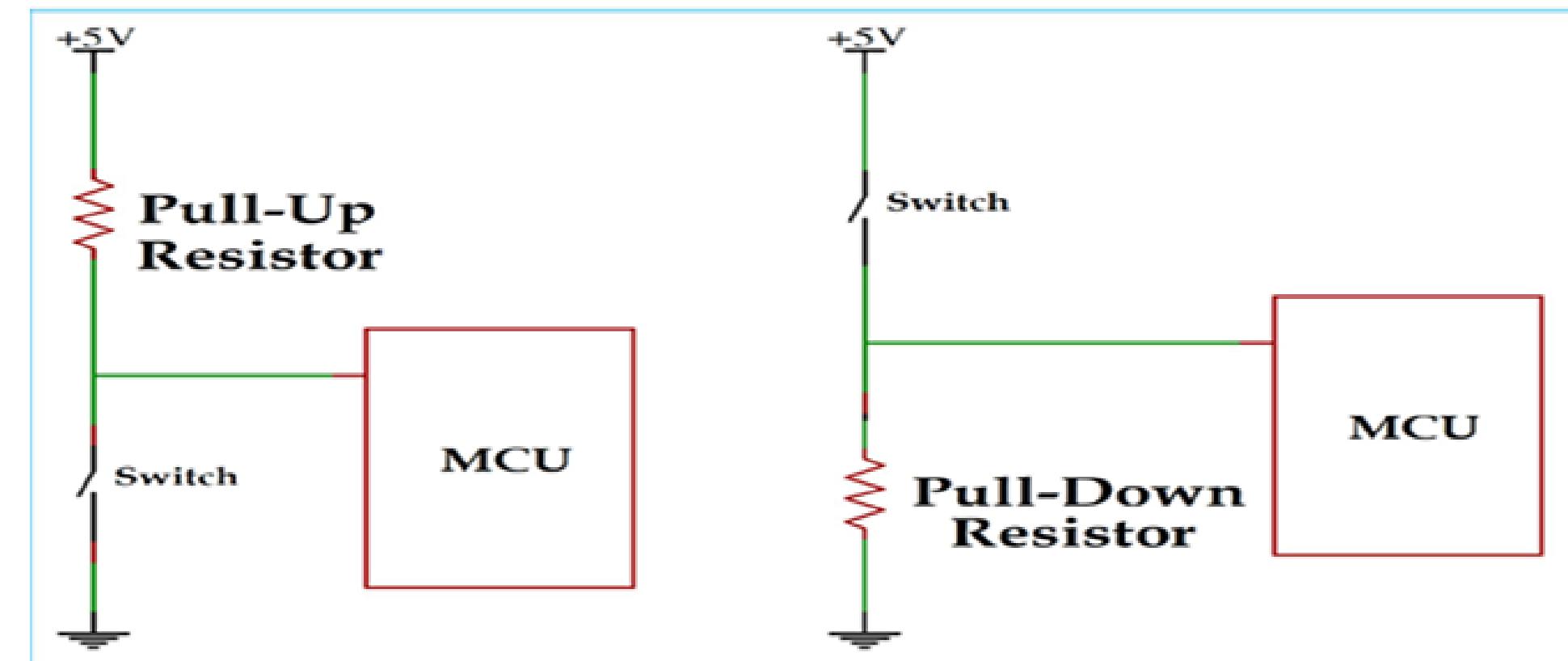
Rocker switch



Interfacing

Mechanical Switch :

Switch shall be connected by pull up or pull down resistor to avoid short circuits.



Interfacing

De-bounce Issue

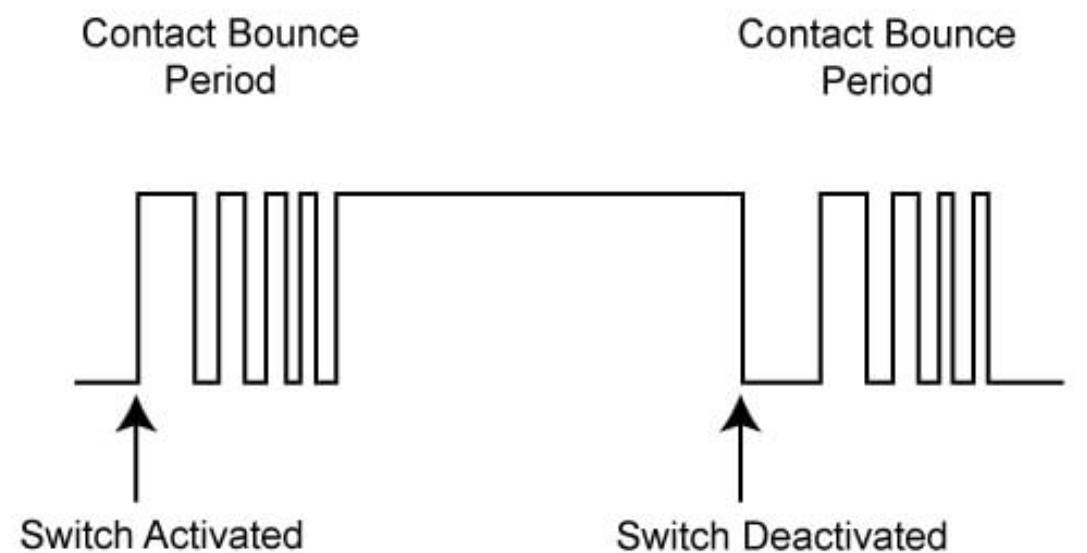
**When the Push Button is pressed the switch does not provide a clean edge
The input value is bouncing for 10-30 ms**

**If this signal was used as an input to a digital counter for
example you would get multiple counts rather than the expected
single count**

Sol:

1-Software

2-hardware



infinite Loop

Any C project in Embedded Systems application shall have an infinite loop called Super loop . This loop is a must even if you will leave it empty!!

Super loop used as here we have an embedded application used for a certain purpose

This loop prevents the program counter (PC) from continues incrementing over the flash memory and execute a garbage code.

```
while(1) {  
/*code*/  
}
```

```
for(;;){  
/*code*/  
}
```

```
loop:  
/*code*/  
gotoloop;
```

Most Common

Better performance

Not Good !



Busy loop Delay

Software Technique to make the processor halt it's activity for a certain time using loop.

Delay Library “util/delay.h” In AVR provides Two basic functions:

_delay_ms (value in ms);

_delay_us (value in us);

Before Using the delay library we have to define our system frequency through the F_CPU macro used by the delay library:

#define F_CPU 8000000UL 8MHz

If not defined it will use its default frequency value which is 1Mhz





TASK

