Address	Label	Mnemonics H	lexcode	Bytes	M-Cycles	<b>T-States</b>	
4500		MVI C,09	0E	2	2	7	
4501		•	09				
4502		LXI H,4501		21	3	3	10
<b>4503</b>		•	01				
4504			45				
4505		<b>MVI A,00</b>	<b>3E</b>	2	2	7	
4506		,	00				
4507		MOV M,A	77	1	2	7	
<b>4508</b>		INR A	<b>3C</b>	1	1	4	
4509		INX H	23	1	1	6	
450A	LOOP	MOV M,A	77	1	2	7	
450B		DCX H	2B	1	1	6	
450C		ADD M	86	1	2	7	
450D		INX H	23	1	1	6	
450E		INX H	23	1	1	6	
450F		DCR C	0D	1	1	4	
4510		JNZ LOOP	C2	3	3	10	
4511			<b>0A</b>				
4512			45				
4513		HLT	76	1	2	5	