Address	Label	Mnemonics H	lexcode	Bytes	M-Cycles	<b>T-States</b>	
8500		LXI H,F000		21	3	3	10
8501			00				
8502			F0				
8503		MOV C,M	4E	1	2	7	
8504		DCR C	0D	1	1	4	
8505	LOOP3	<b>MOV D,C</b>	<b>51</b>	1	1	4	
8506		<b>LXI H,F001</b>		21	3	3	10
8507			01				
8508			F0				
8509	LOOP2	MOV A,M	<b>7E</b>	1	2	7	
850A		INX H	23	1	1	6	
850B		CMP M	BE	1	2	7	
850C		JC LOOP1	DA	3	3	10	
850D			14				
850E			<b>85</b>				
850F		<b>MOV E,M</b>	5E	1	2	7	
<b>8510</b>		MOV M,A	<b>77</b>	1	2	7	
8511		DCX H	<b>2B</b>	1	1	6	
8512		MOV M,E	<b>73</b>	1	2	7	
8513		INX H	23	1	1	6	
8514	LOOP1	DCR D	15	1	1	4	
8515		JNZ LOOP2		<b>C2</b>	3	3	10
8516			09				
8517			85				
8518		DCR C	<b>OD</b>	1	1	4	
8519		JNZ LOOP3		C2	3	3	10
851A			05				
851B			<b>85</b>				
851C		HLT	<b>76</b>	1	2	5	