

PSoC 4 BLE/PRoC BLE: SPI-BLE Peripheral Role

Objective

This example demonstrates how to use PSoC 4 BLE/PRoC BLE device as SPI-BLE Peripheral device.

Overview:

This code example uses a custom BLE profile with two custom service to demonstrate the SPI-BLE peripheral functionality. One custom service is used to notify the data written by the SPI master to the client and the second one is used to update the SPI TX FIFO when the client writes to the GATT DB of the peripheral device

Requirements:

Programming Language : C (GCC 4.8.4)

Associated Parts : PSoC 4 BLE, PRoC BLE

Required software : PSoC Creator 3.1 SP2, PSoC Programmer 3.22.3,CySmart PC application

Required hardware : CY8CKIT-042-BLE Bluetooth® Low Energy (BLE) Pioneer Kit , SPI master device

Optional hardware : Bluetooth sniffer

Project Description:

This project demonstrates the following.

- SPI-BLE bridge implementation Peripheral role
- Connection with any central device
- Two custom services in a single profile
- Low power implementation for coin-cell operation

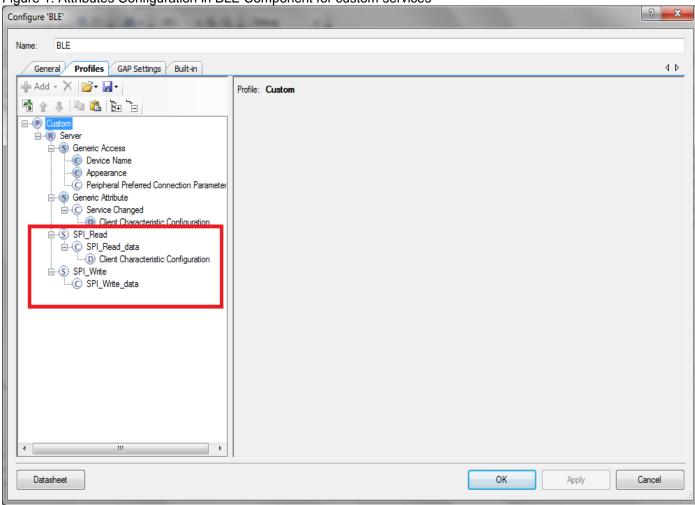
The BLE profile in this project consists of two custom services: SPI_Read and SPI_Write. The SPI_Read service consists of one custom characteristics which is used to send the SPI data written by the SPI master to the client as notifications. This simulates the SPI write in case of wired SPI.

The second custom service SPI_Write consists of one custom characteristics through which the GATT client can write data to the peripheral device. The peripheral device in turn update the data written by the GATT client to the SPI TX FIFO registers for the SPI master to read. This simulates the SPI slave updating its TX FIFO registers for the master to read.

These properties for the custom service/characteristics are configured in the BLE component under the Profiles tab, as shown in Figure 1 below



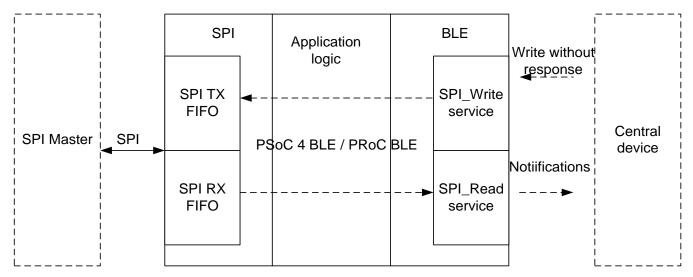
Figure 1. Attributes Configuration in BLE Component for custom services



A simple block diagram of the implementation is shown in the figure 2.



Figure 2. Block diagram of the implementation



The project consist of the following files:

Main.c/.h

These files contain the main function, which is the entry point and execution of the firmware application. It also contains function definition for initialization of the system.

App BLE.c/.h

These files contain all the macros and function definitions related to BLE communication and operation. It contains the event callback function definition that is registered with the BLE component startup and used by the component to send BLE-related events from the BLE stack to the application layer for processing. It contains a method to send notifications to the GATT client device and process the Write commands on the SPI_Read characteristic by the GATT client device.

Low_power.c/.h

These files contain the function to handle low-power mode. This function is continuously called in the main loop and is responsible for pushing the BLE hardware block (BLESS) as well as the CPU to Deep Sleep mode as much as possible. The wakeup source is either the BLE hardware block Link Layer internal timer or the interrupt from the I2C address match. This allows for very low power mode implementation and operation using a coin cell. App SPI.c/.h

These files contain the function to handle the SPI read and write activity.

Config.h

This file has macros to enable or disable - Low power mode implementation and LED indication

Additionally there are LED indications to show the state of the device.

BLUE LED - Device is in advertisement state

RED LED - Device is in disconnected state

GREEN LED - Device is in connected state

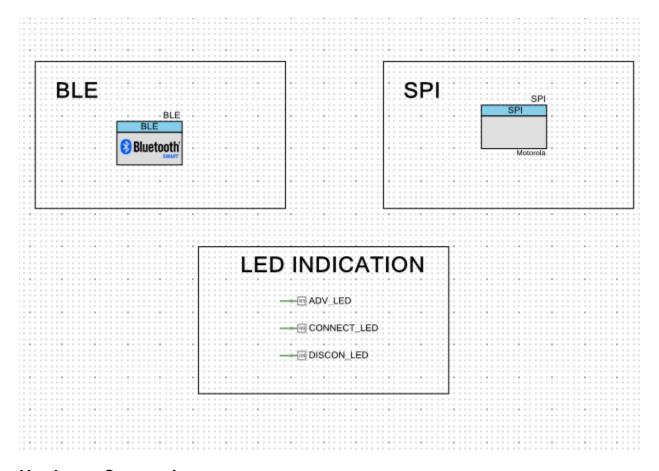
To measure the best power consumption number, disable the LED indication.

The SWD pin are configured as GPIO to get the lowest possible current consumption number.

The top design of the project is shown in the figure 3 below.



Figure 3 Top Design for SPI_BLE Bridge Project



Hardware Connections

Connect the SPI lines of the SPI master to that of the PSoC 4 BLE / PRoC BLE.

The pin assignment for this project is in **SPI_BLE_Bridge.cydwr** in the Workspace Explorer, as shown in Figure 4

Figure 4 Pin Selection for SPI_BLE Project



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Alias	Name /	Port		Pin I		Lock	
	\SPI:miso_s\	PO[1] LPCOMP:in_n[0], TCPWM0:line_out_compl, SCB1:uart_tx, SCB1:i2c_sc1, SCB1:spi miso	•	20	•	▽	
	\SPI:mosi_s\	PO[0] LPCOMP:in_p[0], TCPWM0:line_out, SCB1:uart_rx, SCB1:i2c_sda, SCB1:spi mosi	•	19	•	V	
	\SPI:sclk_s\	PO[3] TCPWM1:line_out_compl, SCB1:uart_cts, LPCOMP:comp[1], SCB1:spi clk	•	22	•	V	
	\SPI:ss_s\	PO[2] TCPWM1:line_out, SCB1:uart_rts, LPCOMP:comp[0], SCB1:spi select[0]	•	21	•	V	
	ADV_LED	P3[7] SARMUX:pads[7], TCPWM3:line_out_compl, SCB1:uart cts, SRSS:ext clk lf	•	54	•	V	
	CONNECT_LED	P3[6] SARMUX:pads[6], TCPWM3:line_out, SCB1:uart rts	•	53	•	V	
	DISCON_LED	P2[6] OAO:vplus_alt	٠	43	•	V	

Verify Output

The project can be verified by using the CySmart Central Emulation Tool and BLE Dongle.

CySmart Central Emulation Tool

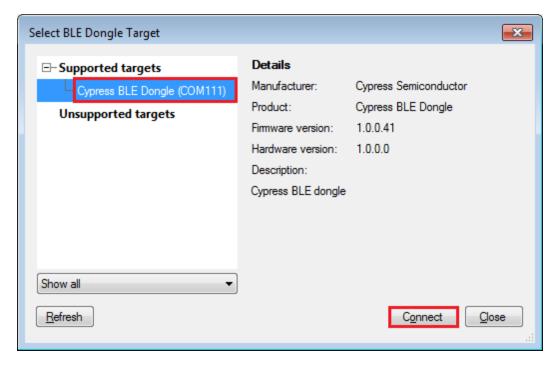
To verify the SPI_BLE_Bridge project using the CySmart Central Emulation Tool, follow these steps: **Note:** Refer CySmart BLE Host Emulation tool to learn how to use the tool.

- 1. Connect the BLE Dongle to one of the USB ports on the PC and wait for the dongle to enumerate.
- 2. Start the CySmart Central Emulation Tool on the PC by going to **Start > All Programs > Cypress> CySmart <version> > CySmart <version>.** You will see a list of BLE Dongles connected to it.

If no BLE Dongle is found, click **Refresh**. Select the BLE Dongle and click **Connect**.

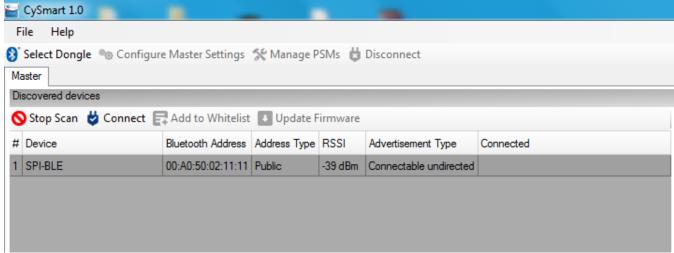
Figure 5 Connect to BLE Dongle





- 3. Connect the PSoC 4 BLE/PRoC BLE module on the J10 and J11 headers on the BLE Pioneer Kit.
- 4. Connect the SPI lines of the SPI master to that of PRoC-BLE or PSoC 4 BLE
- 5. Power the BLE Pioneer Kit through the USB connector J13.
- 6. Program the BLE Pioneer Kit with the SPI_BLE_Bridge project.
- 7. After successful programming the BLUE LED will glow indicating that the device is in advertisement mode.
- 8. On the CySmart Central Emulation Tool, click Start Scan to see the list of available BLE peripheral devices.
- 9. Double-click the **SPI_BLE** device to connect, or click **SPI_BLE** and then Click **Connect**. The GREEN LED on the device will start glowing indicating it is connected to the Client.

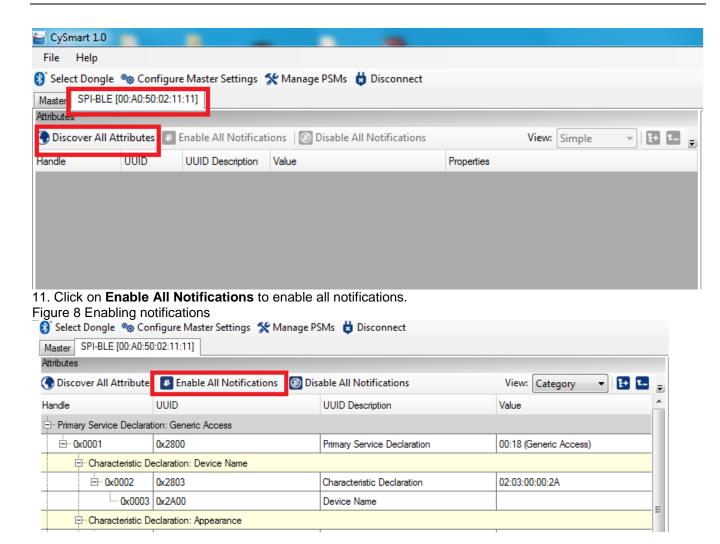
Figure 6 Connect to SPI_BLE



10. Click **Discover All Attributes** to find all attributes supported.

Figure 7 Discover all attributes of SPI BLE

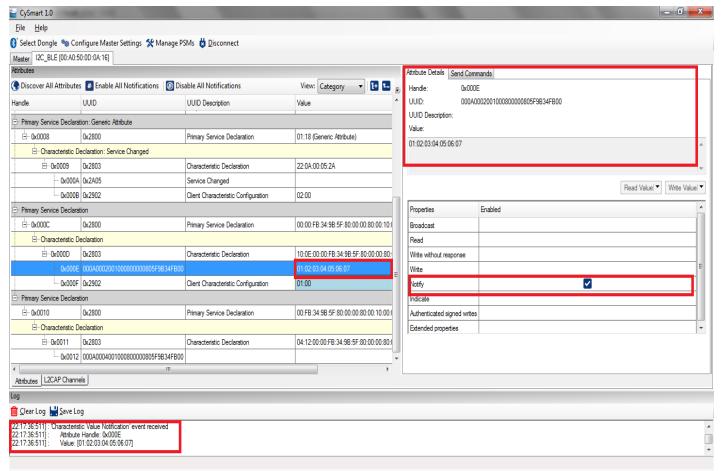




12. Write data from the SPI master to PSoC 4 BLE or PRoC BLE. The data written by the SPI master is sent as notifications to the client device

Figure 13 Check notifications received

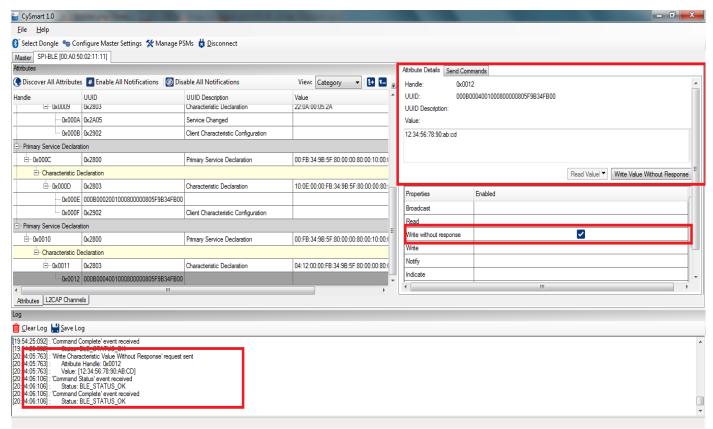




13. On CySmart select the handle 0x0012 and perform write without response to write to peripheral GATT DB

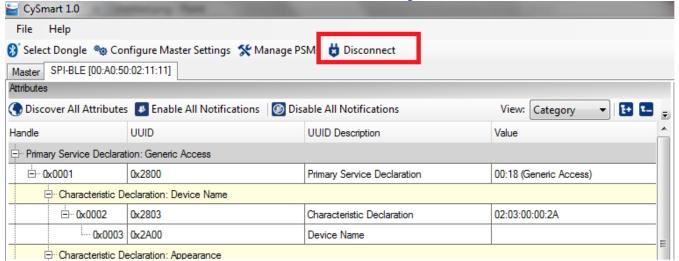
Figure 14 Write data to GATT DB of peripheral





14. Read data from the PRoC-BLE/PSoC 4 BLE device. The data read by the SPI master will be the same as the data written by the client.





16. After disconnection the RED LED will glow for 2 seconds and the device will start advertising as indicated by BLUE LED turning on. You can connect it back following the steps mentioned above.