

CY8CPROTO-063-BLE PSoC 6 BLE Prototyping Kit

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Drawing Numbers

PCBA	121-60527-01
PCB	600-60527-01
FAB DRW	610-60527-01
ASSY DRW	620-60527-01
SCH DRW	630-60527-01



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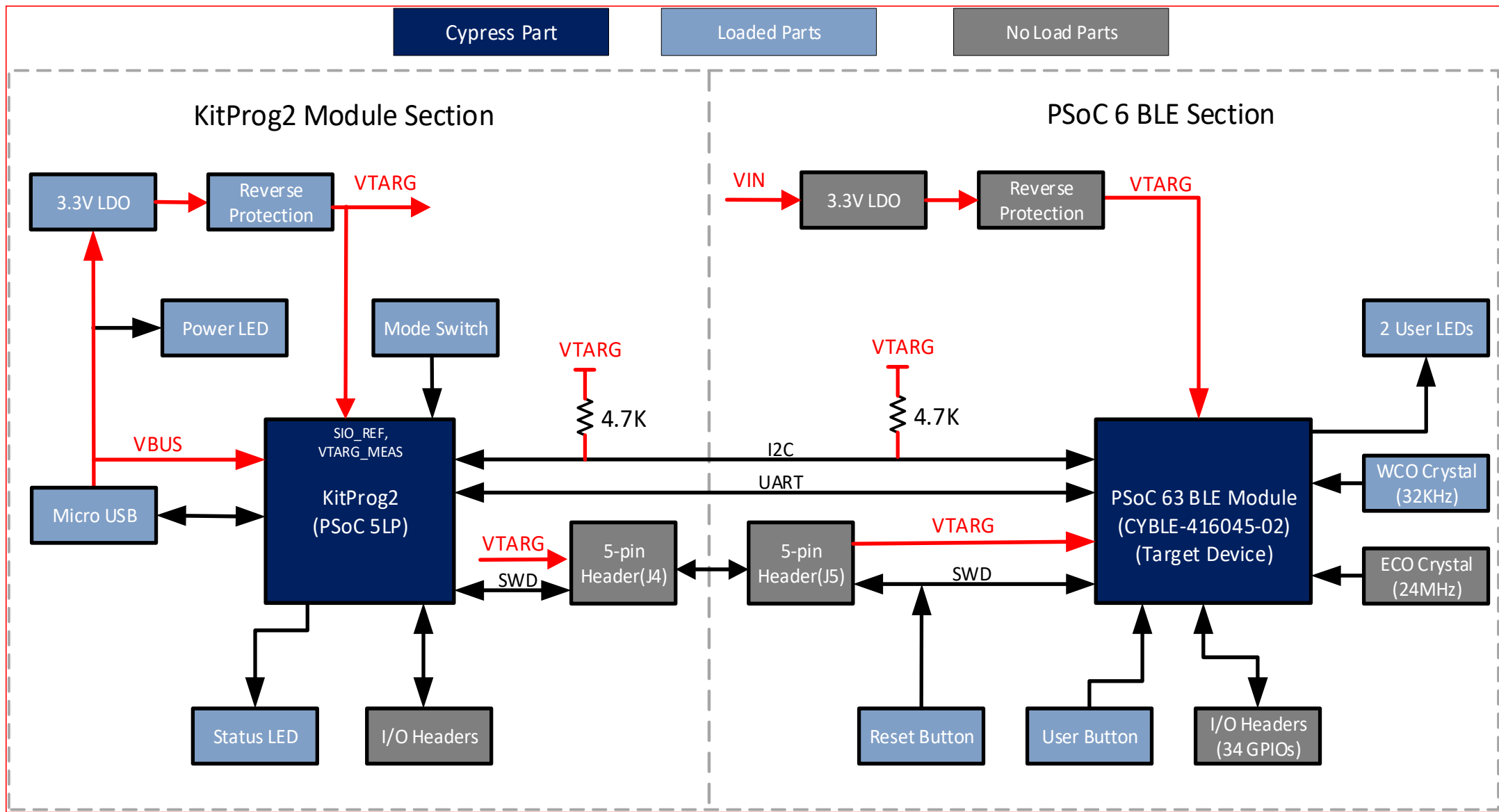
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Page Title : Title Page

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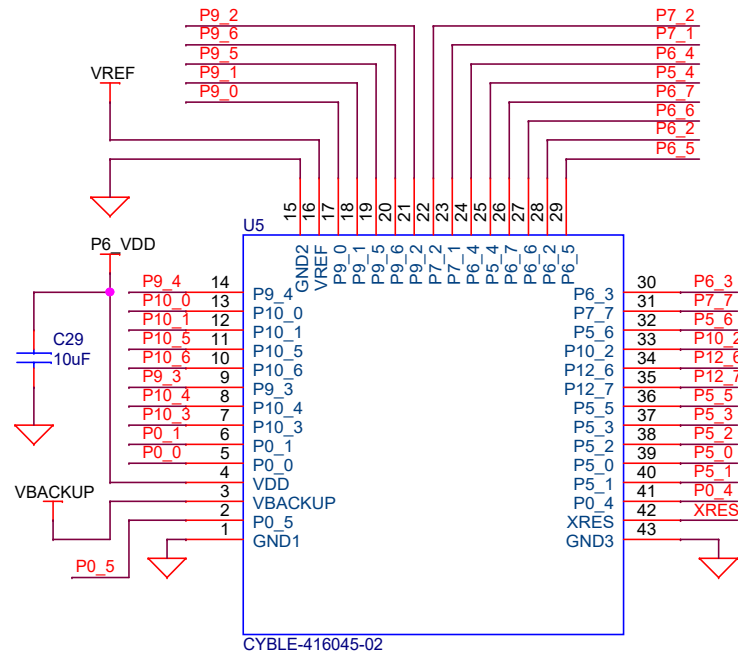
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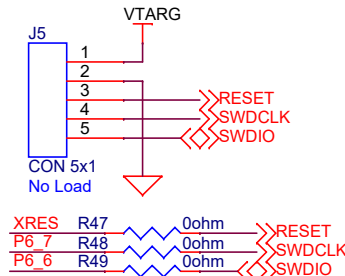
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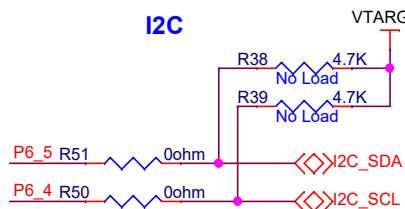
PSoC 63 BLE Module



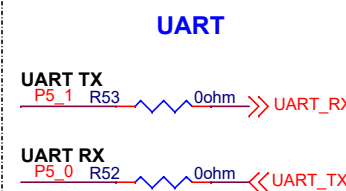
Programming Header



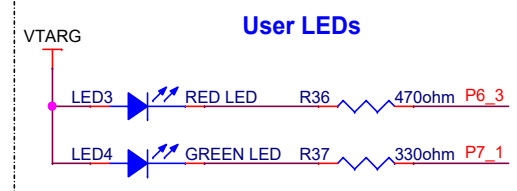
I2C



UART

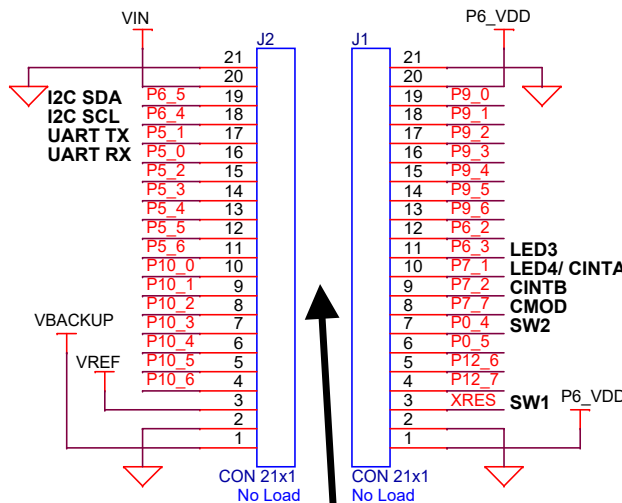


User LEDs

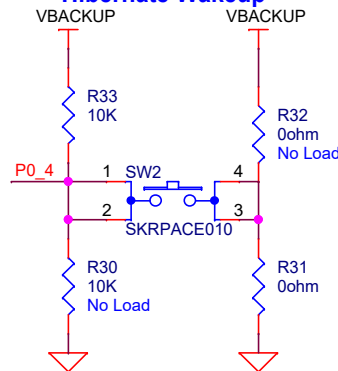


PSoC 63 BLE I/O Headers

Note: Voltage above 3.6V on VTARG/P6_VDD will permanently damage PSoC 6

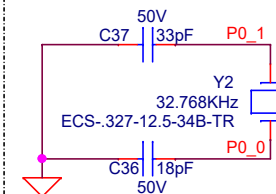


User Button / Hibernate Wakeup



WCO

KHz Crystal



You dont need all these GPIOs broken out.
Just for what makes sense plus some extra.



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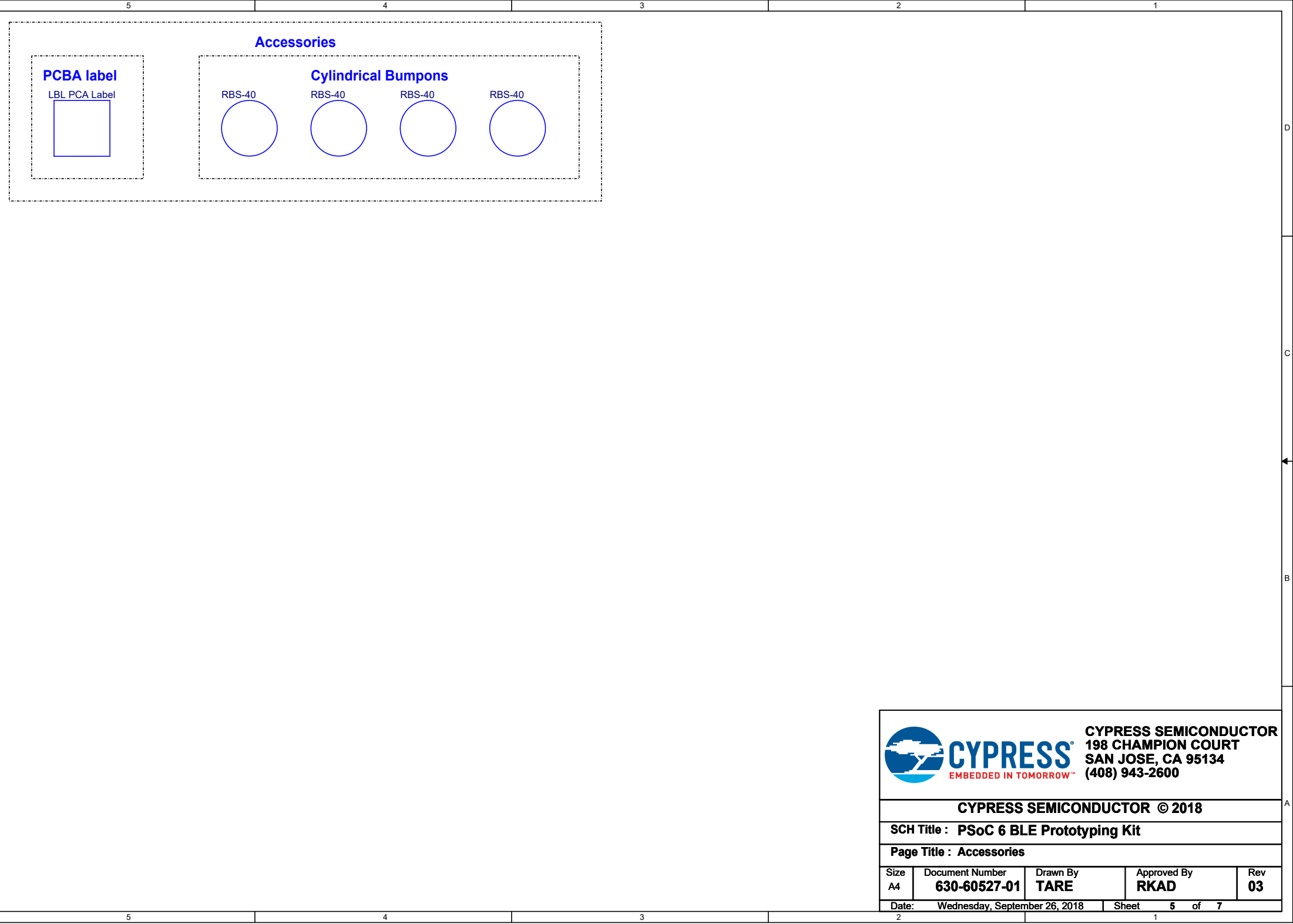
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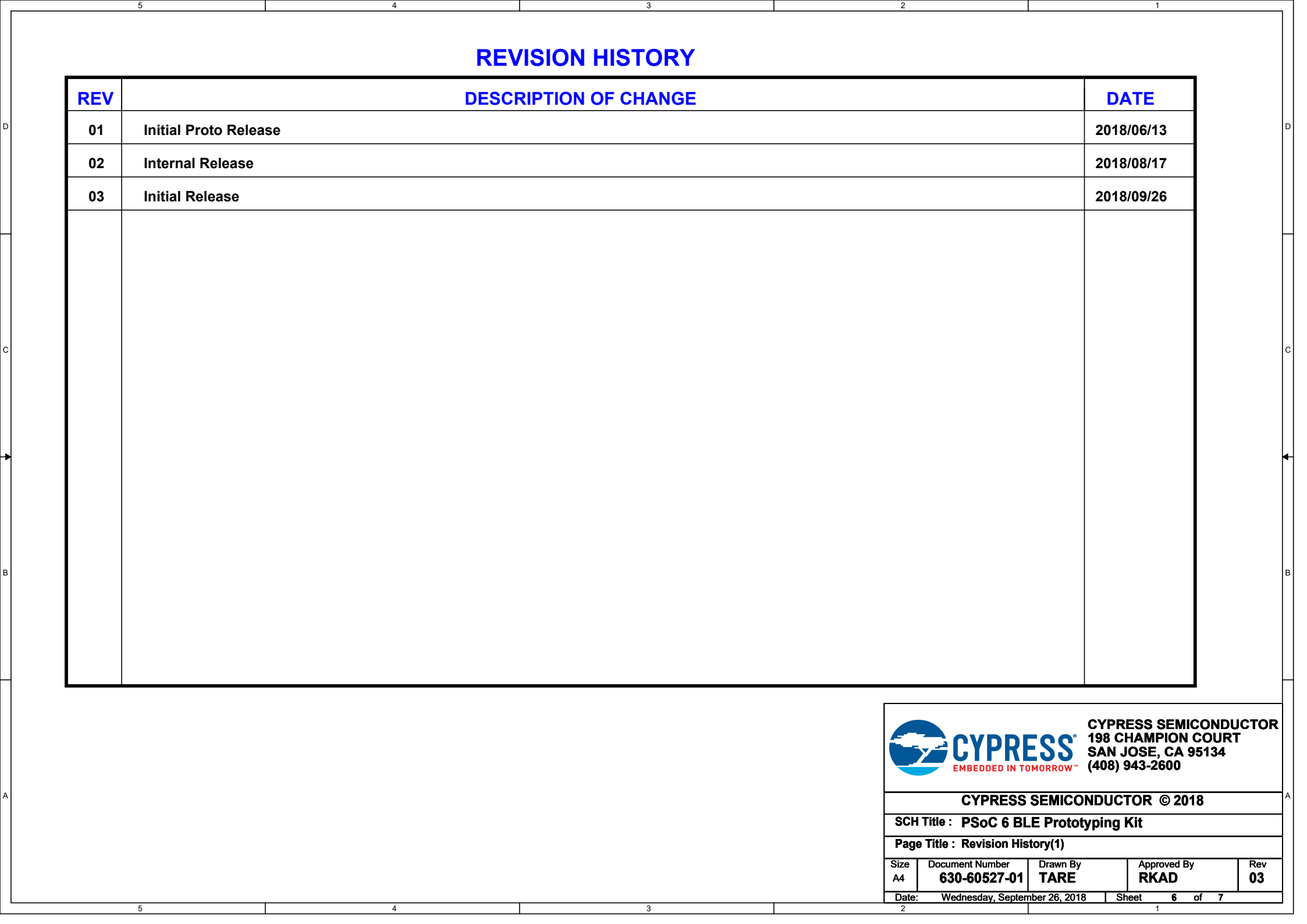




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