Fall Semester, 2025

CSE211: Introduction to Embedded Systems

Assignment

You are required to prepare a **maximum 1-minute video** demonstrating how the **Raw Interrupt Status (RIS)** behaves during an **up-count timer operation.** The video must clearly show **when the RIS flag is set**, and how it is detected through **concatenation of the individual registers** involved.

What your video must include

1. A running up-count timer configuration (e.g., GPTM timer).
2. A demonstration of how RIS changes during the counting process.
3. A clear visualization or explanation of how RIS is captured/read by concatenating the related registers.
4. Any waveform, debug view, code window, or graphical aid that supports your explanation.

Submission Options (choose one)

You may submit the assignment in any ONE of the following formats:
1. Video file only (upload directly).
2. A document containing a Google Drive link to your video.

Deadline

16th of December 2025