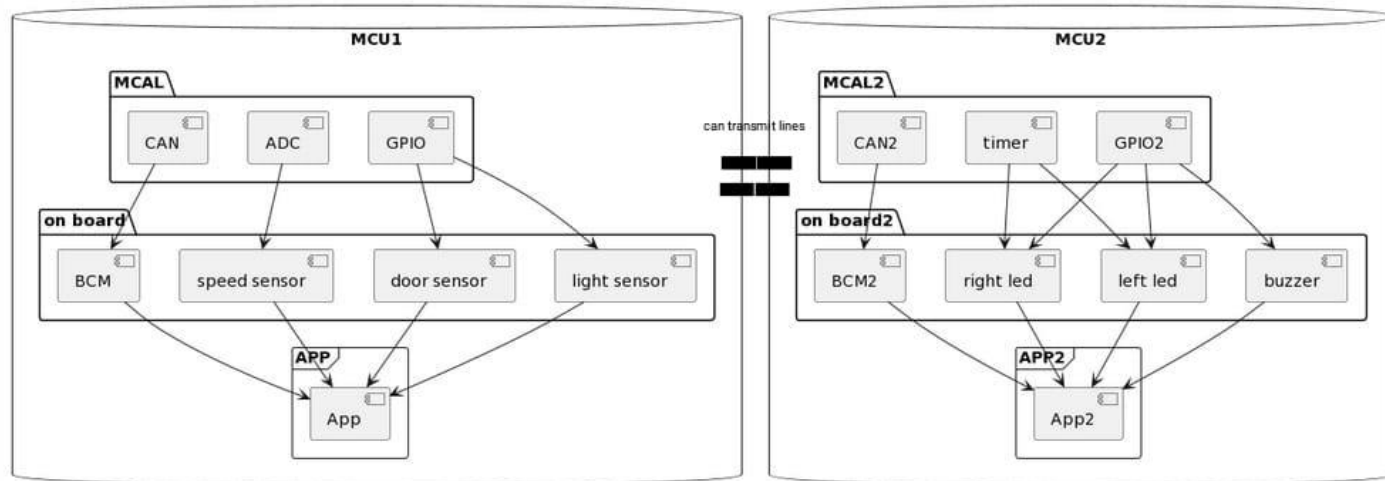


Block diagram



Code

@startuml

database "MCU1"{

package "MCAL" {

[GPIO]

[ADC]

[CAN]}

package "on board" {

[light sensor]

[speed sensor]

[door sensor]

[BCM]

}

frame "APP" {

[App]

}}

[CAN]--> [BCM]

[ADC]--> [speed sensor]

[GPIO] --> [light sensor]

[GPIO] --> [door sensor]

[light sensor]-->[App]

[speed sensor]-->[App]

[door sensor]-->[App]

[BCM]-->[App]

```
database "MCU2"{
package "MCAL2" {

    [GPIO2]

    [timer]

    [CAN2]}
package "on board2" {

    [buzzer]

    [left led]

    [right led]

    [BCM2]

}

frame "APP2" {

[App2]

}

}

[CAN2]--> [BCM2]

[GPIO2] -->[buzzer]

[GPIO2] --> [left led]

[GPIO2] -->[right led]

[timer]-->[left led]

[timer]--> [right led]

[buzzer]-->[App2]

[left led]-->[App2]

[right led]-->[App2]

[BCM2]-->[App2]

@enduml
```