<http://www.iue.tuwien.ac.at/phd/entner/node34.html>

# SRAM Cell

|  |
| --- |
| \includegraphics[width=16cm]{figures/sram-circuit} |
| **Figure 7.18:** Circuit of a 6 transistor SRAM cell. It consists of two CMOS inverters and two access MOSFETs. NBT stress mainly affects the p-channel transistors. |

