

Sahil Yerawar

Curriculum Vitae

Education

- 2015–2019 **Bachelor of Technology, Computer Science and Engineering**, *Indian Institute of Technology, Hyderabad*, GPA – 8.52.
- 2014–2015 **Senior Secondary**, *DAV Public School (CBSE)*, Pune, Percentage – 95.5%.
- 2012–2013 **Secondary**, *DAV Public School (CBSE)*, Pune, Percentage – 96.5%.

Experience

Industrial

- July 2019 – **Embedded Engineer II**, *Honeywell Technology Solutions Private Ltd.*, Hyderabad.
- July 2020 Member of Displays and Graphics Team in Aerospace Domain. Responsible for developing display applications for Aircraft Cockpit Systems.
- May 2018 – **Contributor**, *Google Summer of Code*, Polly Labs.
- July 2018 A joint project with Polly Labs and Chapel Language Community to implement Polly Loop Optimizer within Chapel Compiler for extended GPU support. It was well-appreciated by the LLVM Community.

Research

- May 2017 – **Research Internship**, *Prof Ramakrishna Upadrasta*, IIT-Hyderabad.
- July 2017 Contributed to LLVM Compiler Infrastructure by developing Flag Mining Tool using iterative compilation technique.

Projects

Dassault Aircraft Cockpit Applications, *Honeywell*.

Developed multiple cockpit display applications for Dassault Aircrafts using C++.

- Developed Android applications for a cloud based solution for Runway Traffic Navigation.
- Responsible for writing clear and concise requirement documents for some of the core products in the Display applications.
- Responsible for modularization of Displays software to reduce testing time by more than 50%.

Automatic information extraction from NOTAM text, *Honeywell*.

Built a NLP-Based text extraction tool to parse important data from NOTAM texts. Used Tensorflow and Python to build this tool.

Digital Flight Manual, *Honeywell*.

Built a QA based tool to relate every question regarding particular flight manual towards an appropriate paragraphs which contains it's answer. Used the concepts of Word2Vec, Python and the libraries like Tensorflow and Facebook's FastText for this purpose.

Green Belt DFSS Project, Honeywell.

Based on the project of developing a common config file editor for multiple baselines, used various Six Sigma and DFSS tools to improve the project process. These tools had a profound impact in discovering project defects in the earlier stages of project pipeline. Earned Six Sigma Green Belt Certification through this project.

Predicting Product Review Helpfulness, IIT Hyderabad.

Used language models and deep learning techniques to predict the helpfulness of the Amazon Instant Video Reviews. The language models were efficient in extrapolating the review content.

Finding High-Quality Content in Social Media, IIT Hyderabad.

Developed a prototype machine learning model identifying high-quality content in QA websites based on user contributions and intrinsic content quality.

GCN for Temporal Graphs, IIT Hyderabad.

Developed a prototype CNN model which works on extending the Graph Convolutional Networks to dynamic graphs.

Publications

Yerawar, S., Bhat, S., Ferguson, M., Pfaffe, P., & Upadrasta, R. (n.d.). Leveraging polyhedral compilation in chapel compiler. *Poster Presented at: 2019 European LLVM Developers Meeting, Brussels, Belgium.* (link, pdf)

Courses Taken

- Data Structures and Algorithms
- Operating Systems
- Compilers
- Parallel and concurrent Programming
- Applied Machine Learning
- Deep Learning
- Data Mining
- Distributed Computing
- DBMS
- Information Retrieval

Technical Skills

Languages C, C++, Python
Web/App HTML, CSS, Node.js, Android App Programming, Vue.js
Technologies
Tools and Frameworks MySQL, MongoDB, Git, Tensorflow, Pytorch, Dimensions, DOORS

Languages

English	Professional Level	<i>Con conversationally fluent</i>
Marathi	Mother Tongue	
Hindi	Conversational	<i>Con conversationally fluent</i>