(uxa) Franc [1] Franklis AU(1) Frame [4] Flame[5] DUPACKE IRIT DUPACECEL 2127 Frame [2] threaut ACKESI 3RT 7 [vane [6] ACK[6] 4RTT

1.9(1) Frame[1] [fame[2] Frame C3]X ACKEI Frame[4] [Tane[5] DUPLX[2] IRII DUPACROZ ZUPNOW Frame[2] ACK[5] 2RTT Frame [6] ACK [6] BRT 4277

This reduce the transaction time about IRIT

1. (2) N = SWS + RWS - (5) = 8 When sender window sent 01234 to 18 receiver window, receiver window will accept all of them but all the ACKS are lost since SWS > RWS. RWS expected now expects to report to CLT to receive 567 If we have DAIN(8) in receive window, the enhiest the ACK[6] has been received and DATA[5], was delivered But because SWS = 5, so all the DATALOJ'S sent were sent before DATACS]. The DATACOJ con't arrive later fare by the no-out-of-order arrival hypothesis.

7 BP	Address	D of
0.	MACCA)	Port
151	MAC(C)	F\
	MAC(A)	$\frac{\beta_2}{\beta_1}$
R.	MAC(C)	12
JZ	MACCO)	13 S 13 U
	MACCAS	B <sub>2</sub>
3	MAC(C)	1
13	MAC(D)	B4
B4	MAC (A)	BBZ
W.	MAL CO)	
and the second s	The second section of the sect	

When M sends to L, both B, and Br will receive the message and servel it to L, but they'll also to send a message to each other. Since the message they send to L has been received, they both know the write right path towards L. So the pracket will cacle endlessly in both M->B, ->L->B, and M->B, ->L->B, ways.