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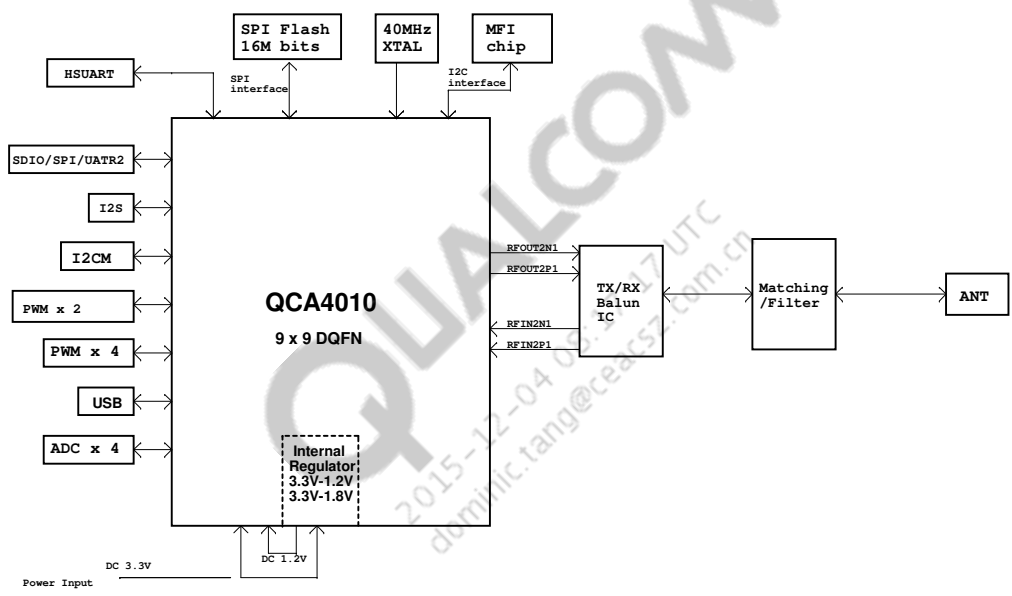
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DATE	REVISION NUMBER	INITIALS	DESCRIPTION
10/19/2015	A	Mingsen	update the I2C pinout based on the MP1-010_YA025 Reserve Pull-Down for MFI_RST

SCHEMATIC, CCA, Ruby QCA4010 1x1 2G SB iPA 2MB SI Flash UART/SPI/GPIO

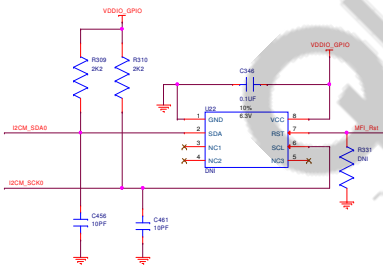
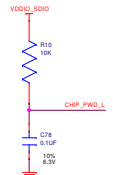
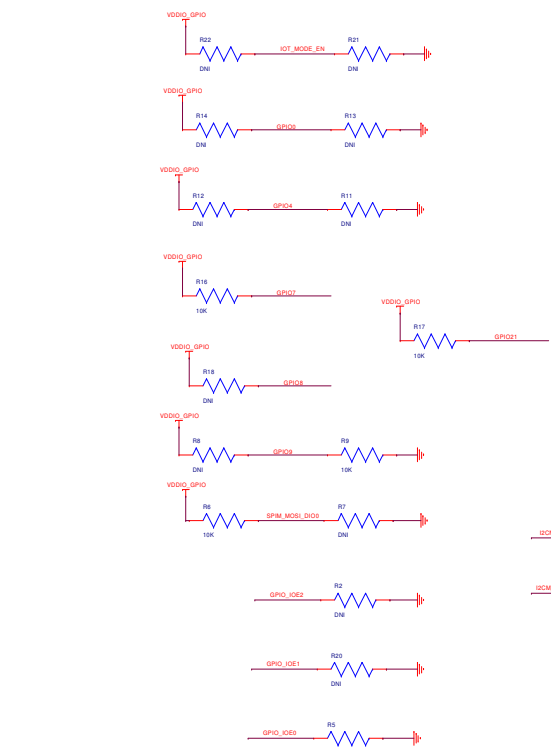
CONTRACT NO.		QUALCOMM Atheros, INC. 1700 TECHNOLOGY DRIVE SAN JOSE, CA 95119 U.S.A.	
DRAWN BY Mingsen	DATE 2015-10-19	TITLE	
DESIGNER		SCHEMATIC, CCA, Ruby QCA4010 1x1 2G SB iPA 2MB SI Flash UART/SPI/GPIO	
PROJECT ENGINEER		SIZE	
QUALITY ASSURANCE		D	
CONFIGURATION		DRAWING NO. LD20-YA163	
DESIGN ACTIVITY APPROVAL		REV A	
DESIGN APPROVAL		SCALE NONE	RELEASE DATE SHEET 1 OF 5

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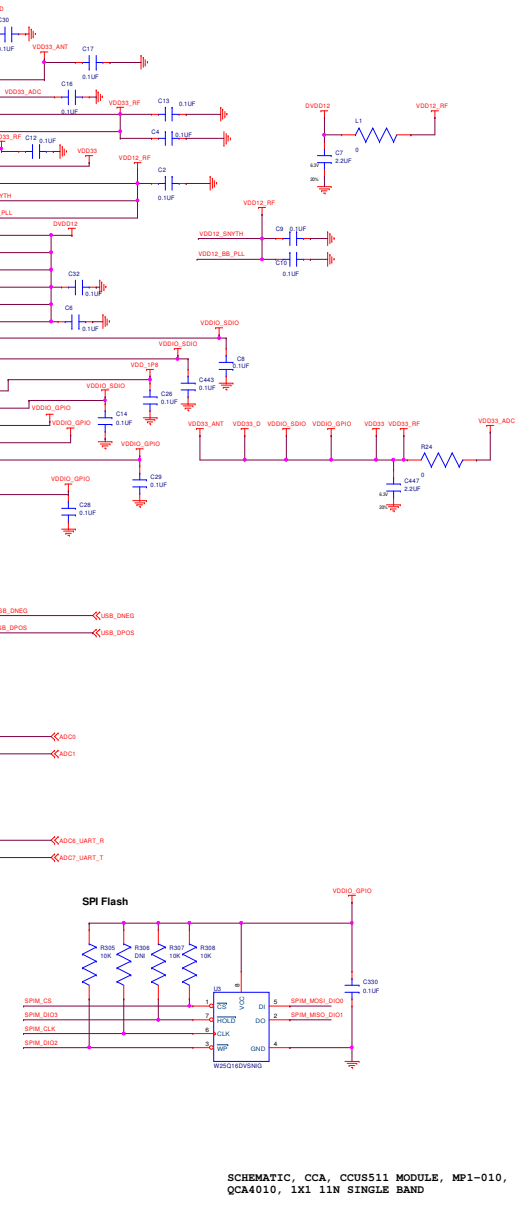
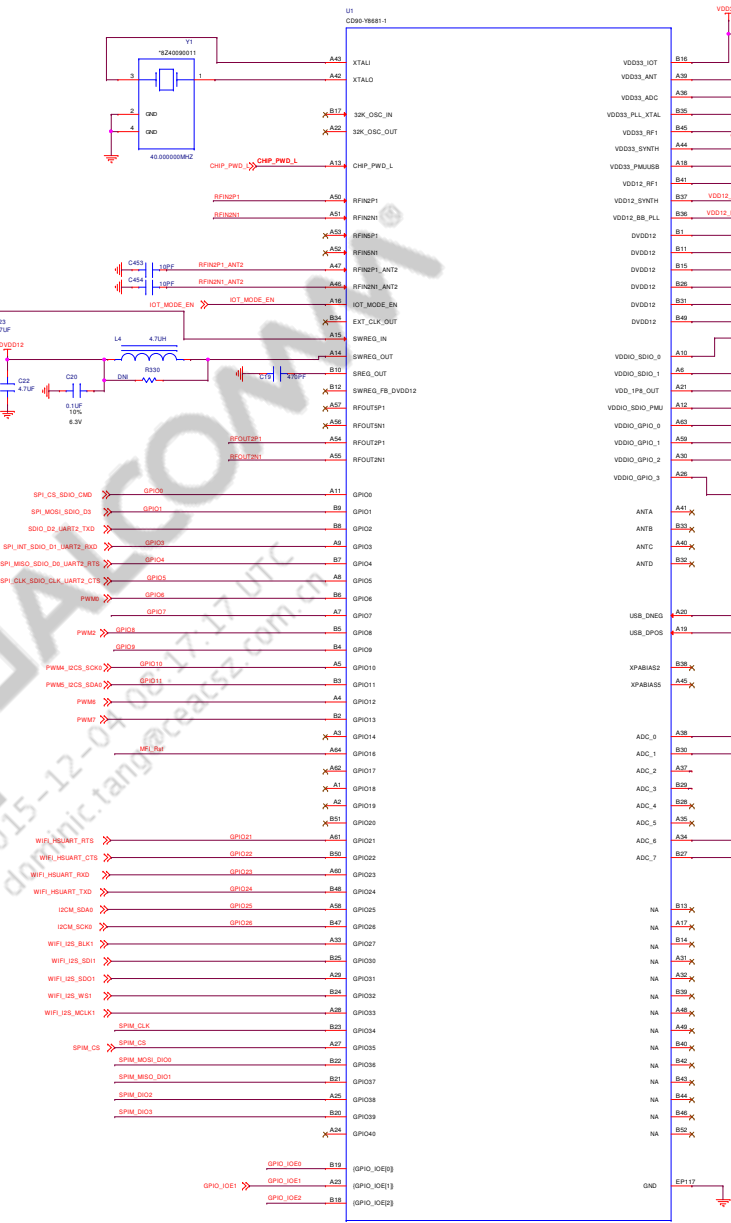
TITLE			
SCHEMATIC, CCA, Rev by QCA4010 1x1 50 GB IPA 2880 SI Flash UART/SPVGPIO			
QUALCOMM Atheros, INC. 1700 TECHNOLOGY DRIVE SAN JOSE, CA 95110 U.S.A.	SIZE	DRAWING NO	REV
	D	LD28-YA163	A
SCALE NONE		SHEET 2 OF 5	

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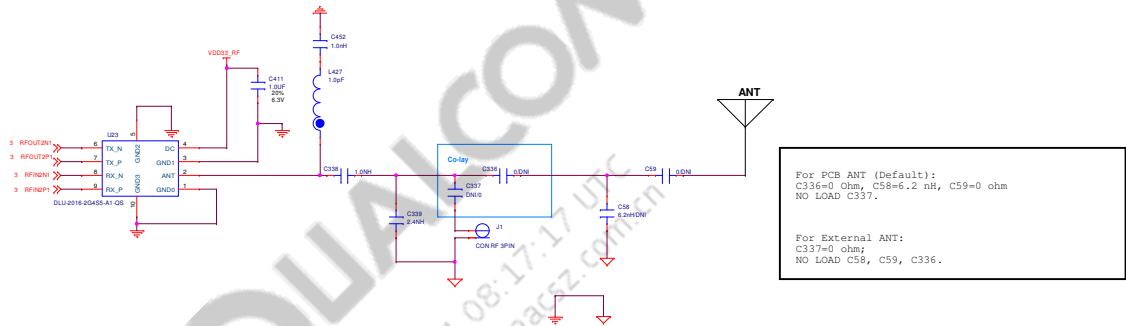
Boot Strap List:

Test Mode:	GPIO[8]	TEST Mode Enable	
Host Mode[0]:	GPIO[4]	HostMode[1:0] = 00, USB/Hostless	
Host Mode[1]:	GPIO[0]	HostMode[1:0] = 01, Hostless	
		HostMode[1:0] = 10, SPI	NO PULL
		HostMode[1:0] = 11, SDIO	
EN_LINEAR:	GPIO[9]	Pull Down for Switching Regulator	NO PULL
XTAL_FREQ:	GPIO[36]	Pull High for 40 MHz Crystal	NO PULL
IOT_MODE_EN:	IOT_WM		NO PULL



SCHEMATIC, CCA, CCUS511 MODULE, MP1-010, QCA4010, 1x1 11N SINGLE BAND

TITLE SCHEMATIC, CCA, CCUS511 MODULE, MP1-010, QCA4010, 1x1 11N SINGLE BAND			
SIZE D	DRAWING NO LD28-YA163	REV A	
SCALE NONE		SHEET 3	OF 5



TITLE		
SCHEMATIC, CCA, Rukby QCA4010 1x1 2G 98 IP4 2MB SIFlash UART/SPVGPID		
SIZE	DRAWING NO	REV
D	LD20-YA163	A
SCALE NONE		SHEET 4 OF 5

