实验五 线下验收表

序号	指令 PC=0	RV32 汇编语句	控制信号赋值，未列出的控制信号值为0	输出信号值
1	008000ef	jal x1, 0x8	ExtOp=4，RegWr=1，ALUBSrc=1，Branch=1	x1：0x00000004
2	ffdff06f	jal x0, -0x20	ExtOp=4，RegWr=1，ALUBSrc=1，Branch=1	x0：0x00000000(skip)
3	00100013	lb x0, 1	RegWr=1，ALUBSrc=2	x0：0x00000000
4	deadc2b7	lui x5, 0xdeadc	ExtOp=1，RegWr=1，ALUBSrc=2，ALUCtr=15	x5：0xdeadc000
5	eef28293	addi x5, x5, 0xeef	RegWr=1，ALUBSrc=2	x5：0xdeadbeef
6	0052d333	sltu x6, x5, x10	RegWr=1，ALUCtr=5	x6：0xdeadbeef
7	00135313	sltiu x6, x1, 1	RegWr=1，ALUBSrc=2， ALUCtr=5	x6：0x0000dead
8	4052d3b3	srai x7, x5, 13	RegWr=1，ALUCtr=13	x7：0x0000dead
9	00529e33	sll x28, x5, 1	RegWr=1，ALUctr=1	x28: 0xdf778000
10	001e1e13	slli x28, x28, 1	RegWr=1，ALUBSrc=2，ALUctr=1	x28：0xbeef0000
11	006e0eb3	add x29,x29, x5	RegWr=1	x29：0xbeefdead
12	01000513	addi x10, x10, 16	RegWr=1，ALUBSrc=2	x10：0x00000010
13	005500a3	sb x5, 16(x10)	ExtOp=2，ALUBSrc=2，MemWr=1，MemOp=5	M[0x10]：0x0000ef00
14	00551223	sh x5, 20(x2)	ExtOp=2，ALUBSrc=2，MemWr=1，MemOp=6	M[0x14]：0x0000beef
15	00551323	sh x5, 20(x2)	ExtOp=2，ALUBSrc=2，MemWr=1，MemOp=6	M[0x14]：0xbeefbeef
16	00552423	sw x5, 24(x2)	ExtOp=2，ALUBSrc=2，MemWr=1	M[0x18]：0xdeadbeef
17	00150483	lb x9, 1(x5)	RegWr=1 ， ALUBSrc=2 ， MemOp=5 ，MemToReg=1	x9：0xffffffef
18	00154903	lbu x9, 1(x5)	RegWr=1 ， ALUBSrc=2 ， MemOp=1 ，MemToReg=1	x18：0x000000ef
19	00451983	lh x19, 4(x5)	RegWr=1 ， ALUBSrc=2 ， MemOp=6 ，MemToReg=1	x19：0xffffbeef
20	00655a03	lhu x20, 6(x5)	RegWr=1 ， ALUBSrc=2 ， MemOp=2 ，MemToReg=1	x20：0xffffbeef
21	00452a83	lw x21, 4(x5)	RegWr=1，ALUBSrc=2，MemToReg=1	x21：0xbeefbeef
22	00a55b03	lhu x22, 10(x5)	RegWr=1 ， ALUBSrc=2 ， MemOp=2 ，MemToReg=1	x22：0x0000dead
23	ffff0bb7	lui x23, 0xffff	ExtOp=1，RegWr=1，ALUBSrc=2，ALUCtr=15	x23：0xffff0000
24	017afc33	and x24, x31, x23	RegWr=1, ALUCtr=7	x24：0xbeef0000
25	016c6cb3	or x25, x25, x12	RegWr=1, ALUCtr=6	x25：0xbeefdead
26	fb9e92e3	bne x29, x23, -1100	ExtOp=3, ALUCtr=2，Branch=5	PC：0x00000068
27	00000073	ecall		PC：0x0000006c