

General Registers

AH	AL
BH	BL
CH	CL
DH	DL
SP	
BP	
DI	
SI	

Execution Unit (EU)

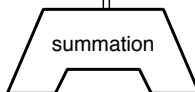
ALU Data Bus

Temporary Reg

ALU

Flag

EU
Control
System



summation

CS

DS

SS

ES

IP

Internal Commu-
nication Reg

Bus
Control
Logic

Outside
of the CPU

Instruction Queue

Bus Interface
Unit (BIU)

