ECE492/592 – Operating Systems Design: Project #3

Due date: November 14, 2018 (firm deadline)

Note: You can perform this assignment in teams of two.

Objectives

- To understand Xinu's implementation of semaphores.
- To understand deadlocks and their detection.
- To understand the priority inversion problem. [Required only for students taking the course at the graduate level ECE592]
- To implement different synchronization mechanisms in Xinu, verify their correct operation and evaluate their performance on representative test cases.

Overview

This programming assignment focuses on synchronization. Your goal is to implement locks in Xinu based on the test and set hardware instruction. You will start with a basic implementation and progressively refine it.

Programming assignment

Important: You will use the <u>same header file (include/lock.h)</u> for all lock variants, but implement each lock variant in a different .c file (as indicated below). <u>Your implementation cannot rely on semaphores or on interrupts disabling</u> (although you don't need to eliminate interrupts disabling from Xinu's code).

1. Provide an assembly implementation of the atomic test_and_set function. The function should have the following declaration and implement the following code atomically:

```
uint32 test_and_set(uint32 *ptr, uint32 new_value) {
   uint32 old_value = *ptr;
   *ptr = new_value;
   return old_value;
}
```

Your implementation should be based on the XCHG x86 instruction (see Intel Architecture Software Developer's Manual, Volume 2) and be written entirely in assembly. Note that the GNU assembler (as) used in the development-system machine uses the AT&T System V/386 assembler syntax, which is slightly different from the Intel x86 syntax described in the manual. For example, the ctxsw.S file is written using the AT&T syntax.

You can find information on the differences between the Intel and AT&T syntax here:

http://www.cs.cmu.edu/afs/cs/academic/class/15213-f01/docs/gas-notes.txt

You can find more information (including how to instruct as to support the Intel syntax) here:

http://web.mit.edu/rhel-doc/3/rhel-as-en-3/i386-syntax.html

Name the file containing the test_and_set implementation testandset.S and place it in the system folder. Comment each line of this assembly file.

2. Implement a spinlock based on your test_and_set function. The spinlock should have an initialization, a lock and an unlock functions declared as specified below. You are free to define the sl_lock_t data type as you wish.

```
void sl_init(sl_lock_t *1);
void sl_lock(sl_lock_t *1);
void sl unlock(sl lock t *1);
```

The spinlock implementation should be in a system/spinlock.c file.

3. Implement a lock that limits busy waiting by putting the current process to sleep. In particular, your implementation should follow Section 28.14 of the textbook (http://pages.cs.wisc.edu/~remzi/OSTEP/threads-locks.pdf). Note that you need to implement your own park, unpark and setpark primitives (you can, whenever possible, reuse existing Xinu's system calls). The lock should again be based on the test_and_set function that you have implemented, and should have an initialization, a lock and an unlock functions declared as specified below. You are free to define the lock t data type as you wish.

```
void init_lock(lock_t *1);
void lock(lock_t *1);
void unlock(lock t *1);
```

The lock implementation should be in a system/lock.c file.

4. Modify your lock implementation (3) so to automatically detect the presence of a deadlock. Your implementation should notify of the presence of a deadlock (without performing any corrective actions). The initialization, lock and unlock functions should now be declared as follows.

```
void al_init(al_lock_t *1);
void al_lock(al_lock_t *1);
void al_unlock(al_lock_t *1);
bool8 al trylock(al lock t *1);
```

Note that this lock variant has the additional al_trylock function (similar to POSIX threads pthread_mutex_trylock). al_trylock tries to obtain a lock and it returns immediately to the caller if the lock is already held. The function returns true if it has obtained the lock, and false if it hasn't.

The lock implementation should be in a system/active_lock.c file. Again, you are free to define the al_lock_t data type as you wish.

5. [Required only for students taking the course at the graduate level – ECE592] Modify your lock implementation (3) so to avoid priority inversion. Use the "Basic Priority Inheritance Protocol" described in the following paper:

L. Sha, R. Rajkumar and J. P. Lehoczky, "Priority inheritance protocols: an approach to real-time synchronization," in IEEE Transactions on Computers, vol. 39, no. 9, pp. 1175-1185, Sep 1990.

The initialization, lock and unlock functions should now be declared as follows.

```
void pi_init(pi_lock_t *1);
void pi_lock(pi_lock_t *1);
void pi_unlock(pi_lock_t *1);
```

This lock's implementation should be in system/pi_lock.c file. Again, you are free to define the pi lock t data type as you wish.

6. Write different representative test cases as follows:

```
Test case #1 (main-basic.c)
```

main-basic.c is meant to evaluate the correctness of your lock implementation by allowing multiple threads to perform the summation of a large array of integer numbers. You can use this test case just on the lock variant (3). Write three versions of the global summation code:

where

- array is the array of numbers to be summed;
- n is the size of the array;
- num threads is the number of threads performing the summation.

Run your test case using different array sizes and numbers of threads, and verify that the lock-based summation always provides correct results. Indicate in the report if your test case confirms that your implementation works as you expected (briefly explain).

Test case #2 (main-perf.c)

main-perf.c should be used to compare the performance of the locks (2) and (3).

Test case #3 (main-deadlock.c)

main-deadlock.c should be used to verify your active lock (4) implementation. This test case should include two parts.

Part 1 should trigger a deadlock situation and verify that the deadlock detection code works properly.

Part 2 should be a corrected version of the code of Part 1 that avoids the deadlock by making use of the trylock function.

[Required only for students taking the course at the graduate level – ECE592] Test case #4 (main-pi.c)

main-pi.c should be used to both verify and evaluate your priority inversion-free implementation. In other words, you will use main-pi.c to compare the performance and behavior of your implementations (3) and (5) – the former without and the latter with priority inheritance. This file should contain two test cases: one where priority inversion allows efficient execution, and one where the effectiveness of priority inversion is limited by the formation of a chain of blocking. Make sure that your test case allows verify that priority inheritance is transitive.

Important – include in the report:

- A description of your implementation approach.
- A description of your test cases, indicating whether its outcome is as you expected.

Please be clear and succinct.

Submissions instructions

- 1. <u>Important</u>: We will test your implementations using different test cases. Therefore, do not implement any essential functionality (other than your test cases) in the main.c file. Also, turn off debugging output before submitting your code.
- 2. Go to the xinu/compile directory and invoke make clean.
- 3. As for the previous project, create a xinu/tmp folder and **copy** all the files you have modified/created into it (the tmp folder should have the same directory structure as the xinu folder).
- 4. Go to the parent folder of the xinu folder. Compress the whole xinu directory into a tgz file.

```
tar czf xinu project3.tgz xinu
```

5. Submit your assignment – including tgz file and report – through Moodle. There is no need to print the report and bring it to class.