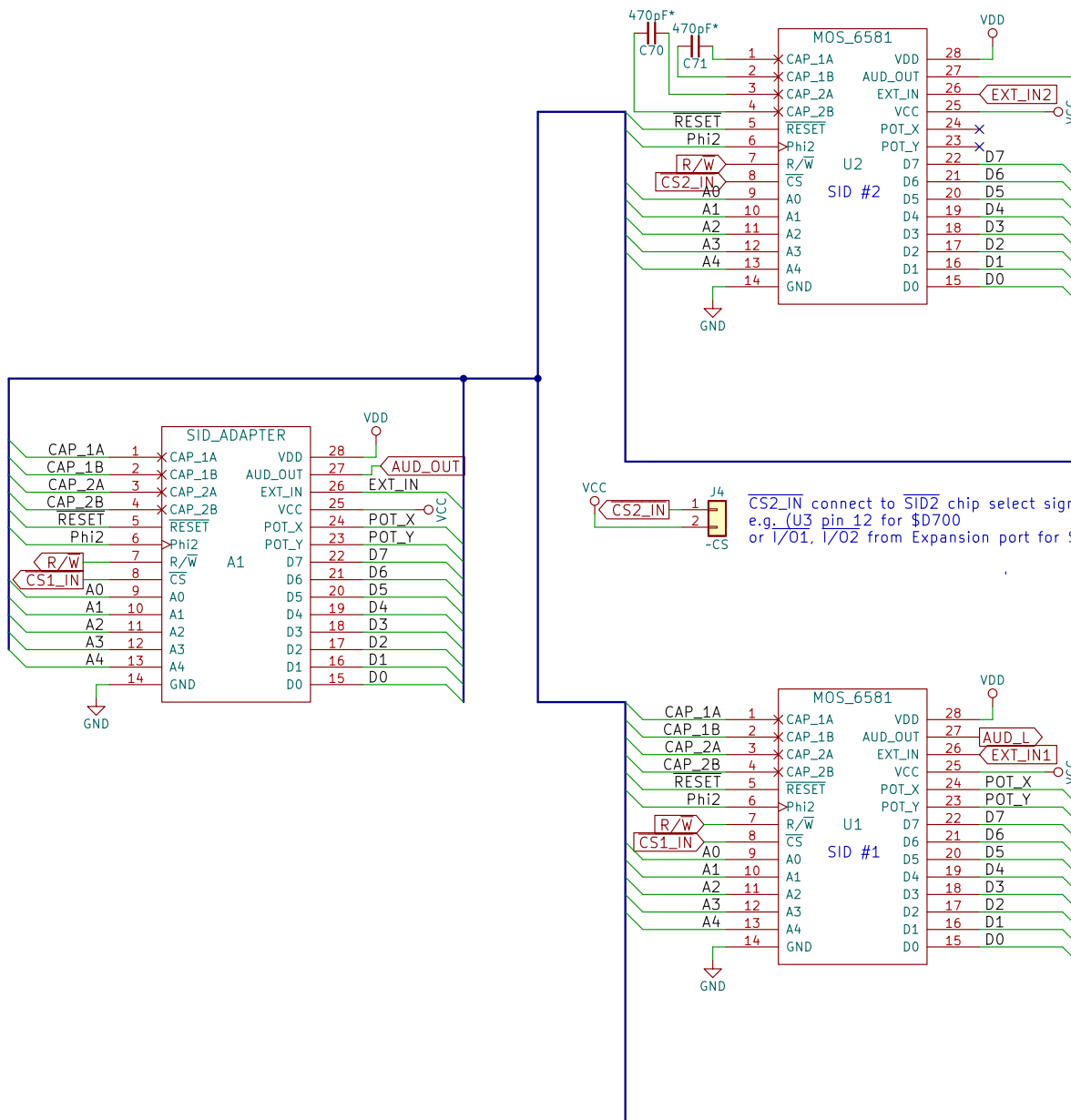
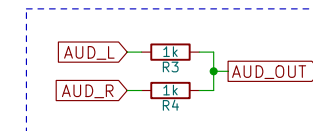
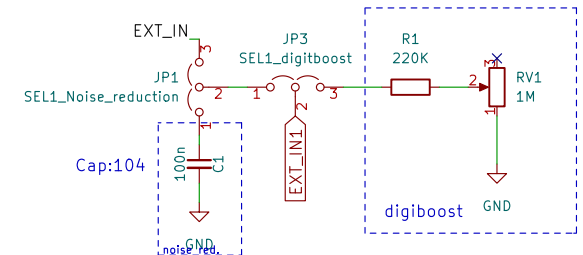
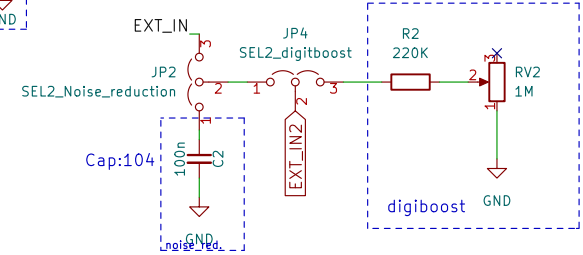


C128DCR – 8580 9V, skip installing R8, use 22nF (223) for C70/C71
 C128D – 6581 12V, install R8, use 470pF (471) for C70/C71



R8: Not used with 8580 chips, for motherboards that already support them!

JP1/JP2 bridge 1–2 for noise reduction
 JP1/JP2 bridge 2–3 for ext in
 JP3/JP4 bridge 1–2 to pass JP1/JP2 choice: noise reduction or ext in
 JP3/JP4 bridge 2–3 for digi boost



This section downmixes both SID outputs into a single signal output on the regular audio output (5 or 8 pin DIN).

based on <https://github.com/tebl/C64-Stereo-SID>
 and <http://henning-liebenau.de/mixsid>

YTM Enterprises

Sheet: /
 File: C128DCR Dual SID.kicad_sch

Title: C128DCR DualSID

Size: A4 Date: 2022-11-23

KiCad E.D.A. kicad (6.0.5)

Rev: D

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