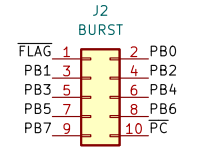
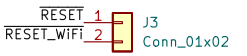
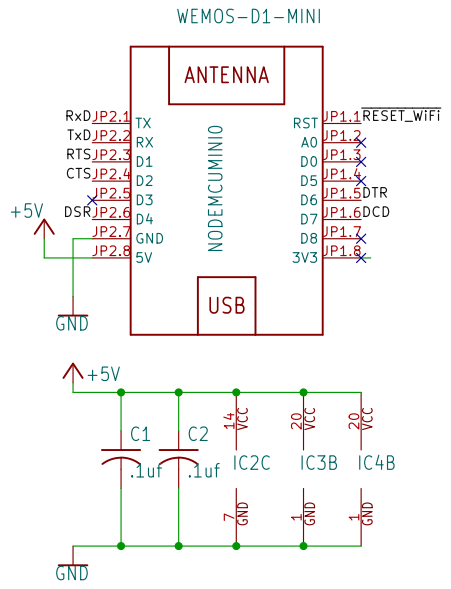
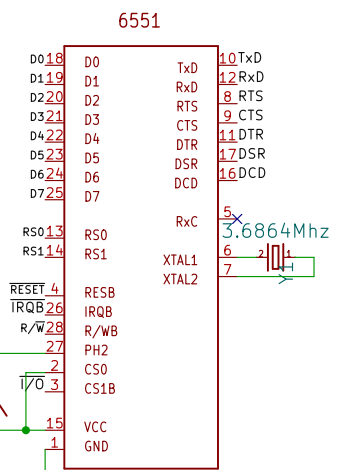
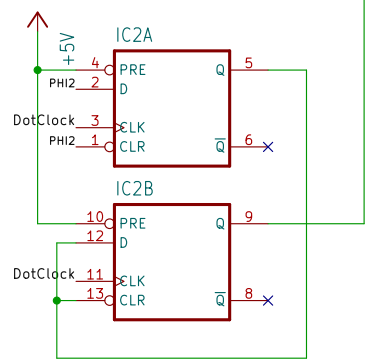


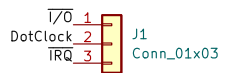
NMI/IRQ Header  
Jumper Pins 1-2 for NMI  
Jumper pins 2-3 for IRQ



VIA#1 in 1541 to CIA#2  
39, CA2 - FLAG  
2, PA0 - PB0  
3, PA1 - PB1  
4, PA2 - PB2  
5, PA3 - PB3  
6, PA4 - PB4  
7, PA5 - PB5  
8, PA6 - PB6  
9, PA7 - PB7  
18, CB1 - PC



I/O to expansion port (\$DExx pin 7/\$DFxx pin 10) or U3 pin 12 (\$D7xx)  
DotClock from expansion port pin 6  
IRQ from expansion port pin 4 or CIA#1 pin 21



Maciej Witkowiak, <ytm@elysium.pl>  
**YTM Enterprises**

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File: Link232-wifi.kicad\_sch

**Title: C128D Swift-L over CIA#2**

Size: A4 Date: 2022-12-13  
KiCad E.D.A. kicad (6.0.5)

Rev: B  
Id: 1/1