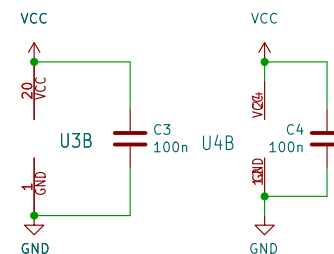
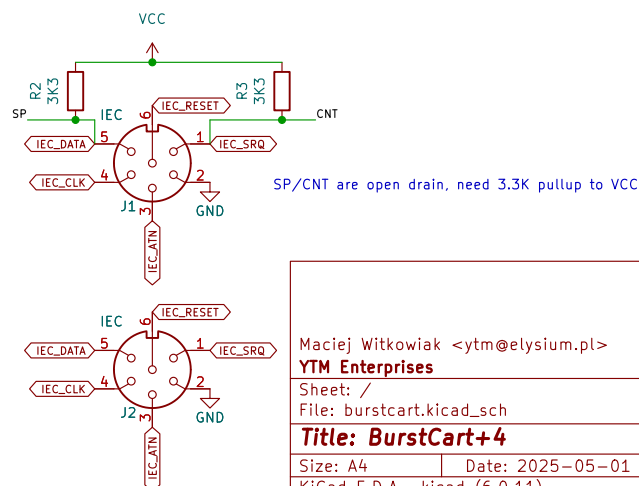
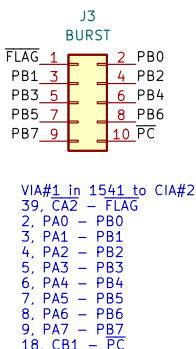
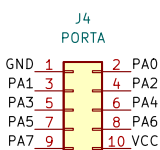
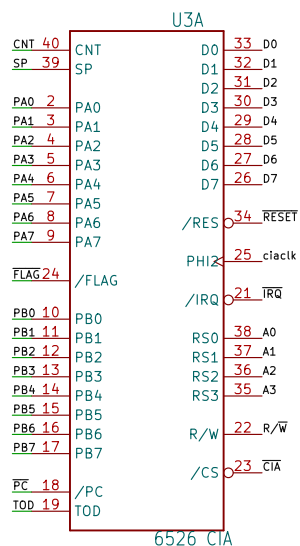
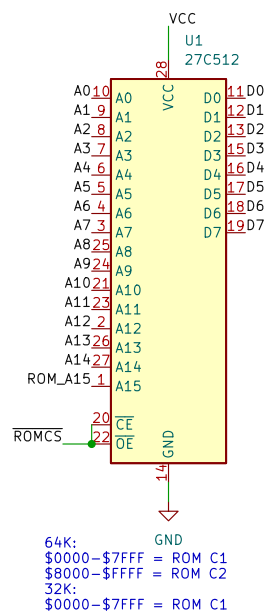
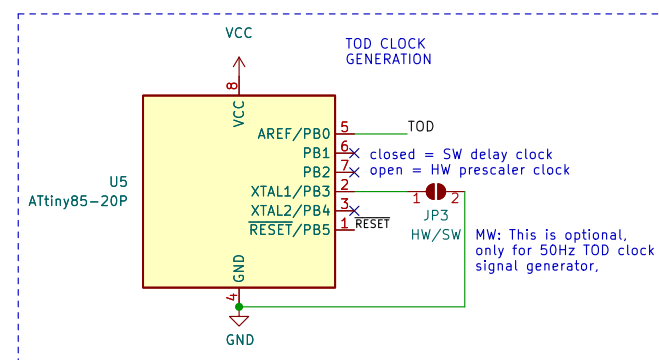
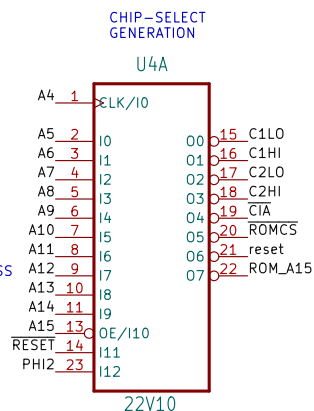
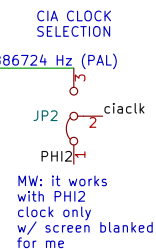


WE ALSO GENERATE AN ACTIVE-HIGH RESET SIGNAL THROUGH THE GAL IN ORDER TO LET THE CD4520 ALWAYS BE IN A KNOWN CONDITION AT POWER-ON/RESET.

THESE TWO THINGS DO NOT SEEM TO BE REQUIRED BY THE OLDER
4520'S (THE SAME ONES THAT SOLDER HIMSELF USED ON THE CARDS
HE BUILT), IN THIS CASE JUST SHORT R1 AND DO NOT MOUNT C5, THE
RESET SHOULD NOT HURT ANYWAY.

SEE <https://plus4world.powweb.com/forum/45294> FOR MORE DETAILS

MW: it only worked with CIA with R1 shorted and C5 removed, but was not reliable anyway. Therefore ciack comes from Phi2



Maciej Witkowiak <ytm@elysium.pl>

YTM Enterprises

Sheet: /

File: burstcart.kicad_sch

Title: BurstCart+4

Size: A4 Date: 2025-05-01

Size: A4	Date: 20
KiCad E.D.A.	kicad (6.0.11)

Rev: 1.1

Id: 1/1