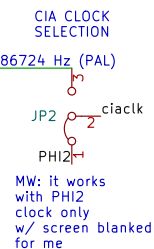


WE ALSO GENERATE AN ACTIVE-HIGH RESET SIGNAL THROUGH THE GAL IN ORDER TO LET THE CD4520 ALWAYS BE IN A KNOWN CONDITION AT POWER-ON/RESET.

THESE TWO THINGS DO NOT SEEM TO BE REQUIRED BY THE OLDER 4520'S (THE SAME ONES THAT SOLDER HIMSELF USED ON THE CARDS HE BUILT), IN THIS CASE JUST SHORT R1 AND DO NOT MOUNT C5, THE RESET SHOULD NOT HURT ANYWAY.

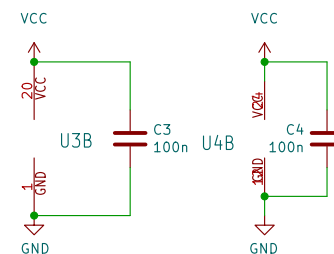
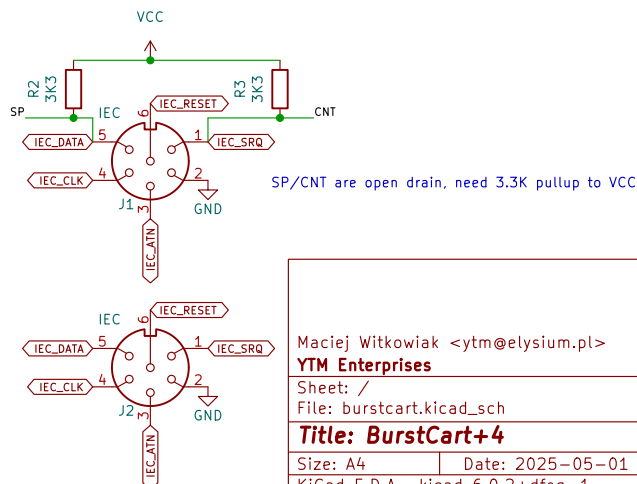
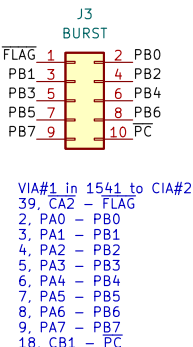
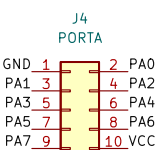
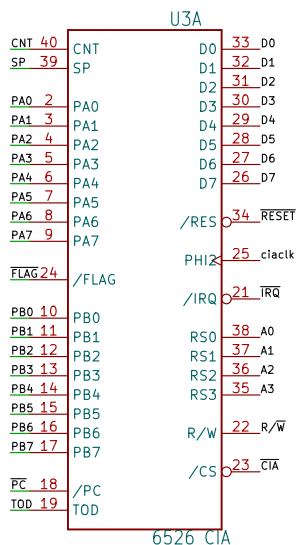
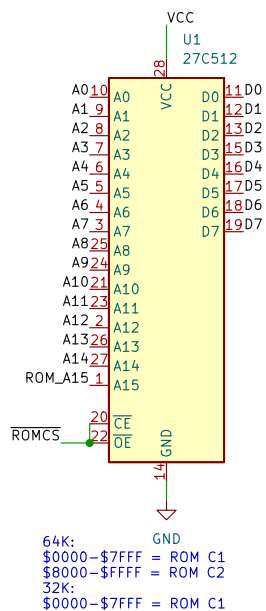
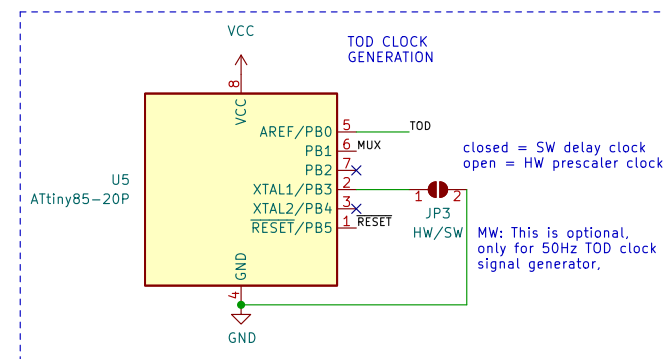
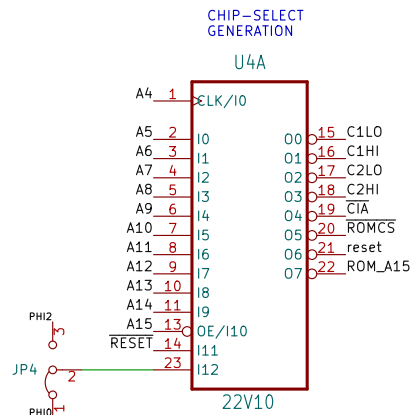
SEE <https://plus4world.powweb.com/forum/45294> FOR MORE DETAILS

MW: it only worked with CIA with R1 shorted and C5 removed, but was not reliable anyway. Therefore ciack comes from Phi2



PHI2 IS NECESSARY FOR ADDRESS
DECODING BECAUSE ADDRESSES
ARE VALID ON ITS RISING EDGE

MW: It seems reliable option is
Phi0 for GAL and Phi2 for CIA



YTM Enterprises

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Title: BurstCart+4

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Rev: 1.1

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