

CC=bottom left

GND CC GND
NC3 BB NC
NC2 AA NC
NC1 Z NC
A0 Y A0
A1 X A1
A2 W A2
A3 V A3
A4 U A4
A5 T A5
A6 S A6
A7 R A7
A8 P A8
A9 N A9
A10 M A10
A11 L A11
A12 K A12
A13 J A13
A14 H A14
A15 F A15
PHI0 E PHI0
RAS D RAS
RESET C RESET
C1LO B C1LO
GND A GND

A=bottom right

25=top left

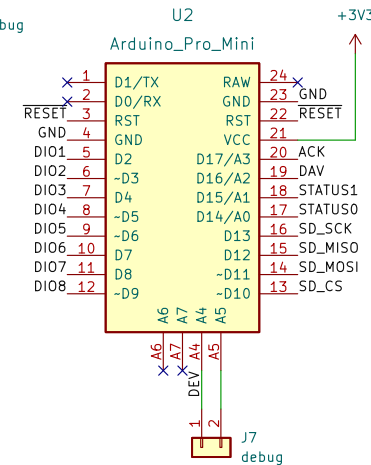
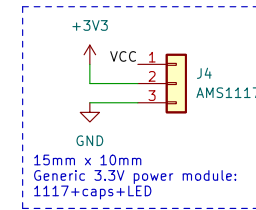
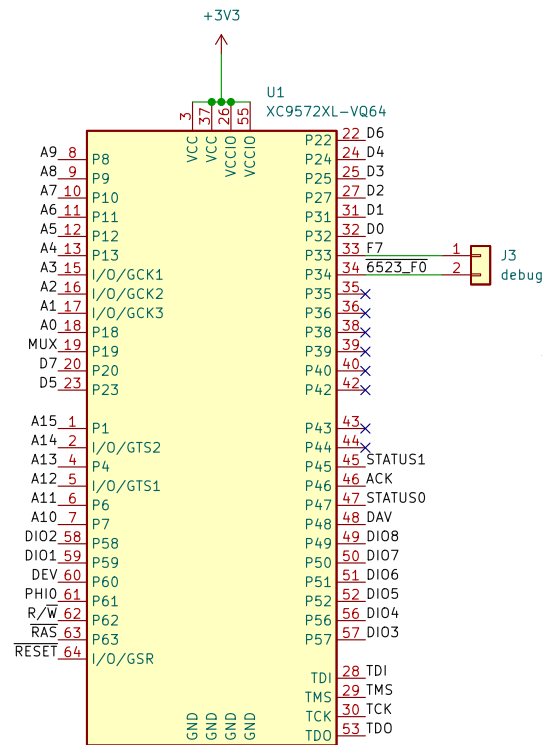
GND 25 GND
PHI2 24 PHI2
EXT_AUDIO 23 EXT_AUDIO
AEC 22 AEC
D0 21 D0
D1 20 D1
D2 19 D2
D3 18 D3
D4 17 D4
D5 16 D5
D6 15 D6
D7 14 D7
BA 13 BA
MUX 12 MUX
CAS 11 CAS
CS0 10 CS0
CS1 9 CS1
C2HI 8 C2HI
C2LO 7 C2LO
C1HI 6 C1HI
R/W 5 R/W
IRQ 4 IRQ
5V 3
5V 2 VCC
GND 1 GND

1=top right

GND CC GND
NC3 BB NC
NC2 AA NC
NC1 Z NC
A0 Y A0
A1 X A1
A2 W A2
A3 V A3
A4 U A4
A5 T A5
A6 S A6
A7 R A7
A8 P A8
A9 N A9
A10 M A10
A11 L A11
A12 K A12
A13 J A13
A14 H A14
A15 F A15
PHI0 E PHI0
RAS D RAS
RESET C RESET
C1LO B C1LO
GND A GND

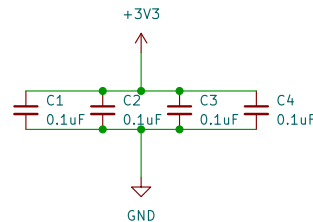
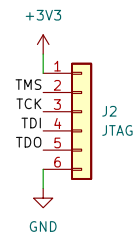
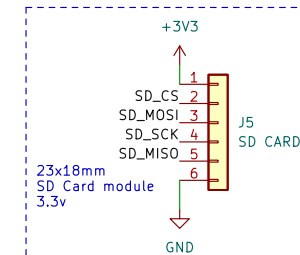
GND 25 GND
PHI2 24 PHI2
EXT_AUDIO 23 EXT_AUDIO
AEC 22 AEC
D0 21 D0
D1 20 D1
D2 19 D2
D3 18 D3
D4 17 D4
D5 16 D5
D6 15 D6
D7 14 D7
BA 13 BA
MUX 12 MUX
CAS 11 CAS
CS0 10 CS0
CS1 9 CS1
C2HI 8 C2HI
C2LO 7 C2LO
C1HI 6 C1HI
R/W 5 R/W
IRQ 4 IRQ
3 VCC
2 VCC
1 GND

C16-Exp-Port
J8



02x08 or 01x17 connector for a real drive
(or just use U2 solder points)

NOTE: ACK here is controller's ACK output and goes to cable pin 13 (drive's DAV input)
NOTE: DAV here is controller's ACK input and goes to cable pin 11 (drive's ACK output)



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YTM Enterprises

Sheet: /
File: tcbm2sd.kicad_sch

Title: tcbm2sd

Size: A4 Date: 2024-06-14
KiCad E.D.A. kicad (6.0.11)

Rev: 1
Id: 1/1