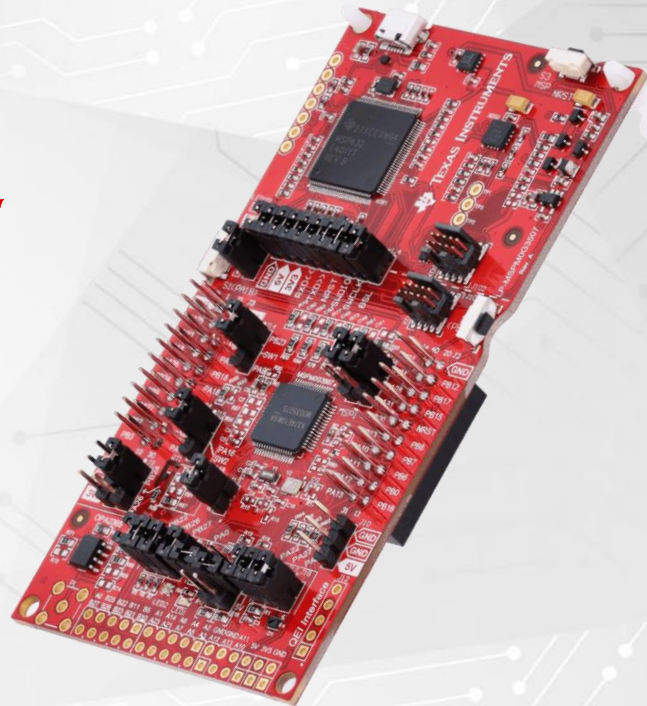


本文档根据TI官网手册整理，如有错误，[请参考文档s1au869](#)

LP-MSPM0G3507硬件描述

- 谢胜祥
- 31342592@qq.com
- 2024.1.29



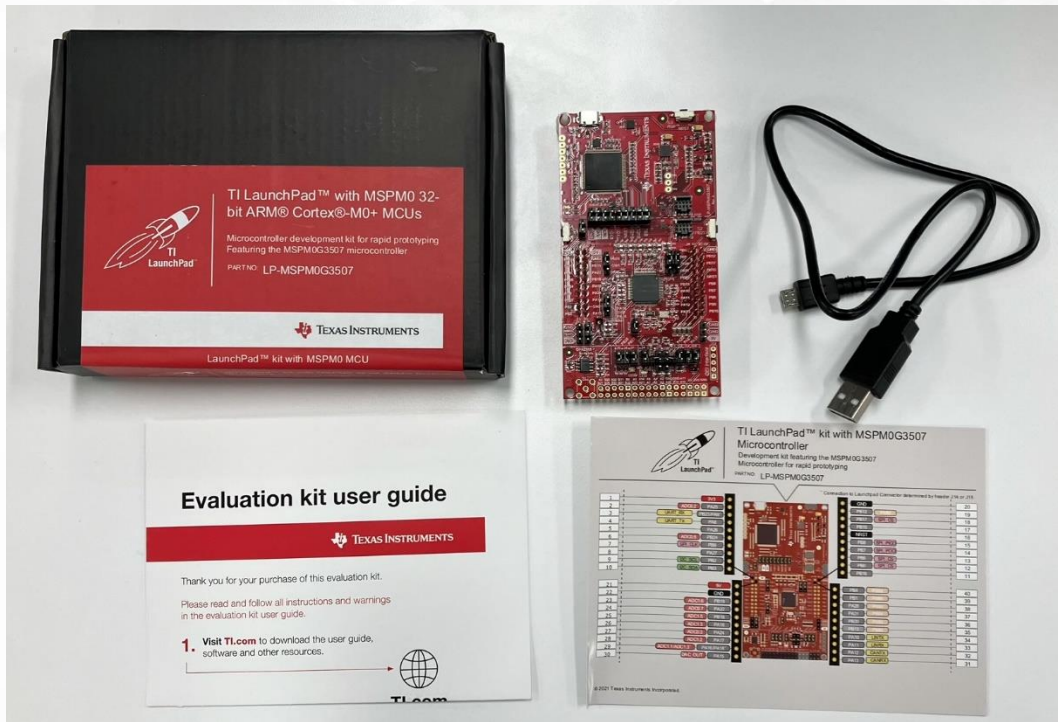
内容概述

- Launchpad套件概述
 - 板卡特性/MCU特性
- 调试器
- 系统供电
- 具体功能电路
 - MCU小系统
 - 按键、LED、
- 接口和跳线帽小结

Launchpad套件

LP-MSPM0G3507套件包含：

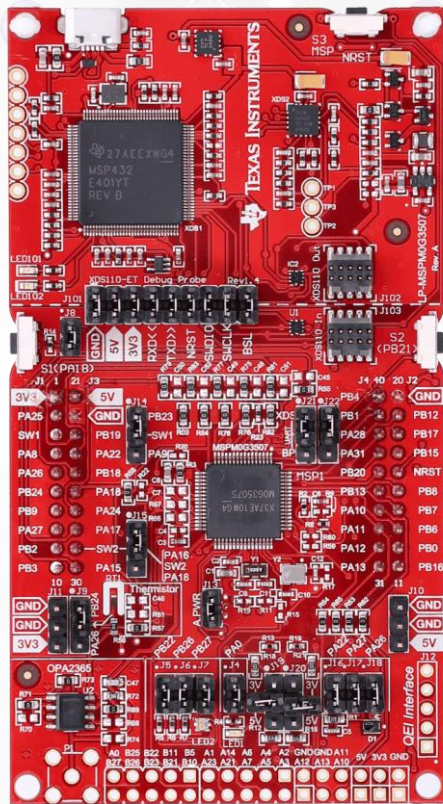
- MSPM0G3507 LaunchPad 板卡 x1
- Micro USB 数据线 x1
- 《快速入门指南》纸卡 x1



LP-MSPM0G3507特性

基本特性

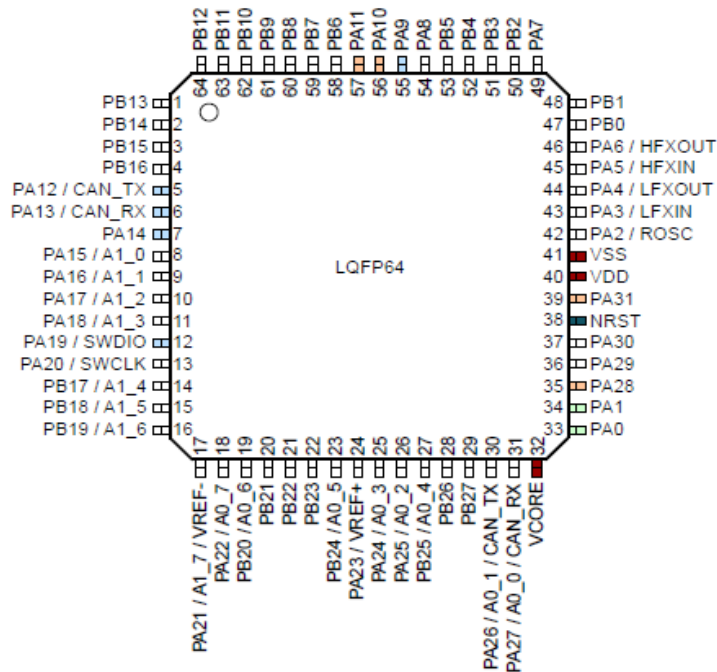
- 带外扩接口的板载 XDS110 -ET调试器
 - 可用于超低功耗调试的EnergyTrace 技术（配合CCS使用）
 - USB到PC的虚拟UART（USB-TTL转换器）
- 2 个用户按键(S1和S2)
- 1 个单色 LED1 和 1 个 RGB三色LED2
- 温度传感器电路（热敏电阻）
- 光传感器电路
- OPA2365电路（默认缓冲模式）可用于评估 4MSPS 的ADC
- ADC 输入的 RC 滤波器（默认未焊接）
- 所有引脚都引出



MCU—MSPM0G3507

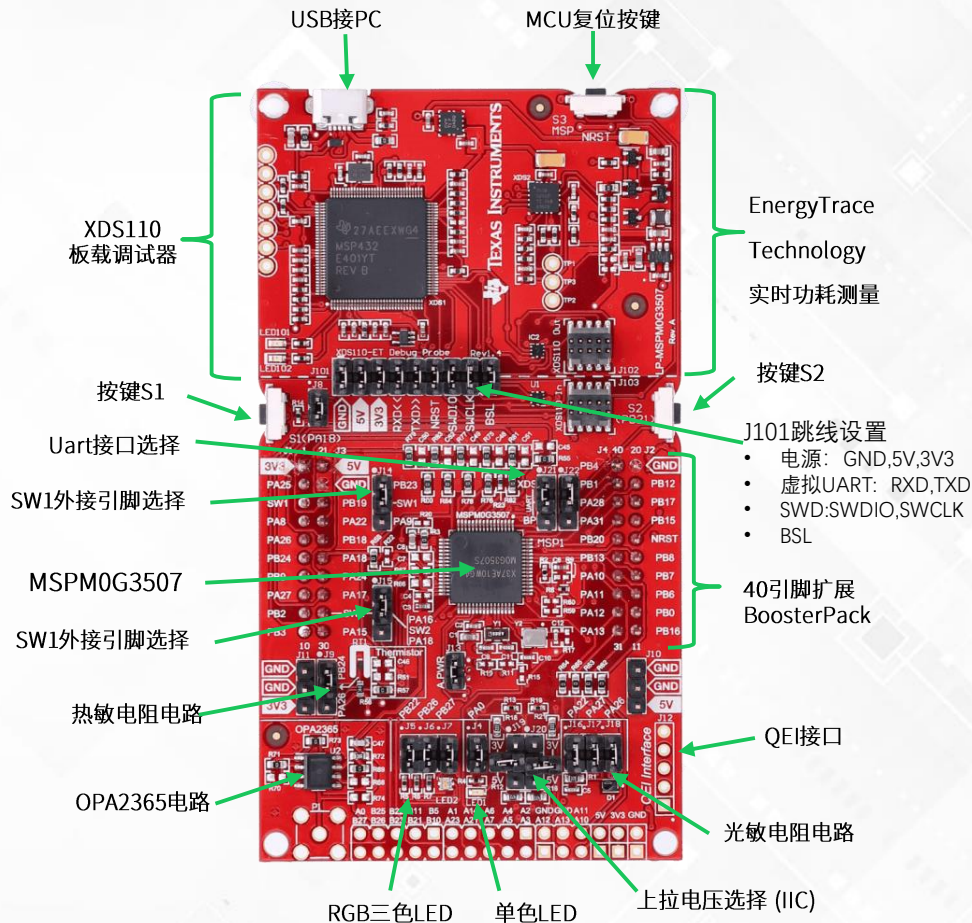
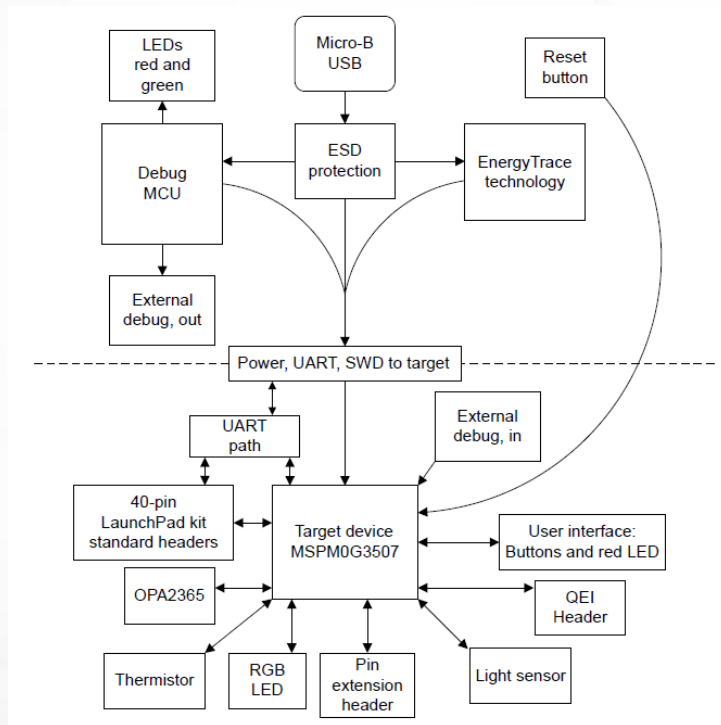
芯片特性:

- Arm 32 位 Cortex-M0+内核, 最高80MHz
- 宽电源电压范围: 1.62 V 至 3.6 V
- 工作温度范围: -40°C ~ 125 °C
- 具有纠错码128KB 闪存, 32KB SRAM
- 2个总计多达17个外部通道的12 位 4Msps同步采样ADC
- 1个具有集成输出缓冲器的 12 位 1Msps DAC
- 2个零漂移、零交叉斩波运算放大器OPA
- 1个通用运算放大器GPAMP
- 3个带8位DAC参考的高速比较器
- 内部参考电压 (1.4V或2.5V)
- 4个UART, 2个IIC, 2个SPI, 1个CAN
- 7个 定时器, 2个窗口式看门狗定时器, RTC
- 60个 GPIO



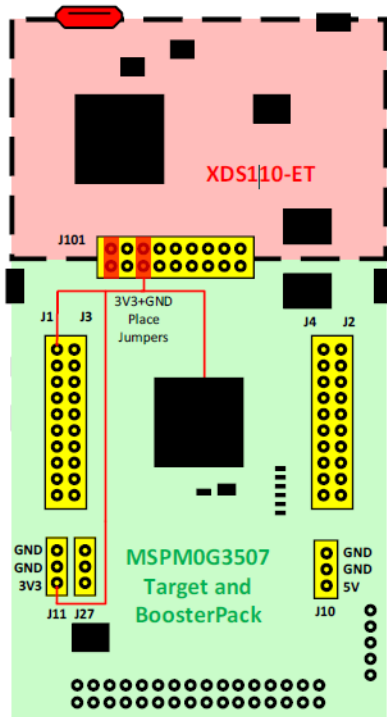
更多特性描述请查考器件手册

功能框图



板卡供电描述--电源(3.3V)

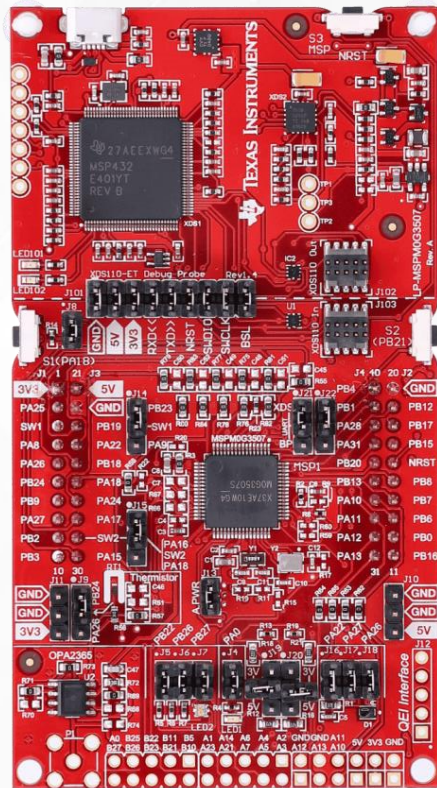
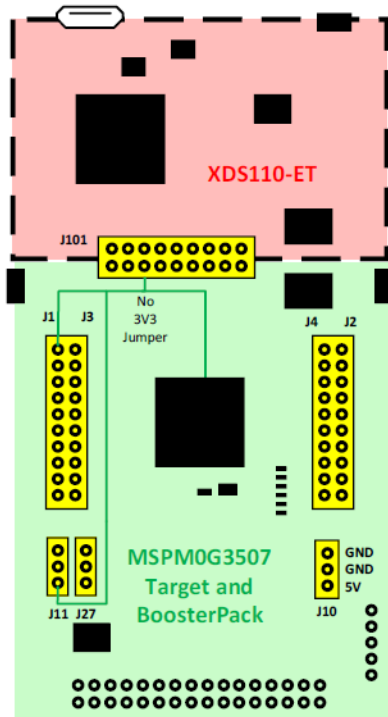
USB(XDS110-ET)
Power Configuration



Debug
Power
Domain

Target and
BoosterPack
Power
Domain

BoosterPack and External
Power Configuration

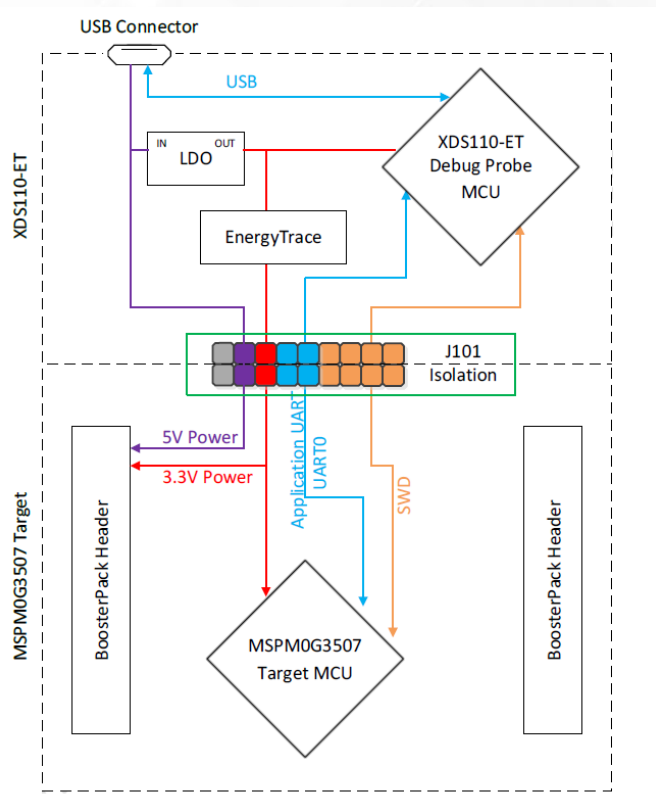


板载调试器

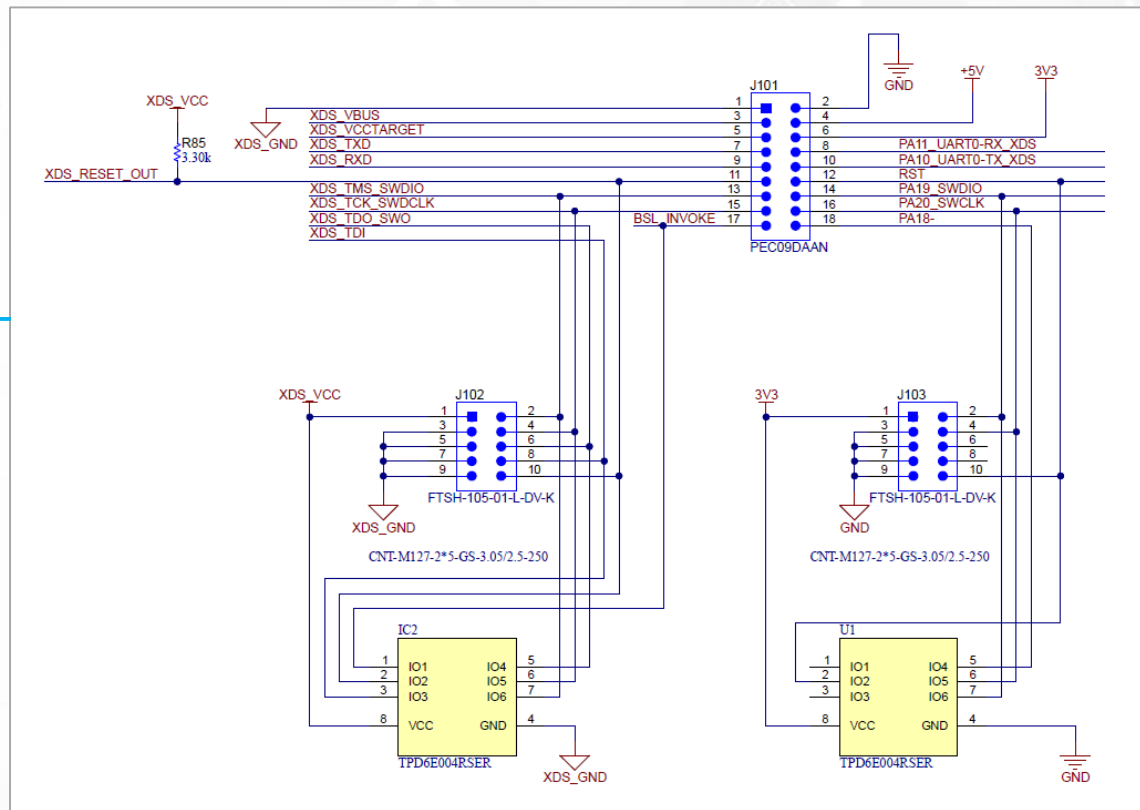
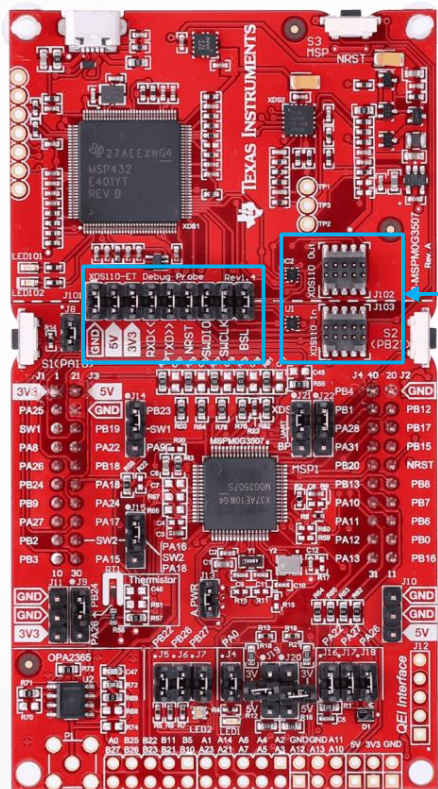
- 支持EnergyTrace技术的 XDS110-ET 板载调试器
 - 调试器连接：跳线帽的设置
- XDS110-ET调试器对外使用（SWD）
- XDS110-ET调试器用于外部板卡(JTAG)
- 使用外部调试器代替板载 XDS110-ET

跳线	描述
GND	地
5V	来自USB的VBUS 5V
3V3	3.3V, 从XDS110-ET域的VBUS出取得
RXD<<	虚拟UART: MSPM0G3507从该引脚接收数据
TXD>>	虚拟UART: MSPM0G3507发送数据到该引脚
NRST	RST信号
SWDIO	SWD调试信号: SWDIO数据信号
SWCLK	SWD调试信号: SWCLK时钟信号
BSL	Bootstrap Loader信号

J101接口描述

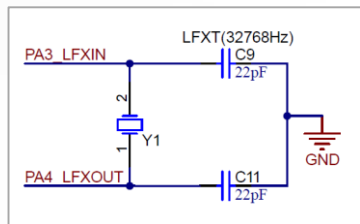


调试器接口

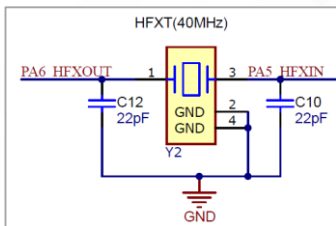


MCU小系统

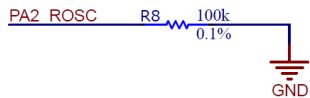
低频晶振



高频晶振



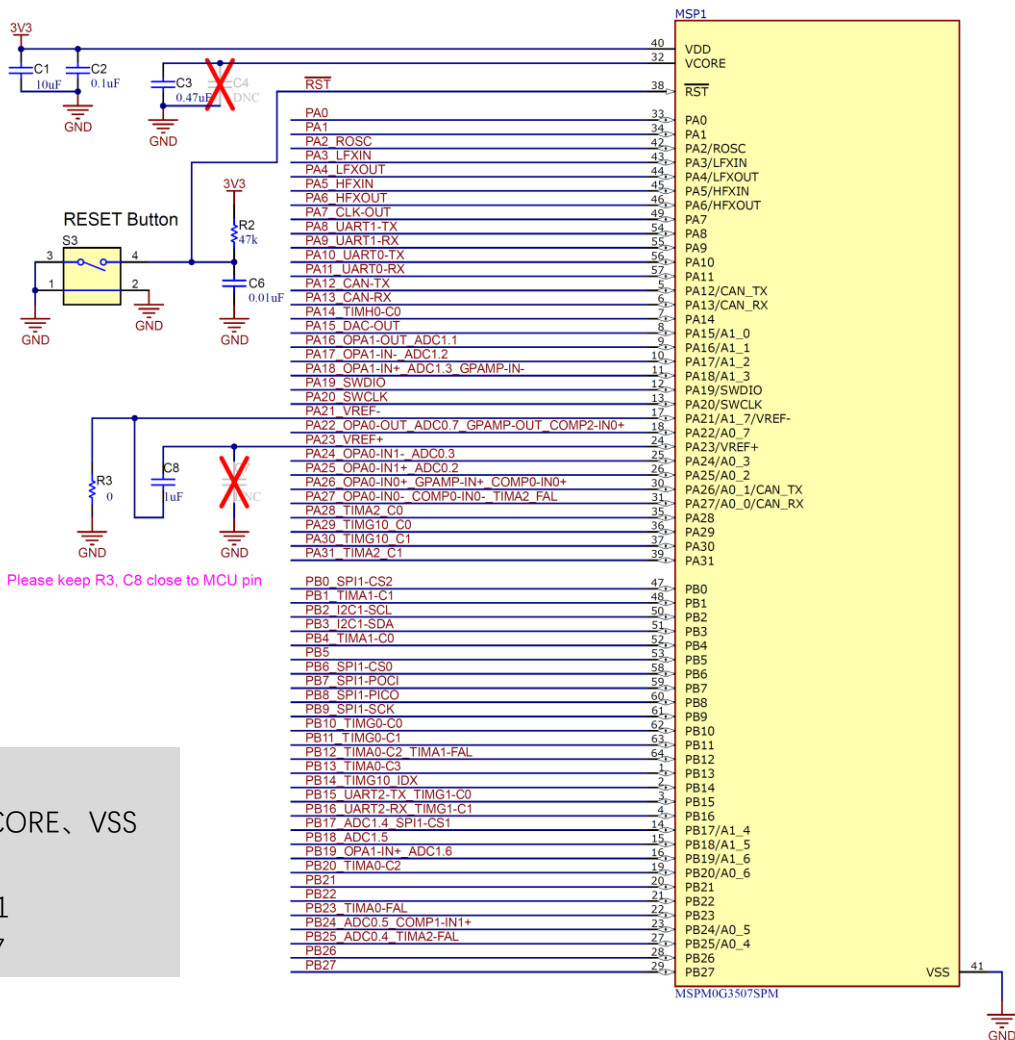
ROSC



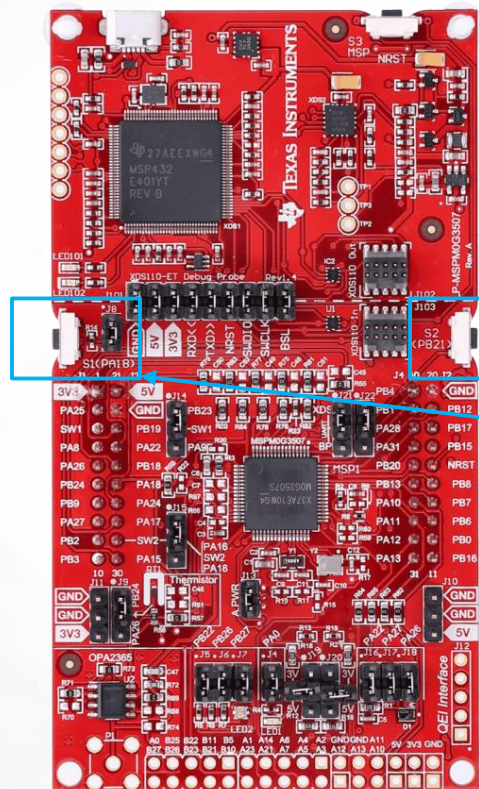
Please keep R8 close to MCU pin

芯片共64个引脚:

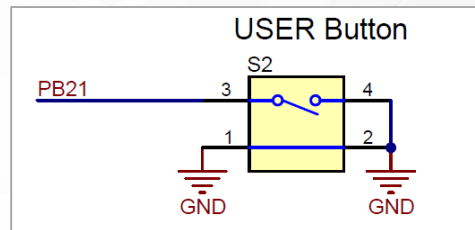
- 3个: VDD、VCORE、VSS
- 1个: RST
- 32个: PA0-PA31
- 28个: PB0-PB27



按键

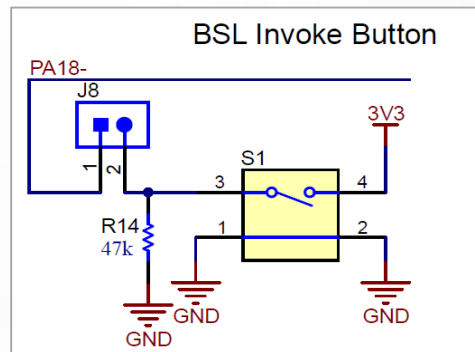


用户按键S2



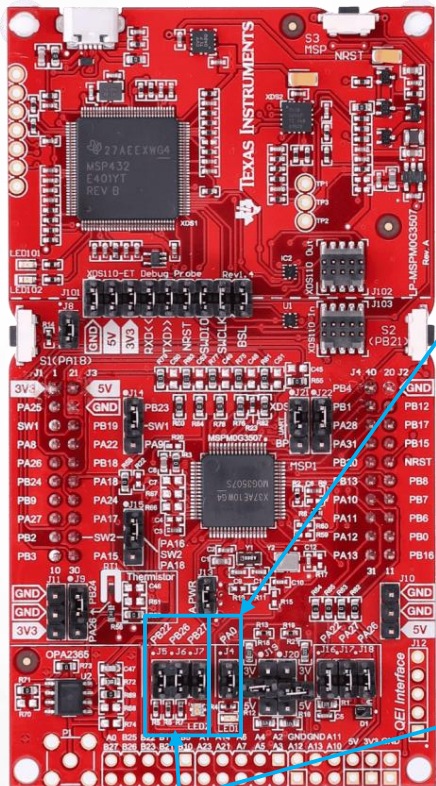
PB21配置输入，还需配置内部上拉，按键未按下时为高电平；按键按下时为低电平

BSL按键S1

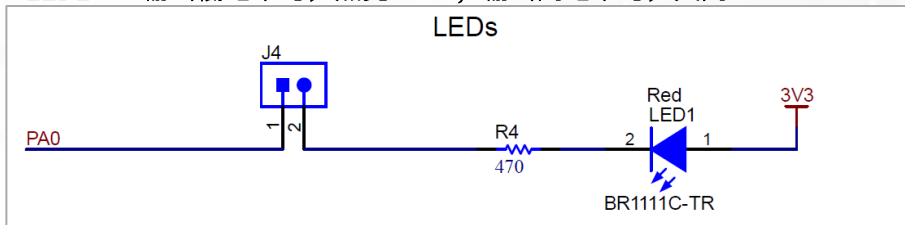


不用使能内部电阻，S1按键未按下时为低电平；按键按下时为高电平

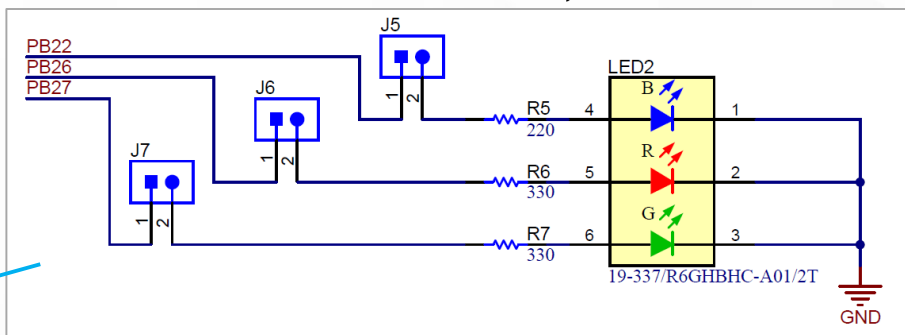
LEDs



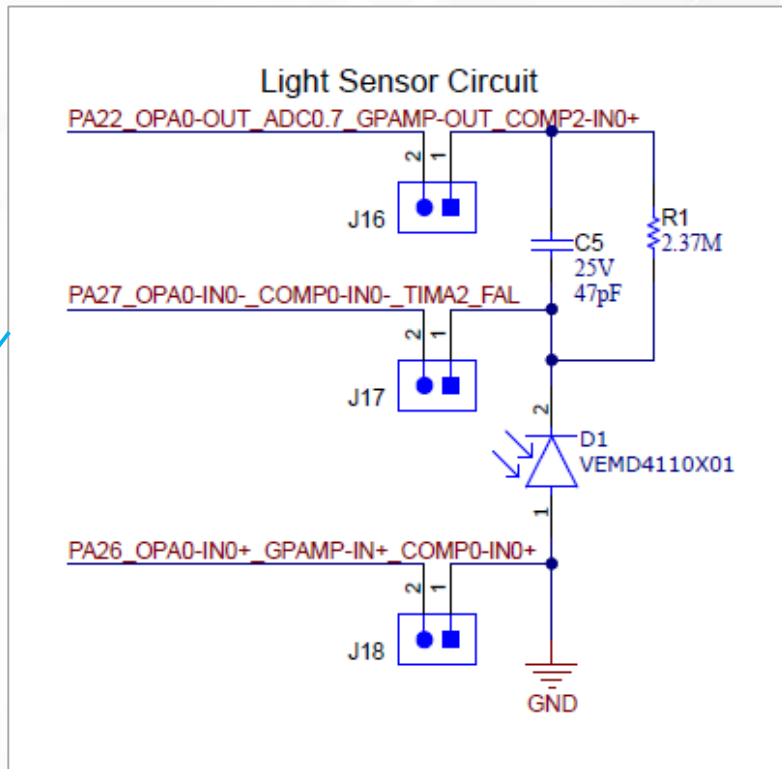
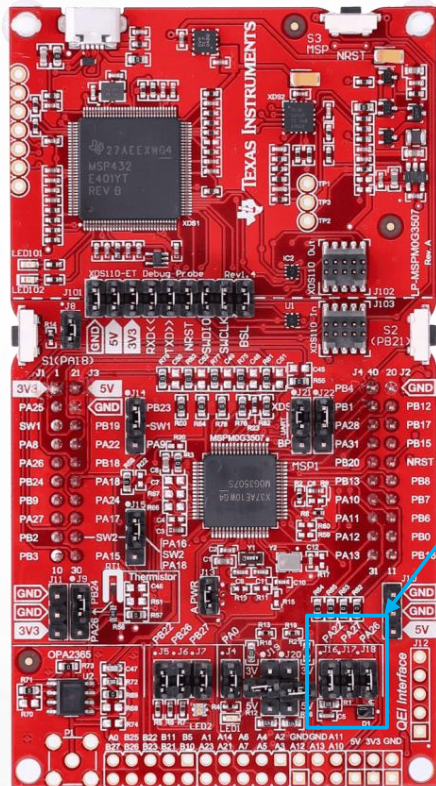
LED1 输出低电平时，点亮LED1；输出高电平时，关闭LED1



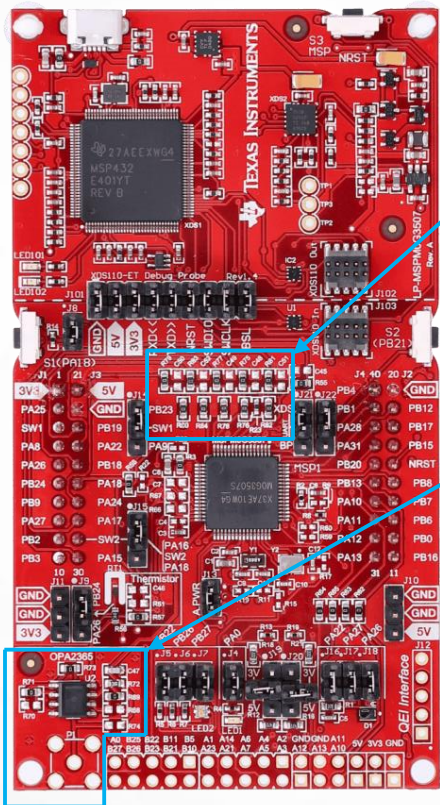
LED2,三色LED 输出高电平时，点亮LED；输出低电平时，关闭LED



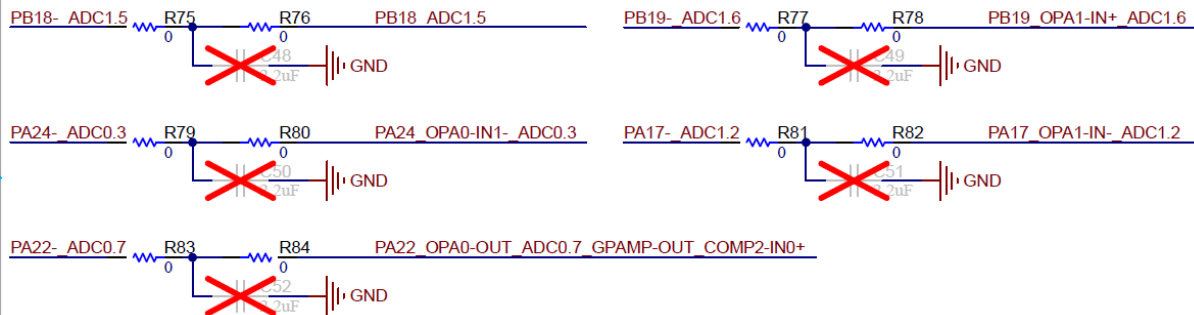
光照度传感器电路



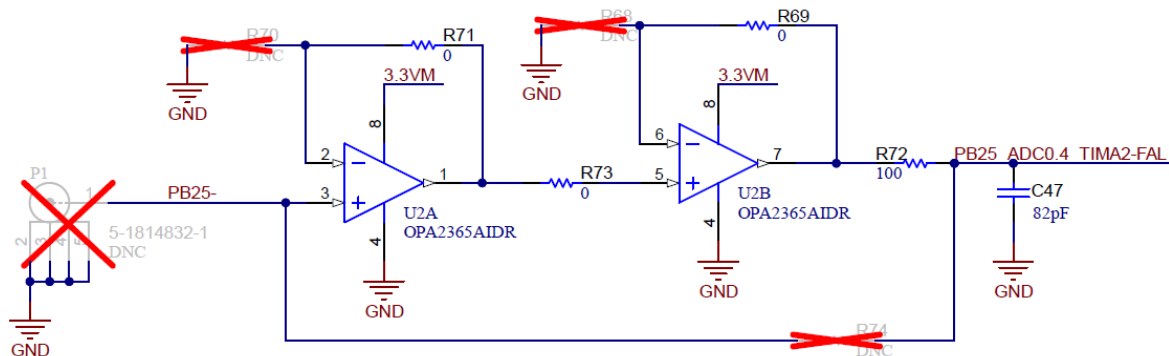
ADC测试电路



RC Filter for ADC Input

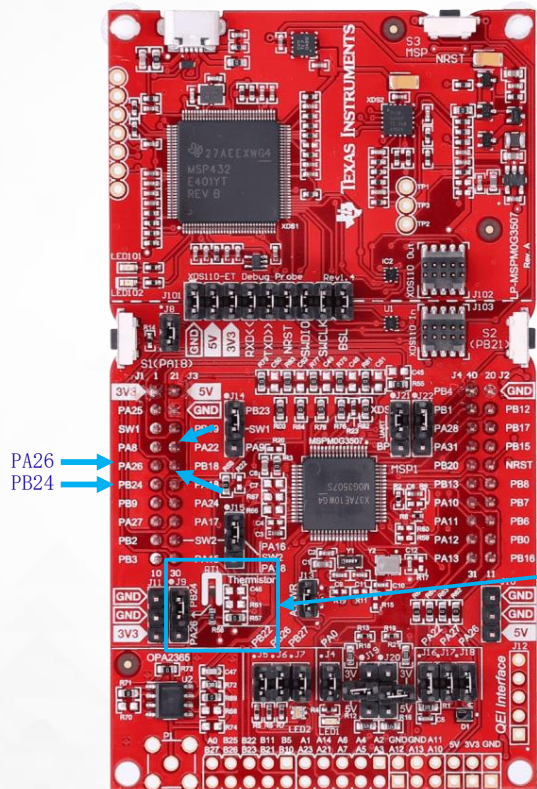


ADC Input with Active Buffer



Default OPA2365 is populated for the ADC input test;
If OPA2365 is NOT used, R72 need to be taken off and R74 0-ohm to be populated;

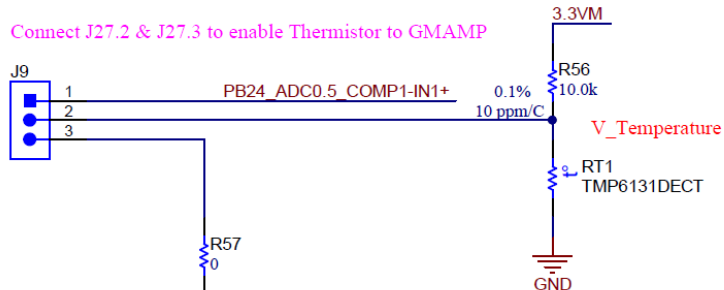
热敏电阻和通用运放



Thermistor Circuit

Connect J27.1 & J27.2 to enable Thermistor to ADC

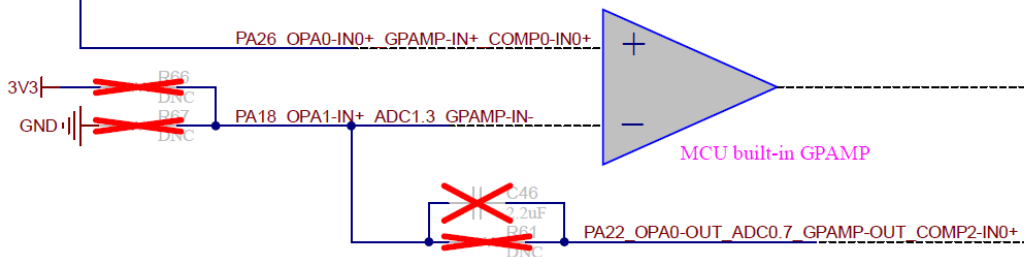
Connect J27.2 & J27.3 to enable Thermistor to GMAMP



GPAMP can work in Buffer mode or Amplify mode via change R57, R61, R66, R67, C46

GPAMP Test Circuit

This GPAMP can used in Thermistor or Motor Control(3 ISEN algorithm)

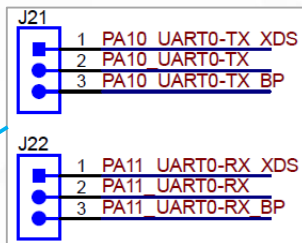


R66, R67 is voltage bias for GPAMP, 100-300mV

跳线帽设置

芯片的uart引脚通过跳线:

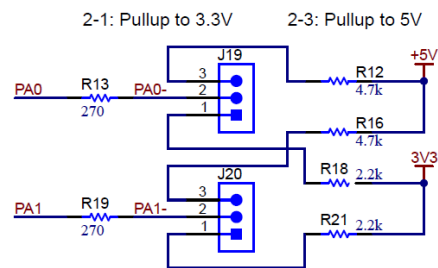
- 连接到xds
- 连接到可以BP



开漏输出的引脚选择上拉电源

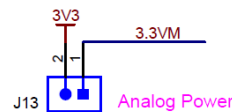
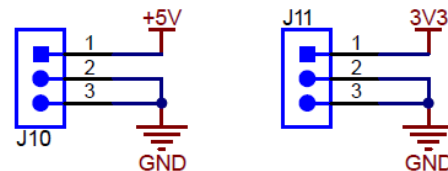
- 3.3V
- 5V

5V and 3.3V Pullup for Open-Drain IOs



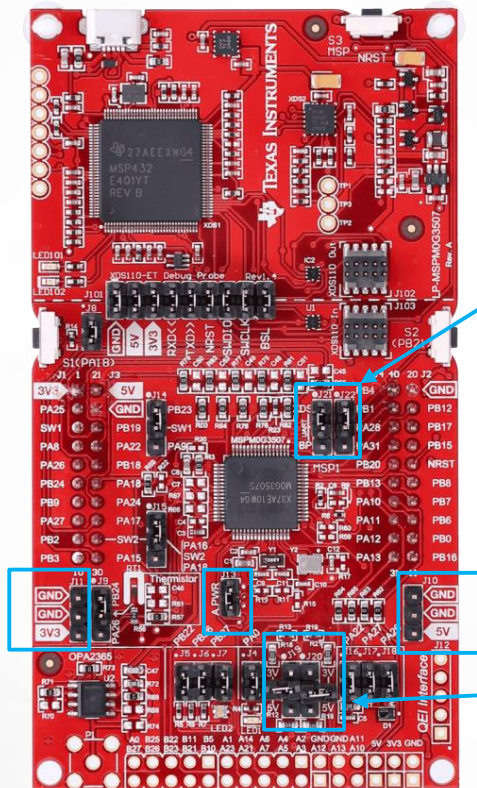
电源接口

Power Headers

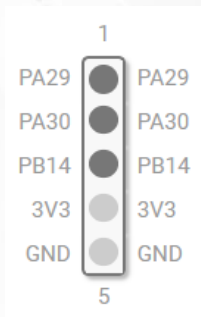
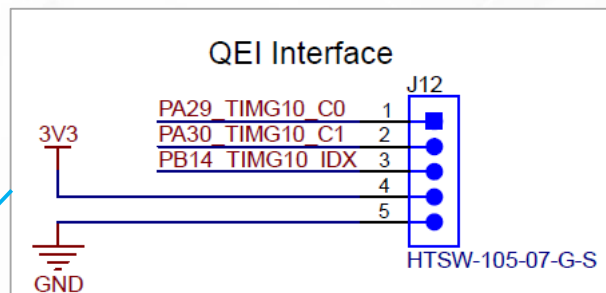
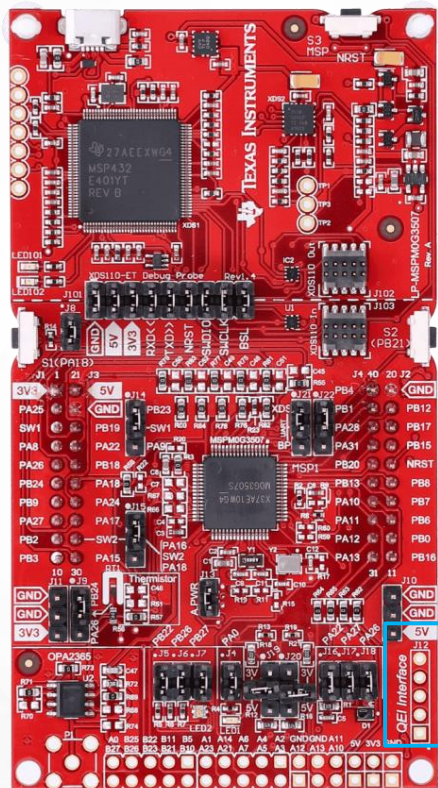


Current Measurement Jumper

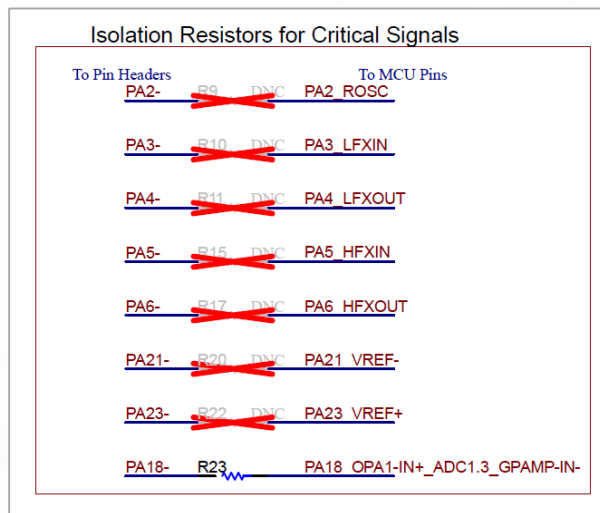
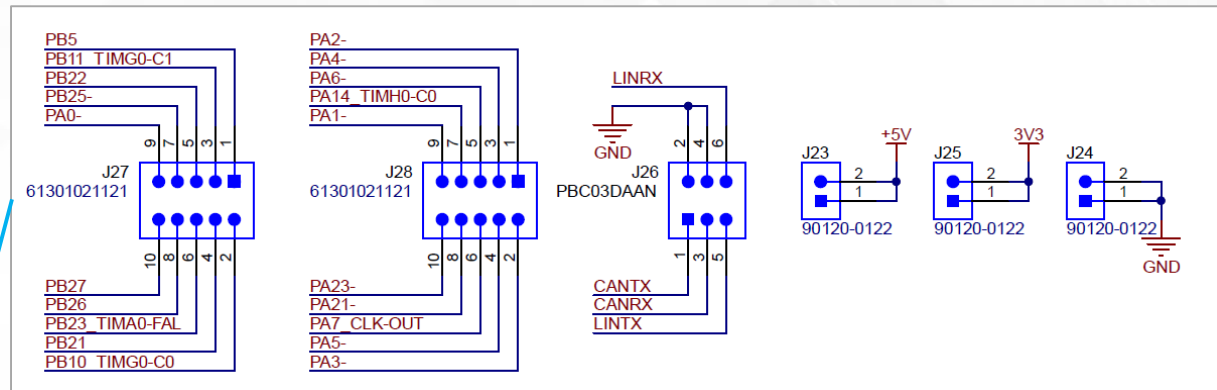
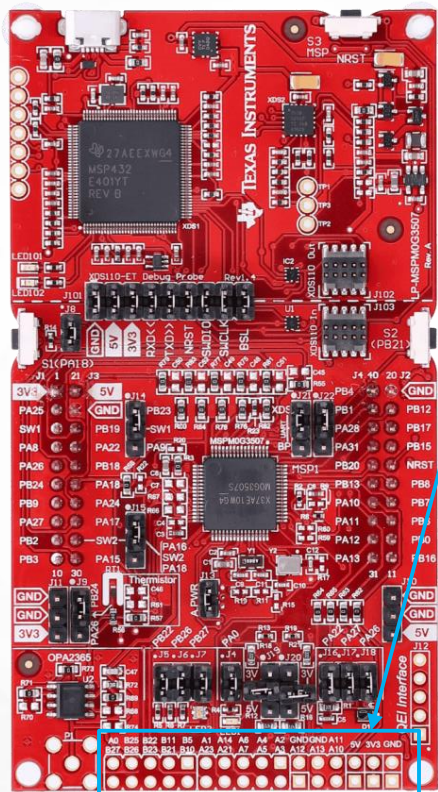
Unpopulate J13 to enable MCU power consumption measure



QEI接口



底部的扩展接口

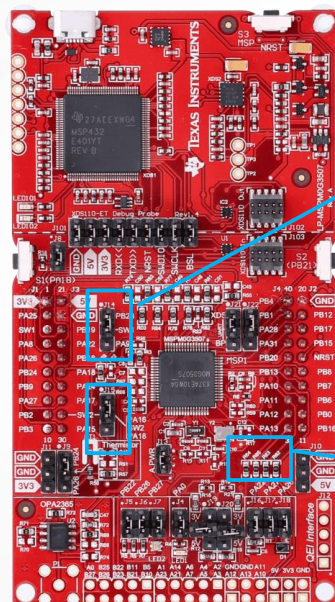


部分特殊功能引脚也对外引出到底部的接口，通过0欧电阻配置。

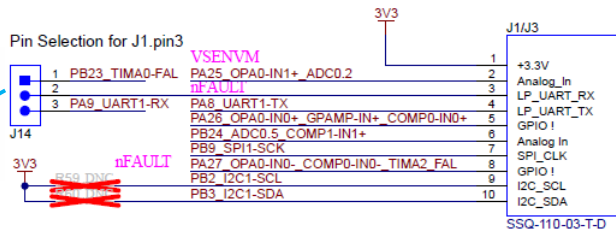
默认0欧电阻未焊接，是断开的。

BoosterPack接口

BoosterPack Connectors

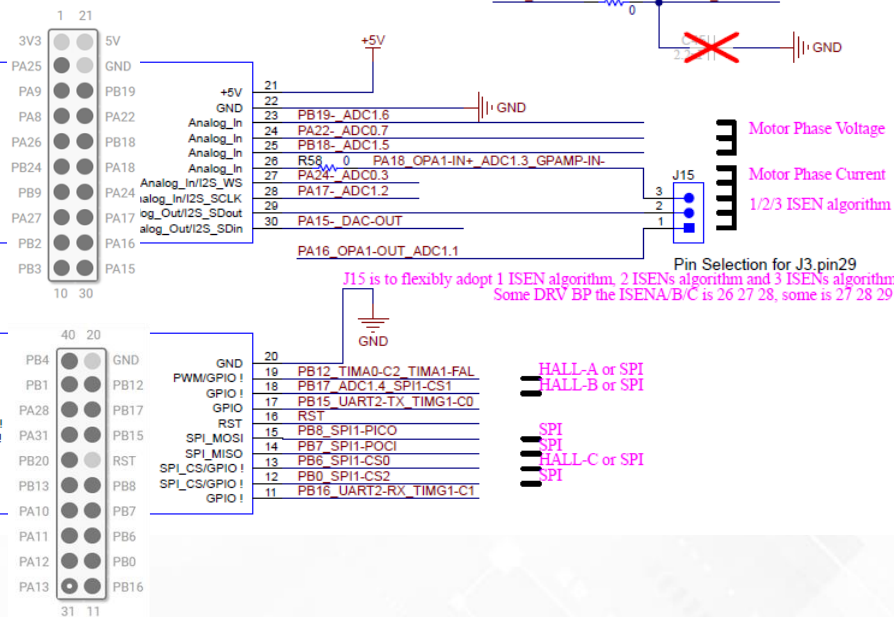
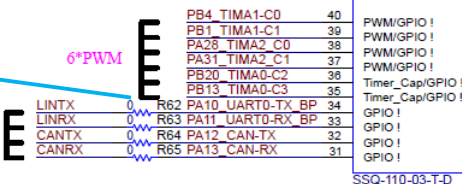


Pin Selection for J1.pin3

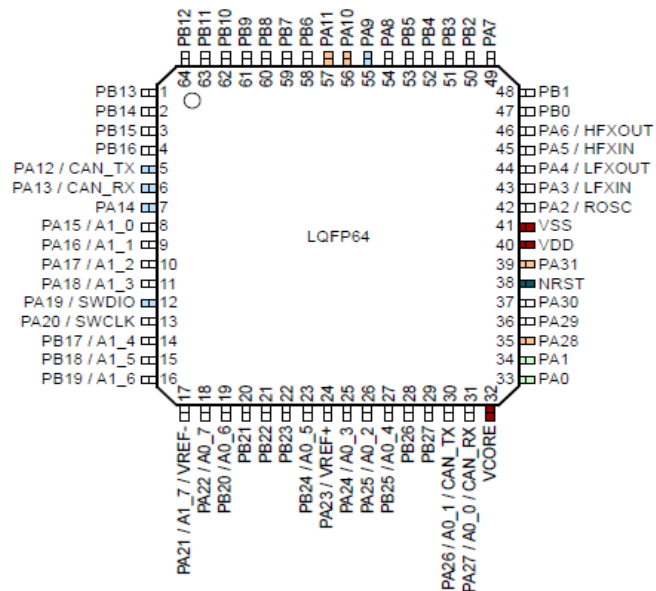


CAN/LIN

6*PWM



接口描述



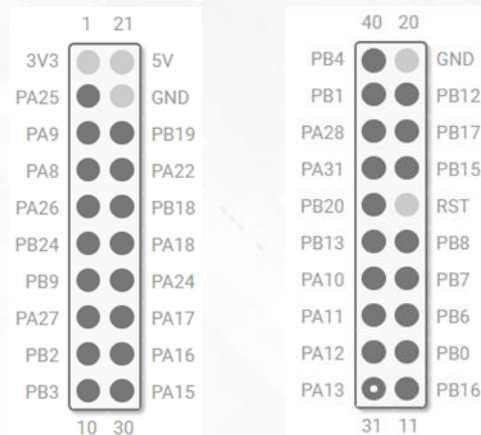
芯片共64个引脚:

3 个: VDD、VCORE、VSS

1 个: RST

32个: PA0-PA31

28个: PB0-PB27

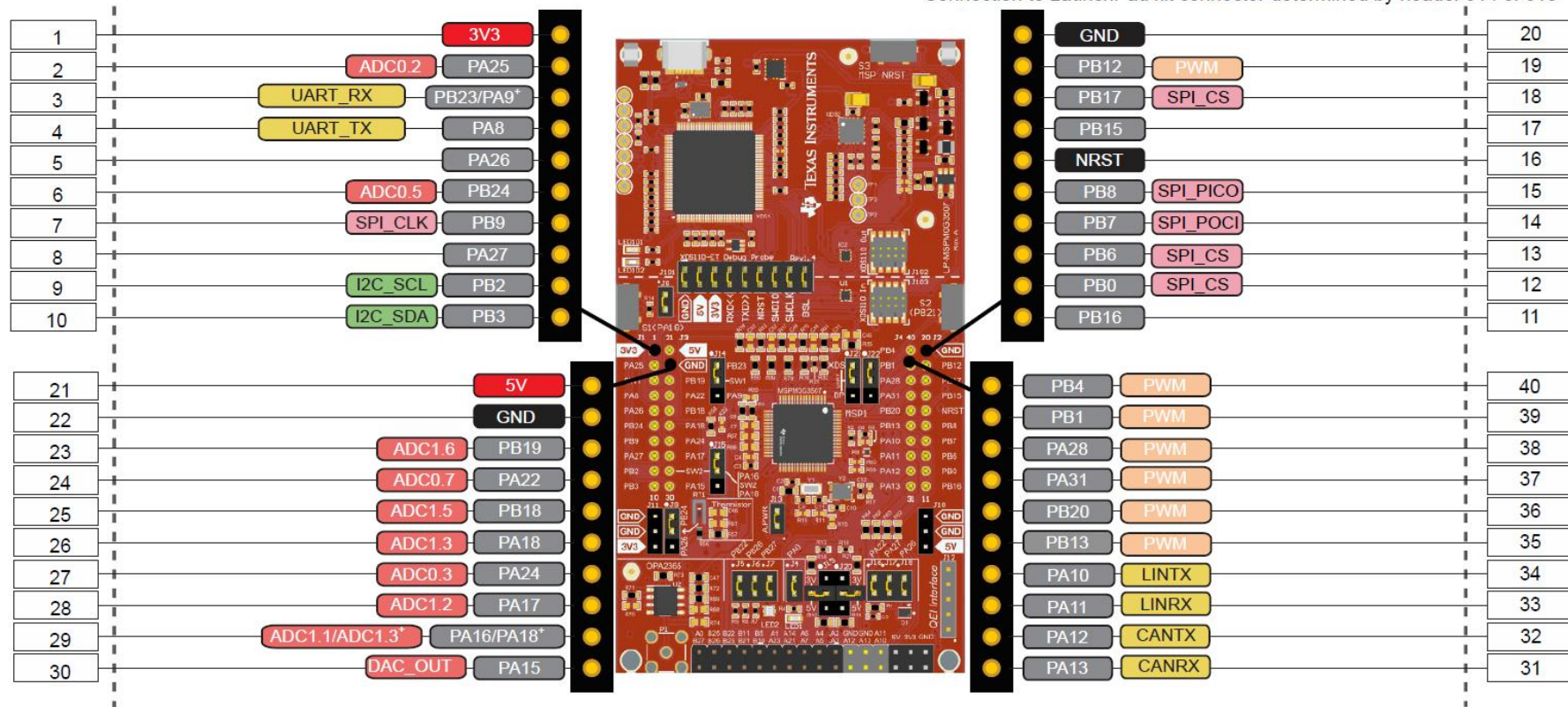


所有的IO引脚都引出来了

但是功能有复用，需参考具体电路

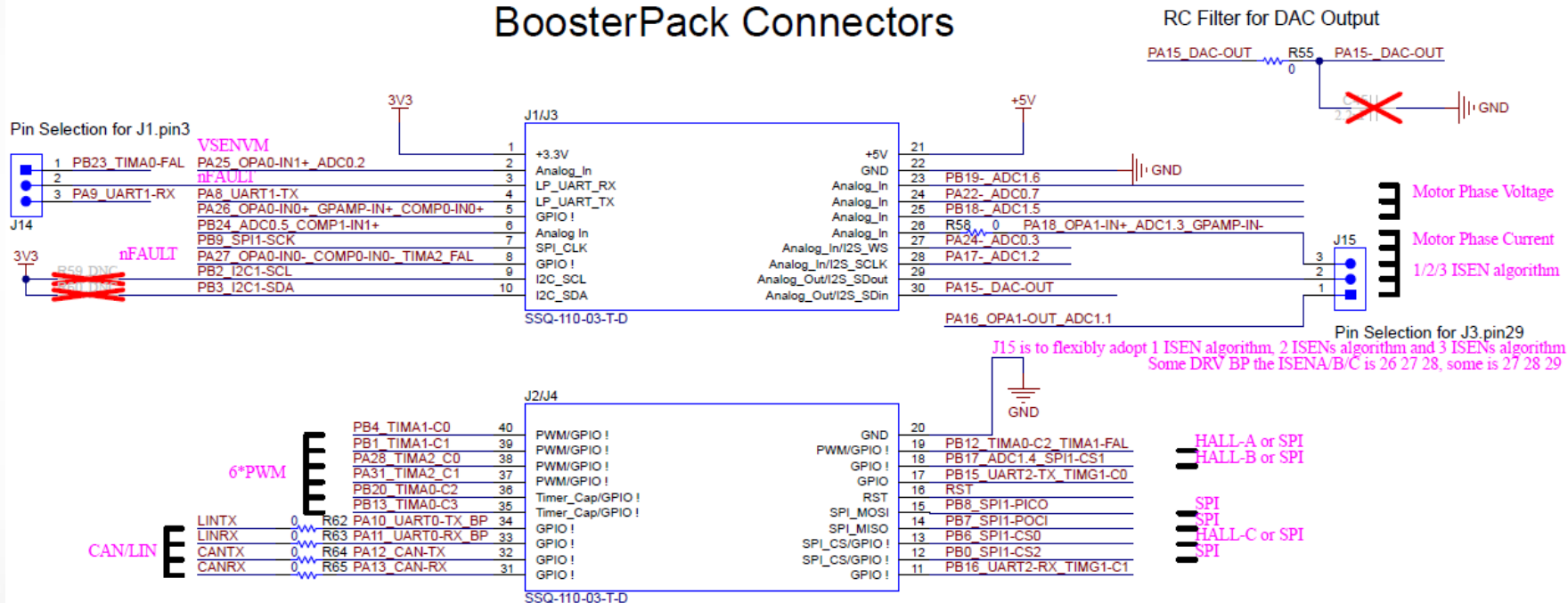
BoosterPack接口

* Connection to LaunchPad kit connector determined by header J14 or J15

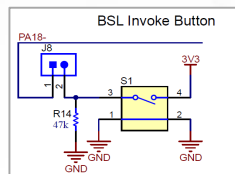
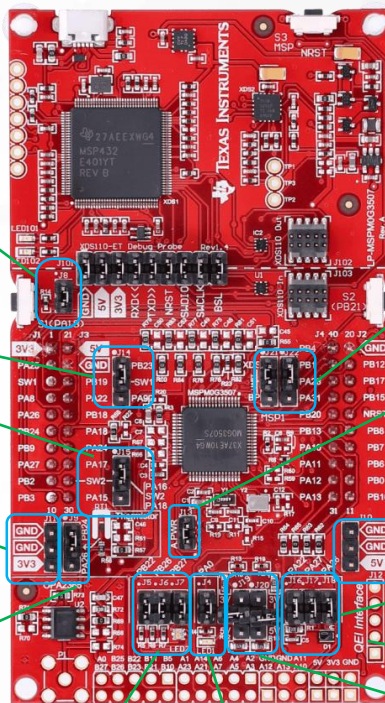


BoosterPack接口

BoosterPack Connectors

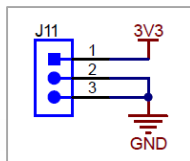


跳线配置说明



J8

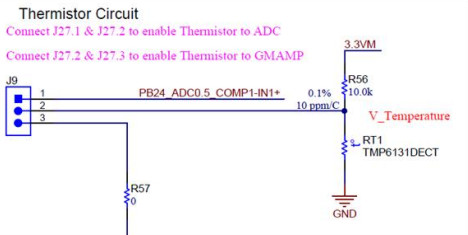
SW1和SW2引脚选择



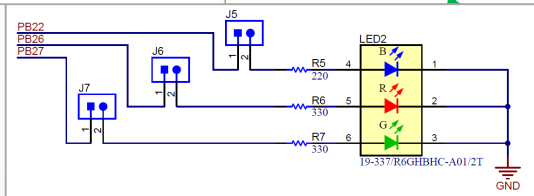
J14

J15

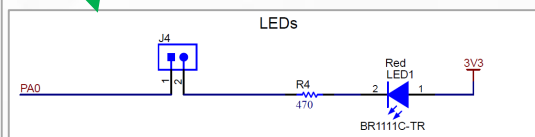
J11



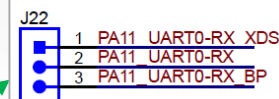
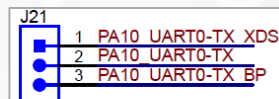
J9



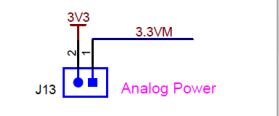
J5 J6 J7



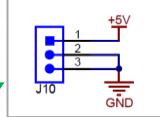
J4



J21 J22

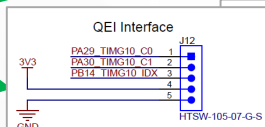


J13



J10

J16 J17 J18

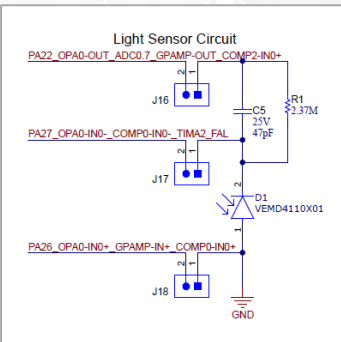


J12

J19 J20

芯片的uart引脚选择:

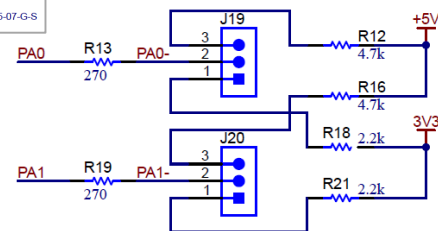
- 连到XDS110
- 连到BP排针



5V and 3.3V Pullup for Open-Drain IOs

2-1: Pullup to 3.3V

2-3: Pullup to 5V



谢谢!