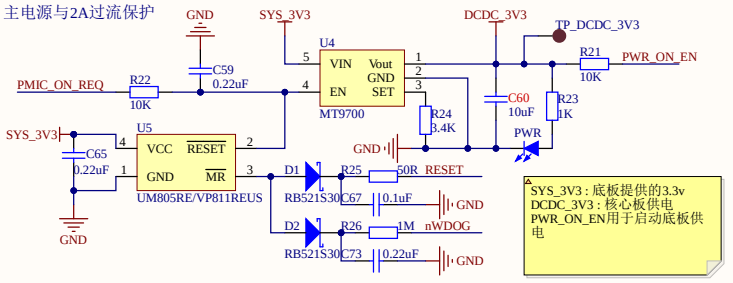
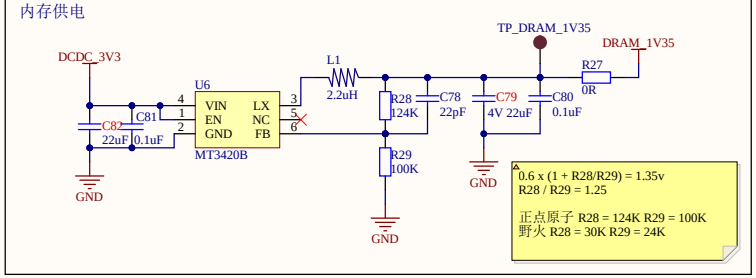
电源供电电路

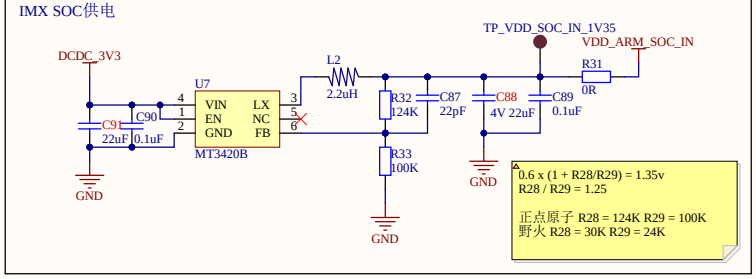
主电源与2A过流保护



内存供电



IMX SOC供电



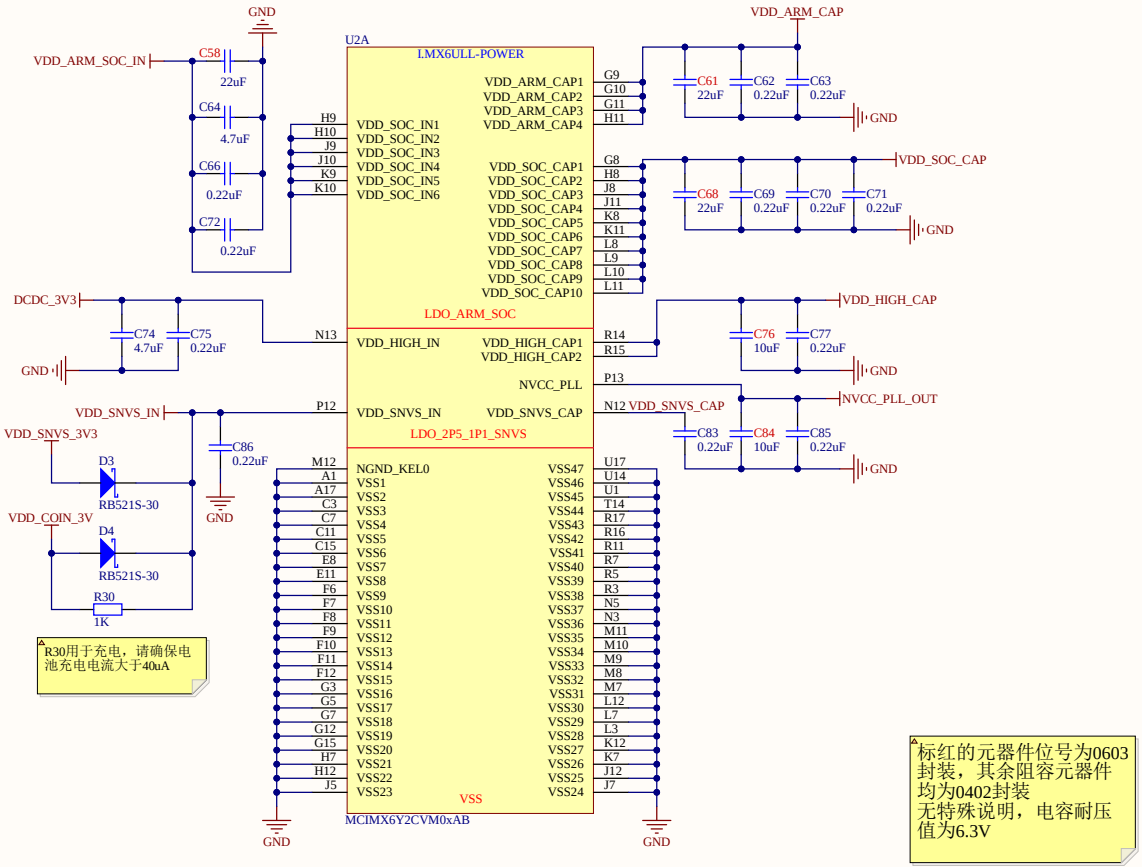
SD卡供电

NVCC_SD -- 3.3V

一般大卡为1.8V供电
小卡为3.3V供电

其他供电

MPU供电电路



IMX6ULL上电时序

1、VDD_SNVS_IN (VDD_SNVS_IN supply must be turned on before any other power supply)

2、VDD_HIGH_IN (VDD_HIGH_IN should be turned on before VDD_SOC_IN)

3、VDD_SOC_IN 与其他供电 (PWR_ON_EN 使能)

VDD_SNVS_IN ---------- SYS_3V3 || VDD_COIN_3V

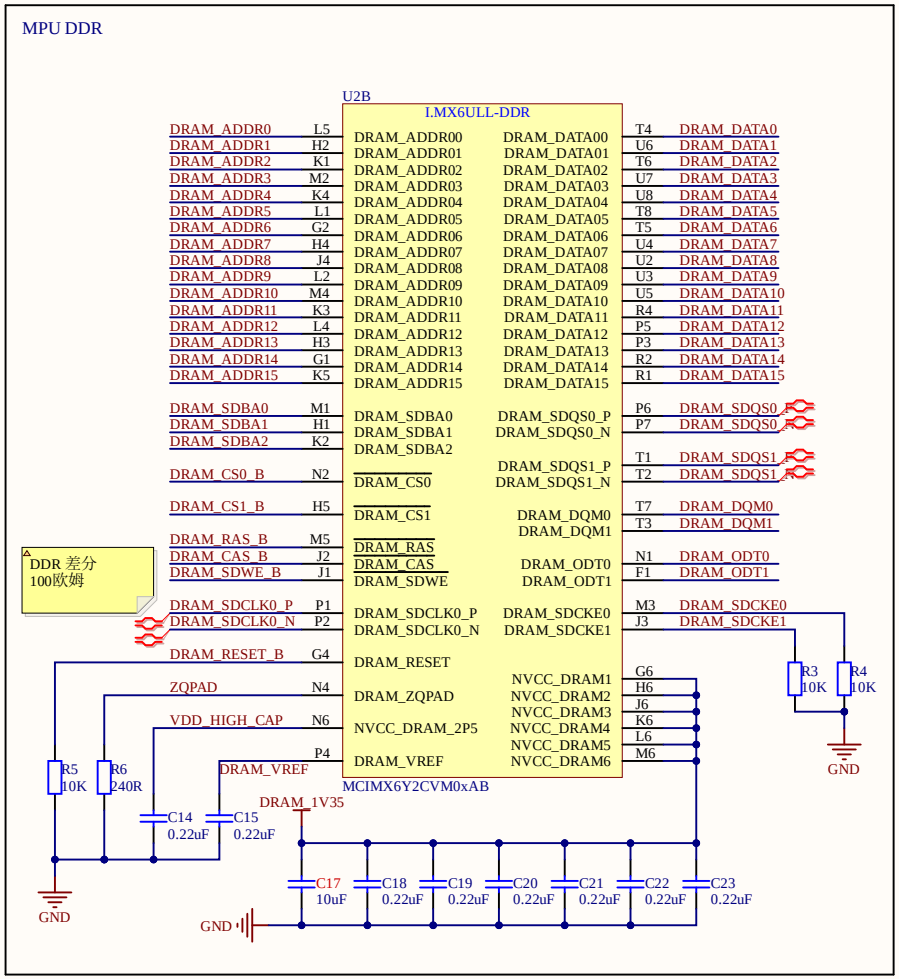
VDD_HIGH_IN ---------- DCDC_3V3

VDD_SOC_IN ---------- VDD_ARM_SOC_IN

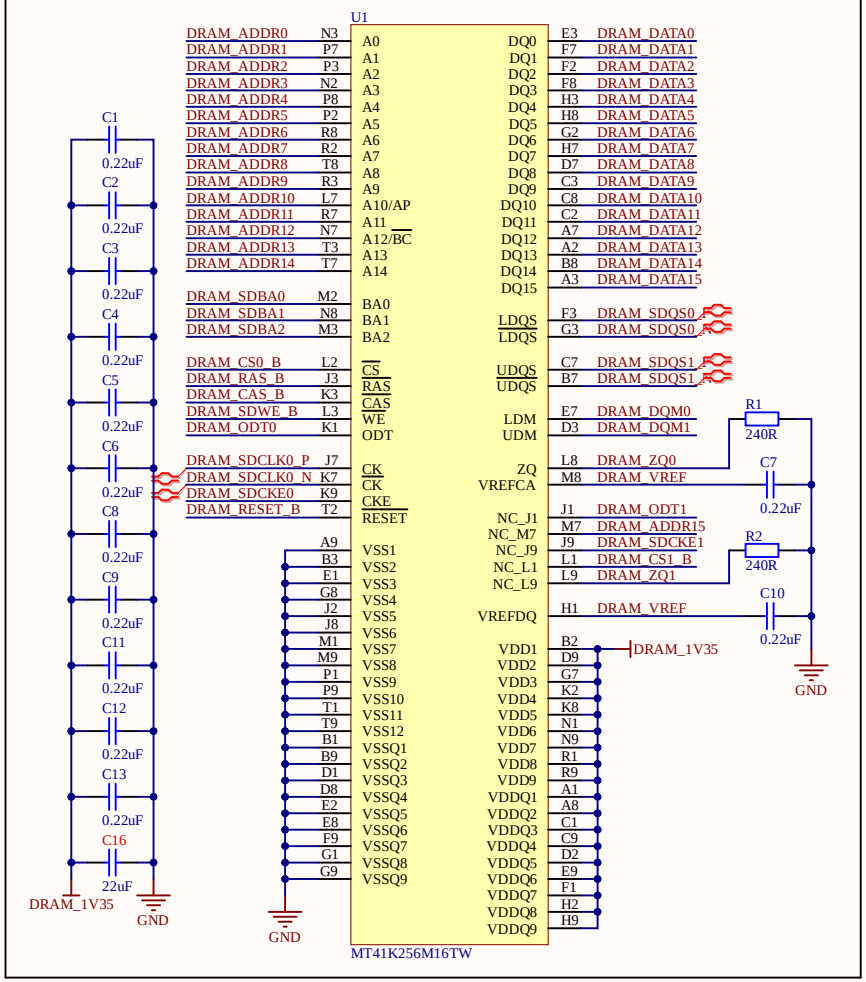
Title		
Size A3	Number	Revision
Date: 5/16/2023		Sheet of
File: E:\AltiumDesigner\..\Power.SchDoc		Drawn By:

DDR电路

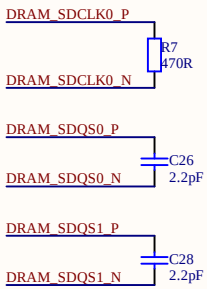
MPU DDR



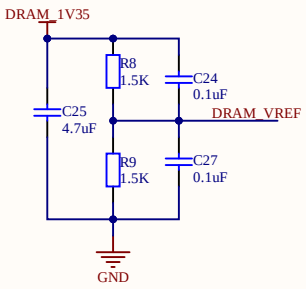
DDR_Device



CLK终端电阻 靠近DDR



DDR3参考电压



DDR布线规范

DDR需要分组等长处理 (电源、数据总线、其他)

数据线低八位 = (d0-d7、dqm0、dqs0_p/n)

数据线高八位 = (d8-d15、dqm1、dqs1_p/n)

其余分为一组等长处理

MPU 片上外设

信号完整性测试点

CLK1_N TP4

CLK1_P TP5

U2D

I.MX6ULL-LCD

NVCC_LCD E13

LCD_PCLK R18 10R A8

LCD_DE B8

LCD_HSYNC D9

LCD_VSYNC C9

nWDOG E9

LCD_RESET

LCD_DATA0 B9 LCD_DATA00

LCD_DATA1 A9 LCD_DATA01

LCD_DATA2 E10 LCD_DATA02

LCD_DATA3 D10 LCD_DATA03

LCD_DATA4 C10 LCD_DATA04

LCD_DATA5 B10 LCD_DATA05

LCD_DATA6 A10 LCD_DATA06

LCD_DATA7 D11 LCD_DATA07

LCD_DATA8 B11 LCD_DATA08

LCD_DATA9 A11 LCD_DATA09

LCD_DATA10 E12 LCD_DATA10

LCD_DATA11 D12 LCD_DATA11

LCD_DATA12 C12 LCD_DATA12

LCD_DATA13 B12 LCD_DATA13

LCD_DATA14 A12 LCD_DATA14

LCD_DATA15 D13 LCD_DATA15

LCD_DATA16 C13 LCD_DATA16

LCD_DATA17 B13 LCD_DATA17

LCD_DATA18 A13 LCD_DATA18

LCD_DATA19 D14 LCD_DATA19

LCD_DATA20 C14 LCD_DATA20

LCD_DATA21 B14 LCD_DATA21

LCD_DATA22 A14 LCD_DATA22

LCD_DATA23 B16 LCD_DATA23

I.MX6ULL-NET

NVCC_ENET F13

ENET1_TX_DATA0/UART5_CTS/CSI_DATA19/FLEXCAN2_RX/GPIO2_IO03/KPP_COL01/USDHC2_VSELECT

ENET1_TX_DATA1/UART6_CTS/PWM5_OUT/CSI_DATA20/ENET2_MDIO/GPIO2_IO04/KPP_ROW02

ENET1_TX_EN/UART6_RTS/PWM6_OUT/CSI_DATA21/ENET2_MDC/GPIO2_IO05/KPP_COL02

ENET1_TX_CLK/UART7_CTS/PWM7_OUT/CSI_DATA22/ENET1_REF_CLK1/GPIO2_IO06/KPP_ROW03/GPT1_CLK

ENET1_RX_DATA0/UART4_RTS/PWM1_OUT/CSI_DATA16/FLEXCAN1_TX/GPIO2_IO00/KPP_ROW00

ENET1_RX_DATA1/UART4_CTS/PWM2_OUT/CSI_DATA17/FLEXCAN1_RX/GPIO2_IO01/KPP_COL00

ENET1_RX_ER/UART7_RTS/PWM8_OUT/CSI_DATA23/EIM_CRE/GPIO2_IO07/KPP_COL03/GPT1_CAPTURE2

ENET1_RX_EN/UART5_RTS/CSI_DATA18/FLEXCAN2_TX/GPIO2_IO02/KPP_ROW01/USDHC1_VSELECT

ENET2_TX_DATA0/UART7_RX/I2C4_SDA/EIM_EB_B02/GPIO2_IO11/KPP_COL05

ENET2_TX_DATA1/UART8_TX/ECSPI4_SCLK/EIM_EB_B03/GPIO2_IO12/KPP_ROW06/USB_OTG2_PWR

ENET2_TX_EN/UART8_RX/ECSPI4_MOSI/EIM_ACLK_FREERUN/GPIO2_IO13/KPP_COL06/USB_OTG2_OC

ENET2_TX_CLK/UART8_CTS/ECSPI4_MISO/ENET2_REF_CLK2/GPIO2_IO14/KPP_ROW07/ANATOP_OTG2_ID

ENET2_RX_DATA0/UART6_EX/I2C3_SCL/ENET1_MDIO/GPIO2_IO08/KPP_ROW04/USB_OTG1_PWR

ENET2_RX_DATA1/UART6_RX/I2C3_SDA/ENET1_MDC/GPIO2_IO09/KPP_COL04/USB_OTG1_OC

ENET2_RX_ER/UART8_RTS/ECSPI4_SS0/EIM_ADDR25/GPIO2_IO15/KPP_COL07

ENET2_RX_EN/UART7_TX/I2C4_SCL/EIM_ADDR26/GPIO2_IO10/KPP_ROW05/ENET1_REF_LK_25M

I.MX6ULL-GPIO

NVCC_GPIO J13

GPIO1_IO00/I2C2_SCL/GPT1_CAPTURE1/ANATOP_OTG1_ID/ENET1_REF_CLK1/MQS_RGHT

GPIO1_IO01/I2C2_SDA/GPT1_COMPARE1/USB_OTG1_OC/ENET2_REF_CLK2/MQS_LEFT

GPIO1_IO02/I2C1_SCL/GPT1_COMPARE2/USB_OTG2_PWR/ENET1_REF_CLK_25M/USDHC1_WP/UART1_TX

GPIO1_IO03/I2C1_SDA/GPT1_COMPARE3/USB_OTG2_OC/USDHC1_SD_B/UART1_RX

GPIO1_IO04/ENET1_REF_CLK1/PWM3_OUT/USB_OTG1_PWR/USDHC1_RESET/UART5_TX

GPIO1_IO05/ENET2_REF_CLK2/PWM4_OUT/ANATOP_OTG2_ID/CSI_FIELD/USDHC1_VSELECT/UART5_RX

GPIO1_IO06/ENET1_2_MDIO/USB_OTG_PWR_WAKE/CSI_MCLK/USDCH2_WP/UART1_CTS

GPIO1_IO07/ENET1_2_MDC/USB_OTG_HOST_MODE/CSI_PIXCLK/USDHC2_CD_B/UART1_RTS

GPIO1_IO08/PWM1_OUT/SPDIF_OUT/CSI_VSYNC/USDHC2_VSELECT/GPIO1_IO08/UART5_RTS

GPIO1_IO09/PWM2_OUT/SPDIF_IN/CSI_HSYNC/USDHC1_2_RESET_B/GPIO1_IO09/UART5_CTS

MCIMX6Y2CVM0xAB

C53 18pF C55 0.22uF C56 0.22uF C57 18pF

USB_OTG1_ID K13

USB_OTG1_OC L15

USB_OTG2_PWR L14

USB_OTG2_OC L17

USB_OTG1_PWR M16

GPIO1_IO05 M17

ENET_MDIO K17

ENET_MDC L16

GPIO1_IO08 N17

SD1_nRST M15

POR是芯片复位引脚，但NXP没有使用该引脚，NXP的复位方式是断电复位

U2C

I.MX6ULL-CONTROL

ONOFF R8

POR_B P8

BOOT_MODE0 T10

BOOT_MODE1 U10

N7

TEST_MODE

SNVS_TAMPER0 R10

SNVS_TAMPER1 R9

SNVS_TAMPER2 P11

SNVS_TAMPER3 P10

SNVS_TAMPER4 P9

SNVS_TAMPER5 N8

SNVS_TAMPER6 N11

SNVS_TAMPER7 N10

SNVS_TAMPER8 N9

SNVS_TAMPER9 R6

ONOFF

POR

JTAG_TCK/GPT2_COMPARE2/SAI2_RX_DATA/PWM7_OUT/GPIO1_IO14/SIM2_POWER_FAIL

JTAG_TMS/GPT2_CAPTURE1/SAI2_MCLK/CCM_CLKO1/CCM_WAIT/GPIO1_IO11/SDMA_EXT_EVENT01/EPIT1_OUT

JTAG_TDI/GPT2_COMPARE1/SAI2_TX_BCLK/PWM6_OUT/GPIO1_IO13/MQS_LEFT/SIM1_POWER_FAIL

BOOT_MODE0/GPIO5_IO10

BOOT_MODE1/GPIO5_IO11

JTAG_TDO/GPT2_CAPTURE2/SAI2_TX_SYNC/CCM_CLKO2/CCM_STOP/GPIO1_IO12/MQS_RIGHT/EPIT2_OUT

JTAG_TRST/GPT2_COMPARE3/SAI2_TX_DATA/PWM8_OUT/GPIO1_IO15/CAAM_RNG_OSC_OBS

TEST_MODE

JTAG_MOD/GPT2_CLK/SPDIF_OUT/ENET1_REF_CLK_25M/CCM_PMIC_RDY/GPIO1_IO10/SDMA_EXT_EVENT00

SNVS_TAMPER0/GPIO5_IO00

SNVS_TAMPER1/GPIO5_IO01

SNVS_TAMPER2/GPIO5_IO02

SNVS_TAMPER3/GPIO5_IO03

SNVS_TAMPER4/GPIO5_IO04

SNVS_TAMPER5/GPIO5_IO05

SNVS_TAMPER6/GPIO5_IO06

SNVS_TAMPER7/GPIO5_IO07

SNVS_TAMPER8/GPIO5_IO08

SNVS_TAMPER9/GPIO5_IO09

UART1_TX/ENET1_RDATA02/I2C3_SCL/CSI_DATA02/GPT1_COMPARE1/GPIO1_IO16/SPDIF_OUT/UART5_TX

UART1_5_RX/ENET1_RDATA03/I2C3_SDA/CSI_DATA03/GPT1_CLK/GPIO1_IO17/SPDIF_IN

UART1_5_RTS/ENET1_TX_ER/USDHC1_2_CD_B/CSI_DATA05/ENET2_1588_EVENT1_OUT/GPIO1_IO19

UART1_5_CTS/ENET1_RX_CLK/USDHC1_2_WP/CSI_DATA04/ENET2_1588_EVENT1_IN/GPIO1_IO18

CCM_CLK1_N P16

CCM_CLK1_P P17

NVCC_UART H13

UART1_TXD K14

UART1_RXD K16

UART1_RTS J14

UART1_CTS K15

UART2_TXD J17

UART2_RXD J16

UART2_RTS H14

UART2_CTS J15

UART3_TXD H17

UART3_RXD H16

UART3_RTS G14

UART3_CTS H15

UART4_TXD G17

UART4_RXD G16

UART5_TXD F17

UART5_RXD G13

CCM_PMIC_STBY_REQ T9

SNVS_PMIC_ON_REQ U9

XTALI T16

XTALO T17

RTC_XTALI T11

RTC_XTALO U11

GPANAIO R13

UART2_TX/ENET1_TDATA02/I2C4_SCL/CSI_DATA06/GPT1_CAPTURE1/GPIO1_IO20/ECSPI3_CS0

UART2_RX/ENET1_TDATA03/I2C4_SDA/CSI_DATA07/GPT1_CAPTURE2/GPIO1_IO21/ECSPI3_SCLK

UART2_RTS/ENET1_COL/FLEXCAN2_RX/CSI_DATA09/GPT1_COMPARE3/GPIO1_IO23/ECSPI3_MISO

UART2_CTS/ENET1_CRS/FLEXCAN2_TX/CSI_DATA08/GPT1_COMPARE2/GPIO1_IO22/ECSPI3_MOSI

UART3_TX/ENET2_RDATA02/CSI_DATA01/UART2_CTS_B/GPIO1_IO24/ANATOP_OTG1_ID

UART3_RX/ENET2_RDATA03/CSI_DATA00/UART2_RTS/GPIO1_IO25/EPIT1_OUT

UART3_RTS/ENET2_TX_ER/FLEXCAN1_RX/CSI_DATA11/ENET1_1588_EVENT1_OUT/GPIO1_IO27/WDOG1_WDOGB

UART3_CTS/ENET2_RX_CLK/FLEXCAN1_TX/CSI_DATA10/ENET1_1588_EVENT1_IN/GPIO1_IO26/EPIT2_OUT

UART4_TX/ENET2_TDATA02/I2C1_SCL/CSI_DATA12/CSU_ALARM_AUT02/GPIO1_IO28/ECSPI2_SCLK

UART4_RX/ENET2_RX/ENET2_TDATA03/I2C1_SDA/CSI_DATA13/CSU_ALARM_AUT01/GPIO1_IO29/EPDC_PWRCTRL01

UART5_TX/ENET2_CRS/I2C2_SCL/CSI_DATA14/CSU_ALARM_AUT00/GPIO1_IO30/ECSPI2_MOSI/EPDC_PWRCTRL02

UART5_RX/ENET2_COL/I2C2_SDA/CSI_DATA15/CSU_INT_DEB/GPIO1_IO31/ECSPI2_MISO/EPDC_PWRCTRL03

I.MX6ULL-USB

USB_OTG1_VBUS T12

VDD_USB_CAP R12

USB_OTG2_VBUS U12

USB_OTG1_DN

USB_OTG1_DP

USB_OTG1_CHD

USB_OTG2_DN

USB_OTG2_DP

USB_OTG1_D_N T15

USB_OTG1_D_P U15

U16

USB_OTG2_D_N T13

USB_OTG2_D_P U13

USB 差分90欧姆

I.MX6ULL-SD1\ADC

NVCC_SD1 C4

USDHC1_CLK/GPT2_COMPARE2/SAI2_MCLK/SPDIF_IN/EIM_ADDR20/GPIO2_IO17/USB_OTG1_OC

USDHC1_CMD/GPT2_COMPARE1/SAI2_RX_SYNC/SPDIF_OUT/EIM_ADDR19/GPIO2_IO16/SDMA_EXT_ENVENT00/USB_OTG1_PWR

USDHC1_DATA0/GPT2_COMPARE3/SAI2_TX_SYNC/FLEXCAN1_TX/EIM_ADDR21/GPIO2_IO18/ANATOP_OTRG1_ID

USDHC1_DATA1/GPT2_CLK/SAI2_TX_BLCK/FLEXCAN1_RX/EIM_ADDR22/GPIO2_IO19/USB_OTG_PWR

USDHC1_DATA2/GPT2_CAPTURE1/SAI2_RX_DATA/FLEXCAN2_TX/EIM_ADDR23/GPIO2_IO20/CCM_CLKO1/USB_OTG2_OC

USDHC1_DATA3/GPT2_CAPTURE2/SAI2_TX_DATA/FLEXCAN2_RX/EIM_ADDR24/GPIO2_IO21/CCM_CLKO2/ANATOP_OTG2_ID

SD1_CLK R15 10R C1

SD1_CMD C2

SD1_DATA0 B3

SD1_DATA1 B2

SD1_DATA2 B1

SD1_DATA3 A2

ADC_VREFH M13

VDDA_ADC_3P3 L13

MCIMX6Y2CVM0xAB

C41 18pF C42 18pF X1 24MHz R14 1M C43 18pF C44 18pF Y1 32.768KHz C45 1uF C46 10uF C47 0.1uF C48 1uF C49 0.22uF C50 18pF R16 10K R17 10K C51 1uF C52 0.22uF R13 10K C40 0.22uF

PMIC_ON_REQ

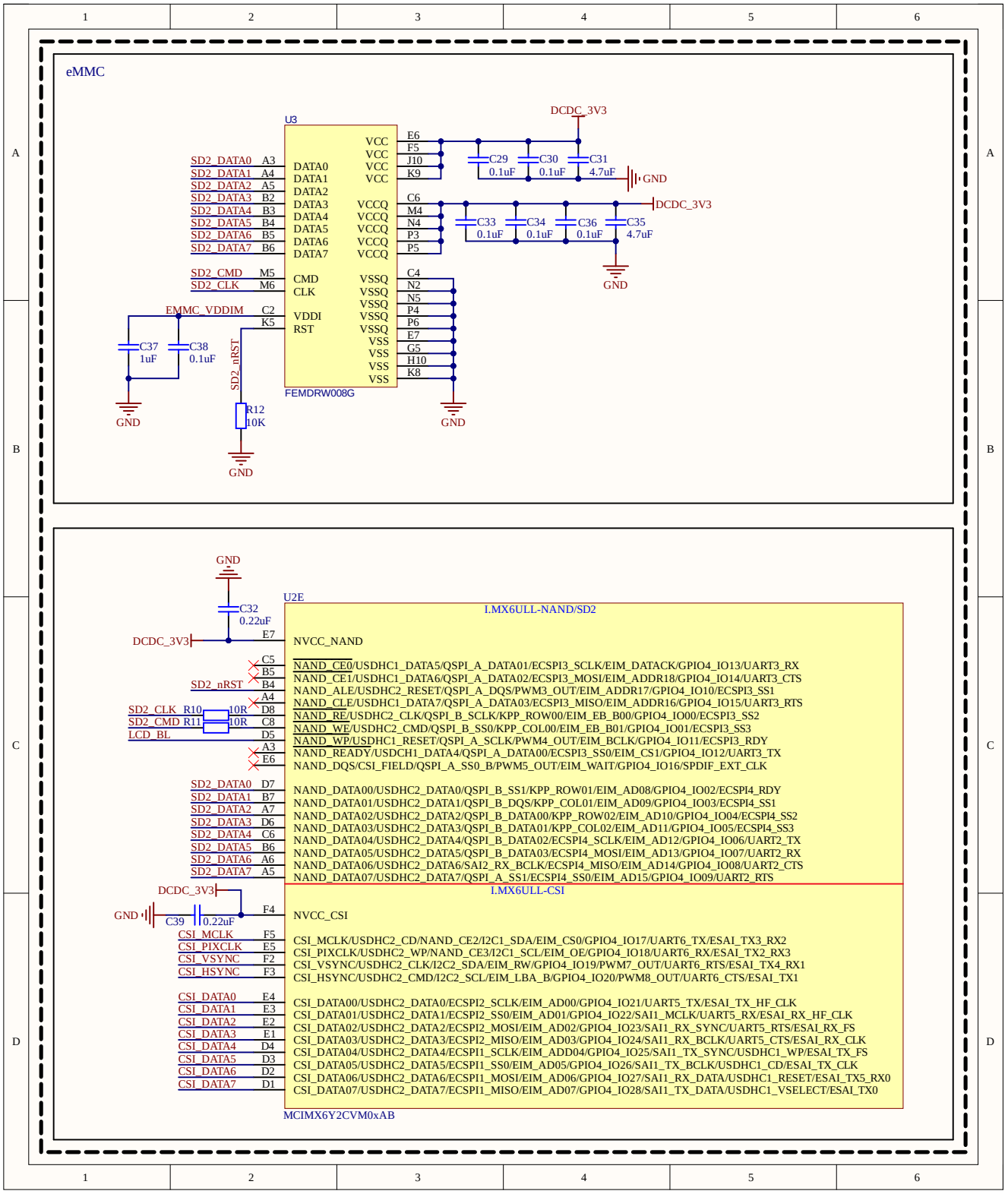
USB_OTG1_VBUS

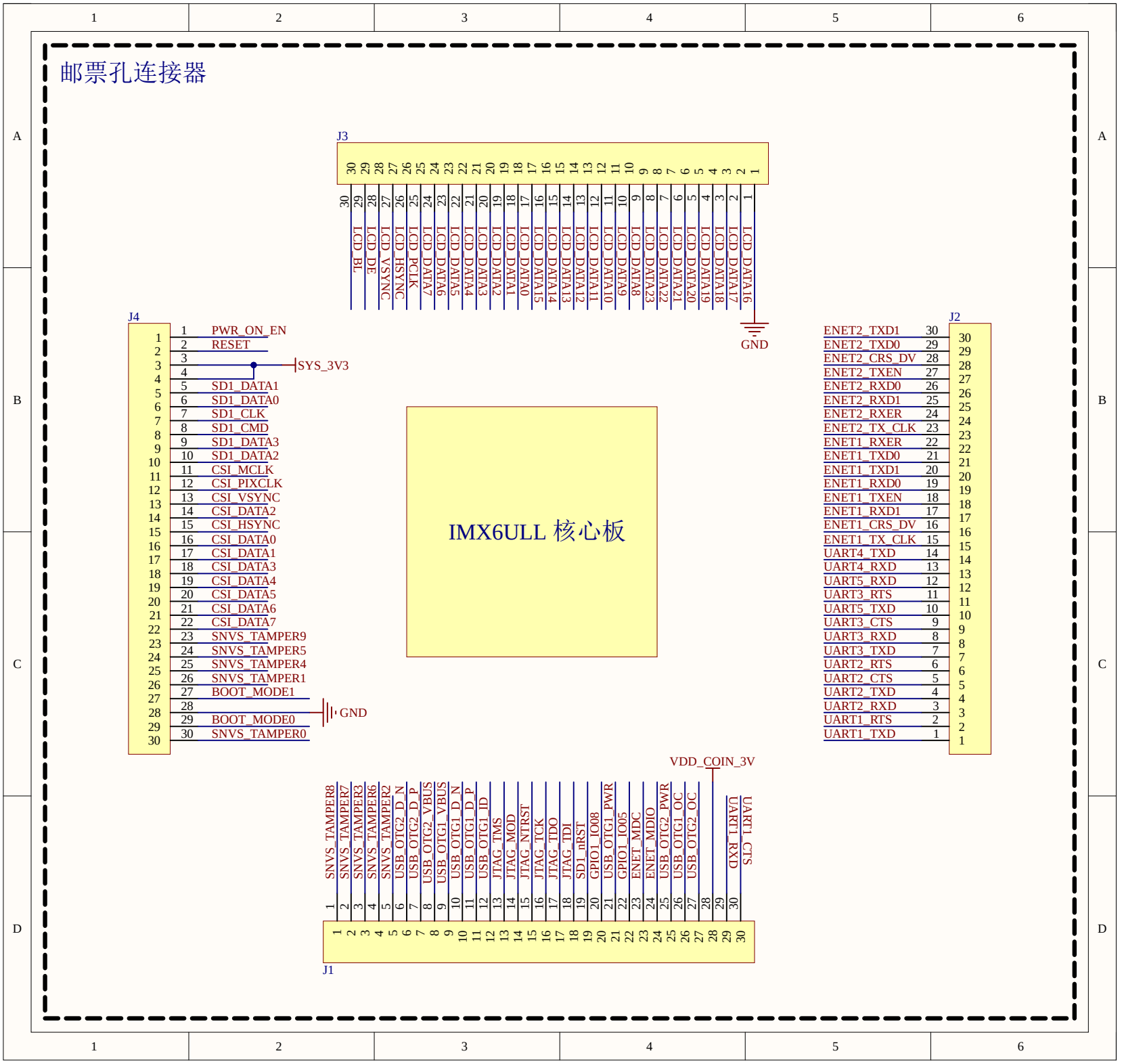
USB_OTG2_VBUS

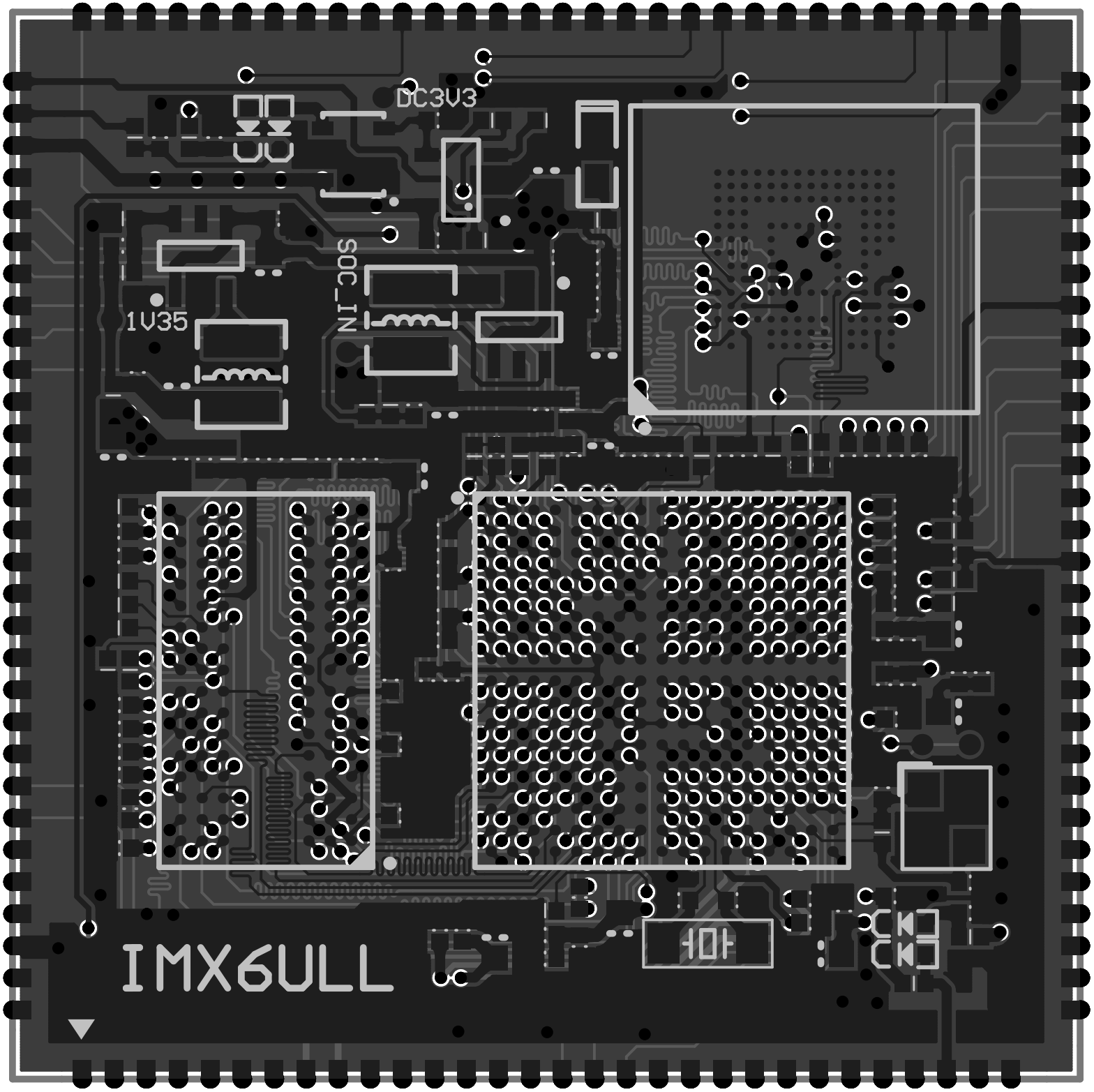
VDD_USB

DCDC_3V3

GND


eMMC
U3
DCDC_3V3
SD2_DATA0 A3
SD2_DATA1 A4
SD2_DATA2 A5
SD2_DATA3 B2
SD2_DATA4 B3
SD2_DATA5 B4
SD2_DATA6 B5
SD2_DATA7 B6
SD2_CMD M5
SD2_CLK M6
EMMC_VDDIM C2
K5
DATA0
DATA1
DATA2
DATA3
DATA4
DATA5
DATA6
DATA7
CMD
CLK
VDDI
RST
VCC
VCCQ
VSSQ
VSS
E6
F5
J10
K9
C6
M4
N4
P3
P5
C4
N2
N5
P4
P6
E7
G5
H10
K8
C29 0.1uF
C30 0.1uF
C31 4.7uF
C33 0.1uF
C34 0.1uF
C36 0.1uF
C35 4.7uF
GND
C37 1uF
C38 0.1uF
SD2_nRST
R12 10K
FEMDRW008G
U2E
I.MX6ULL-NAND/SD2
NVCC_NAND
C32 0.22uF
NAND_CE0/USDHC1_DATA5/QSPI_A_DATA01/ECSPI3_SCLK/EIM_DATACK/GPIO4_IO13/UART3_RX
NAND_CE1/USDHC1_DATA6/QSPI_A_DATA02/ECSPI3_MOSI/EIM_ADDR18/GPIO4_IO14/UART3_CTS
NAND_ALE/USDHC2_RESET/QSPI_A_DQS/PWM3_OUT/EIM_ADDR17/GPIO4_IO10/ECSPI3_SS1
NAND_CLE/USDHC1_DATA7/QSPI_A_DATA03/ECSPI3_MISO/EIM_ADDR16/GPIO4_IO15/UART3_RTS
NAND_RE/USDHC2_CLK/QSPI_B_SCLK/KPP_ROW00/EIM_EB_B00/GPIO4_IO00/ECSPI3_SS2
NAND_WE/USDHC2_CMD/QSPI_B_SS0/KPP_COL00/EIM_EB_B01/GPIO4_IO01/ECSPI3_SS3
NAND_WP/USDHC1_RESET/QSPI_A_SCLK/PWM4_OUT/EIM_BCLK/GPIO4_IO11/ECSPI3_RDY
NAND_READY/USDCH1_DATA4/QSPI_A_DATA00/ECSPI3_SS0/EIM_CS1/GPIO4_IO12/UART3_TX
NAND_DQS/CSI_FIELD/QSPI_A_SS0_B/PWM5_OUT/EIM_WAIT/GPIO4_IO16/SPDIF_EXT_CLK
NAND_DATA00/USDHC2_DATA0/QSPI_B_SS1/KPP_ROW01/EIM_AD08/GPIO4_IO02/ECSPI4_RDY
NAND_DATA01/USDHC2_DATA1/QSPI_B_DQS/KPP_COL01/EIM_AD09/GPIO4_IO03/ECSPI4_SS1
NAND_DATA02/USDHC2_DATA2/QSPI_B_DATA00/KPP_ROW02/EIM_AD10/GPIO4_IO04/ECSPI4_SS2
NAND_DATA03/USDHC2_DATA3/QSPI_B_DATA01/KPP_COL02/EIM_AD11/GPIO4_IO05/ECSPI4_SS3
NAND_DATA04/USDHC2_DATA4/QSPI_B_DATA02/ECSPI4_SCLK/EIM_AD12/GPIO4_IO06/UART2_TX
NAND_DATA05/USDHC2_DATA5/QSPI_B_DATA03/ECSPI4_MOSI/EIM_AD13/GPIO4_IO07/UART2_RX
NAND_DATA06/USDHC2_DATA6/SAI2_RX_BCLK/ECSPI4_MISO/EIM_AD14/GPIO4_IO08/UART2_CTS
NAND_DATA07/USDHC2_DATA7/QSPI_A_SS1/ECSPI4_SS0/EIM_AD15/GPIO4_IO09/UART2_RTS
SD2_nRST
SD2_CLK R10 10R
SD2_CMD R11 10R
LCD_BL
SD2_DATA0
SD2_DATA1
SD2_DATA2
SD2_DATA3
SD2_DATA4
SD2_DATA5
SD2_DATA6
SD2_DATA7
I.MX6ULL-CSI
NVCC_CSI
C39 0.22uF
CSI_MCLK/USDHC2_CD/NAND_CE2/I2C1_SDA/EIM_CS0/GPIO4_IO17/UART6_TX/ESAI_TX3_RX2
CSI_PIXCLK/USDHC2_WP/NAND_CE3/I2C1_SCL/EIM_OE/GPIO4_IO18/UART6_RX/ESAI_TX2_RX3
CSI_VSYNC/USDHC2_CLK/I2C2_SDA/EIM_RW/GPIO4_IO19/PWM7_OUT/UART6_RTS/ESAI_TX4_RX1
CSI_HSYNC/USDHC2_CMD/I2C2_SCL/EIM_LBA_B/GPIO4_IO20/PWM8_OUT/UART6_CTS/ESAI_TX1
CSI_MCLK
CSI_PIXCLK
CSI_VSYNC
CSI_HSYNC
CSI_DATA00/USDHC2_DATA0/ECSPI2_SCLK/EIM_AD00/GPIO4_IO21/UART5_TX/ESAI_TX_HF_CLK
CSI_DATA01/USDHC2_DATA1/ECSPI2_SS0/EIM_AD01/GPIO4_IO22/SAI1_MCLK/UART5_RX/ESAI_RX_HF_CLK
CSI_DATA02/USDHC2_DATA2/ECSPI2_MOSI/EIM_AD02/GPIO4_IO23/SAI1_RX_SYNC/UART5_RTS/ESAI_RX_FS
CSI_DATA03/USDHC2_DATA3/ECSPI2_MISO/EIM_AD03/GPIO4_IO24/SAI1_RX_BCLK/UART5_CTS/ESAI_RX_CLK
CSI_DATA04/USDHC2_DATA4/ECSPI1_SCLK/EIM_ADD04/GPIO4_IO25/SAI1_TX_SYNC/USDHC1_WP/ESAI_TX_FS
CSI_DATA05/USDHC2_DATA5/ECSPI1_SS0/EIM_AD05/GPIO4_IO26/SAI1_TX_BCLK/USDHC1_CD/ESAI_TX_CLK
CSI_DATA06/USDHC2_DATA6/ECSPI1_MOSI/EIM_AD06/GPIO4_IO27/SAI1_RX_DATA/USDHC1_RESET/ESAI_TX5_RX0
CSI_DATA07/USDHC2_DATA7/ECSPI1_MISO/EIM_AD07/GPIO4_IO28/SAI1_TX_DATA/USDHC1_VSELECT/ESAI_TX0
MCIMX6Y2CVM0xAB


邮票孔连接器
J3
30 LCD_BL
29 LCD_DE
28 LCD_VSYNC
27 LCD_HSYNC
26 LCD_PCLK
25 LCD_DATA7
24 LCD_DATA6
23 LCD_DATA5
22 LCD_DATA4
21 LCD_DATA3
20 LCD_DATA2
19 LCD_DATA1
18 LCD_DATA0
17 LCD_DATA15
16 LCD_DATA14
15 LCD_DATA13
14 LCD_DATA12
13 LCD_DATA11
12 LCD_DATA10
11 LCD_DATA9
10 LCD_DATA8
9 LCD_DATA23
8 LCD_DATA22
7 LCD_DATA21
6 LCD_DATA20
5 LCD_DATA19
4 LCD_DATA18
3 LCD_DATA17
2 LCD_DATA16
1
GND
J4
1 PWR_ON_EN
2 RESET
3
4 SYS_3V3
5 SD1_DATA1
6 SD1_DATA0
7 SD1_CLK
8 SD1_CMD
9 SD1_DATA3
10 SD1_DATA2
11 CSI_MCLK
12 CSI_PIXCLK
13 CSI_VSYNC
14 CSI_DATA2
15 CSI_HSYNC
16 CSI_DATA0
17 CSI_DATA1
18 CSI_DATA3
19 CSI_DATA4
20 CSI_DATA5
21 CSI_DATA6
22 CSI_DATA7
23 SNVS_TAMPER9
24 SNVS_TAMPER5
25 SNVS_TAMPER4
26 SNVS_TAMPER1
27 BOOT_MODE1
28
29 BOOT_MODE0
30 SNVS_TAMPER0
GND
IMX6ULL 核心板
J2
ENET2_TXD1 30
ENET2_TXD0 29
ENET2_CRS_DV 28
ENET2_TXEN 27
ENET2_RXD0 26
ENET2_RXD1 25
ENET2_RXER 24
ENET2_TX_CLK 23
ENET1_RXER 22
ENET1_TXD0 21
ENET1_TXD1 20
ENET1_RXD0 19
ENET1_TXEN 18
ENET1_RXD1 17
ENET1_CRS_DV 16
ENET1_TX_CLK 15
UART4_TXD 14
UART4_RXD 13
UART5_RXD 12
UART3_RTS 11
UART5_TXD 10
UART3_CTS 9
UART3_RXD 8
UART3_TXD 7
UART2_RTS 6
UART2_CTS 5
UART2_TXD 4
UART2_RXD 3
UART1_RTS 2
UART1_TXD 1
VDD_COIN_3V
1 SNVS_TAMPER8
2 SNVS_TAMPER7
3 SNVS_TAMPER3
4 SNVS_TAMPER6
5 SNVS_TAMPER2
6 USB_OTG2_D_N
7 USB_OTG2_D_P
8 USB_OTG2_VBUS
9 USB_OTG1_VBUS
10 USB_OTG1_D_N
11 USB_OTG1_D_P
12 USB_OTG1_ID
13 JTAG_TMS
14 JTAG_MOD
15 JTAG_NTRST
16 JTAG_TCK
17 JTAG_TDO
18 JTAG_TDI
19 SD1_nRST
20 GPIO1_IO08
21 USB_OTG1_PWR
22 GPIO1_IO05
23 ENET_MDC
24 ENET_MDIO
25 USB_OTG2_PWR
26 USB_OTG1_OC
27 USB_OTG2_OC
28
29 UART1_RXD
30 UART1_CTS
J1


DC3V3
1V35
SOC_IN
IMX6ULL

Board Stack Report