LI, YUXUAN

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EDUCATION BACKGROUND

National Chiao Tung University, Hsinchu City   09/2018-01/2019
College of Electrical and Computer Engineering, Exchange student

South China University of Technology, Guangzhou   09/2016-06/2020
*Bachelor's Degree in Electrical Engineering and Automation   **GPA: 3.74/4.00***

Tsinghua University, Beijing   09/2021-now
School of Integrated Circuits

RESEARCH EXPERIENCE

Study on Coarse-Grained Reconfigurable Architecture (CGRA)   12/2021-11/2022
Participated in projects:
•A Research on Airborne Intelligent Algorithm Adaptation for Domestic Reconfigurable Computing Chips
•A Research on Reconfigurable computing architecture for 6G communication

Study on Dynamic Random Access Memory (DRAM)-based Near-Memory Processing   01/2022-now
Participated in:
•A research on Novel Time-Domain Computing Architecture Paradigm Based on DRAM

HONORS & AWARDS

Second-class Scholarship, School of Integrated Circuits, Tsinghua University   10/2022
Merit Student, South China University of Technology   06/2018
Third-class Scholarship, South China University of Technology   06/2018
Merit Student, South China University of Technology   06/2017
Second-class Scholarship, South China University of Technology   06/2017

PERSONAL SKILLS

• Languages: English (**IELTS 7.0**, CET-6 616, CET-4 617)
                     Japanese (JLPT **N2**)

• IT Skills: C++ (National Computer Rank Examination Grade 2)
                     Verilog