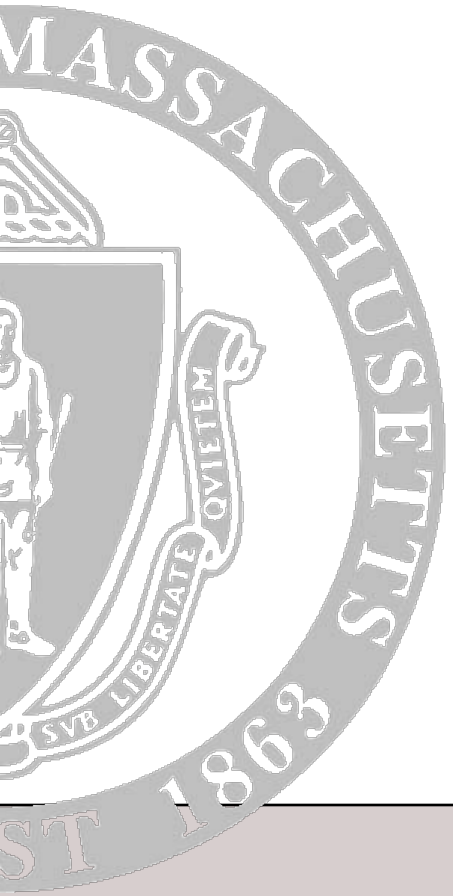
Spim-Cache Tutorial



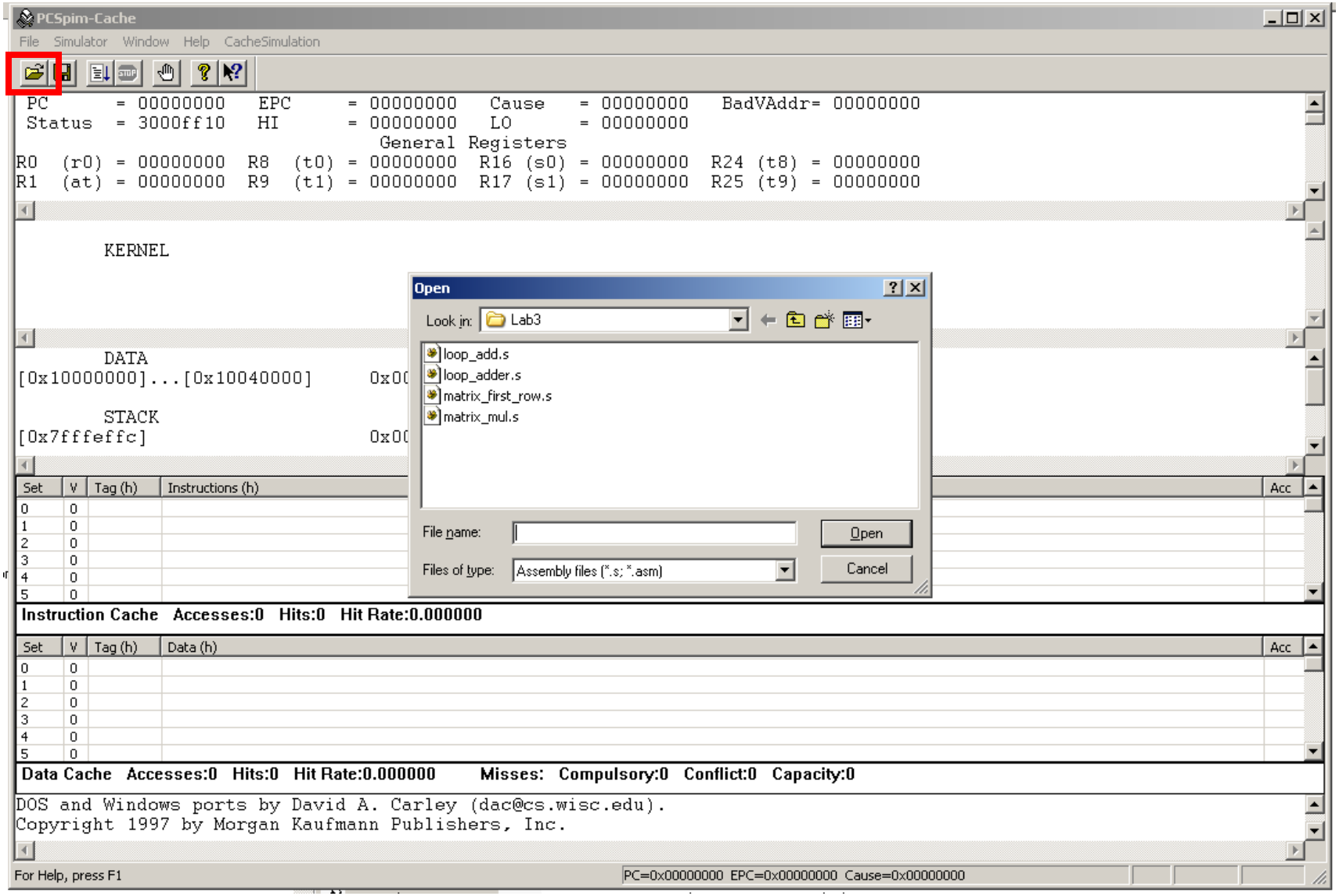
ECE232 TA

Spring 2012

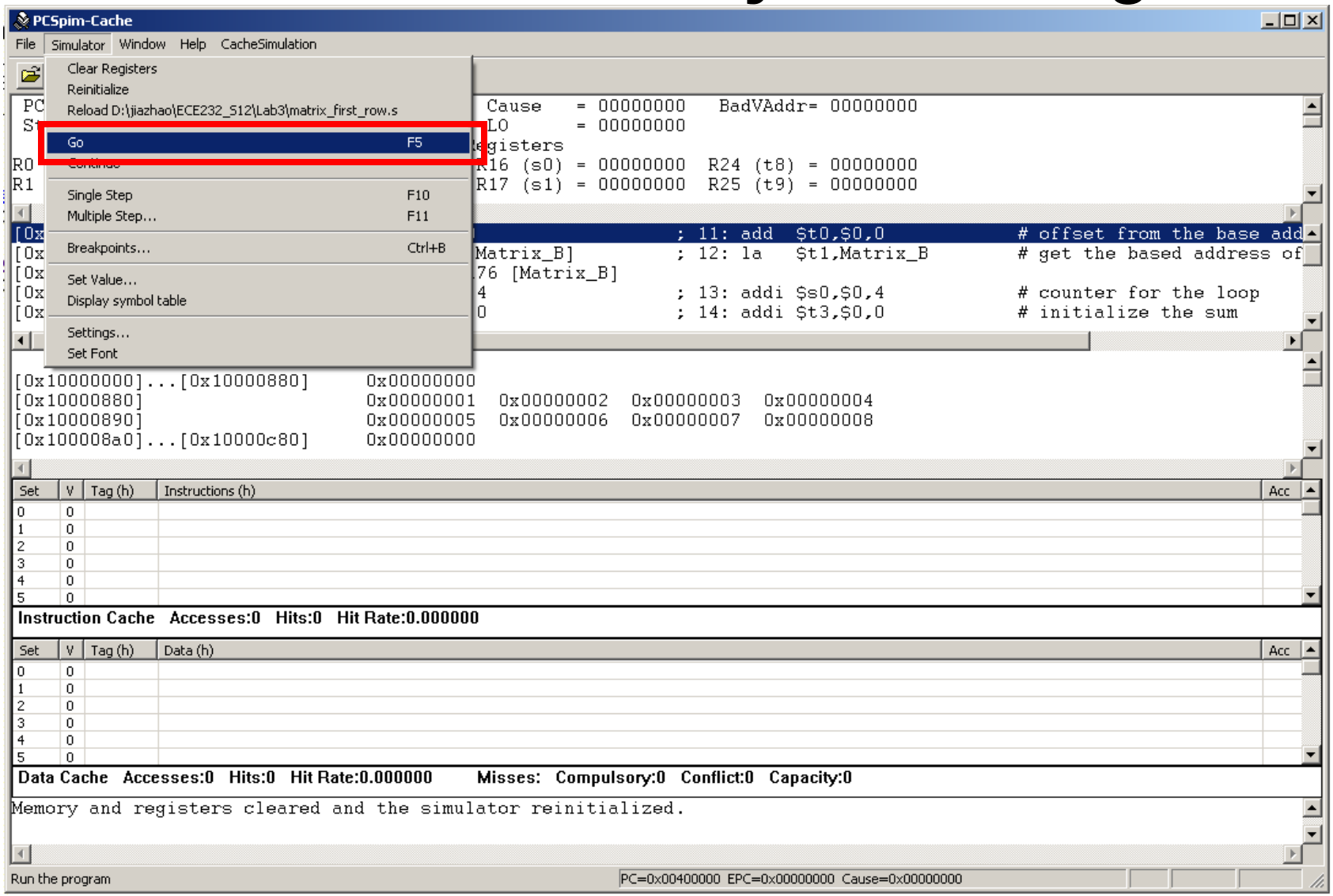
Spim-Cache

- Spim-cache is an educational tool developed at Polytechnic University of Valencia, Spain
- It is based on the Spim MIPS simulator
- Similar to the QTSpim tool
- Similar breakpoint insertion approach
- Configurable cache parameters
- Visualized cache behaviors and cache statistics
- Download link

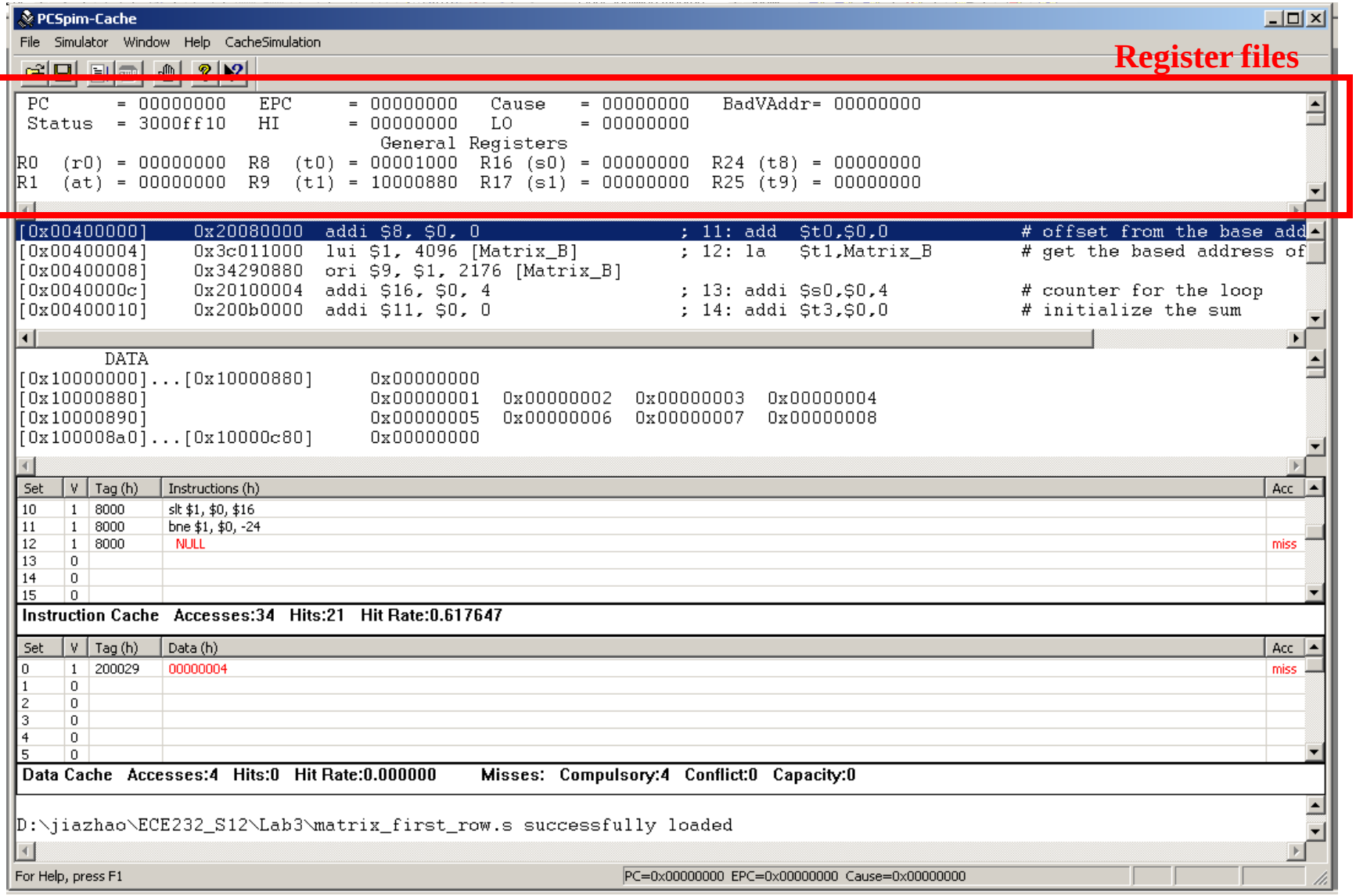
Open an Assembly Code Program



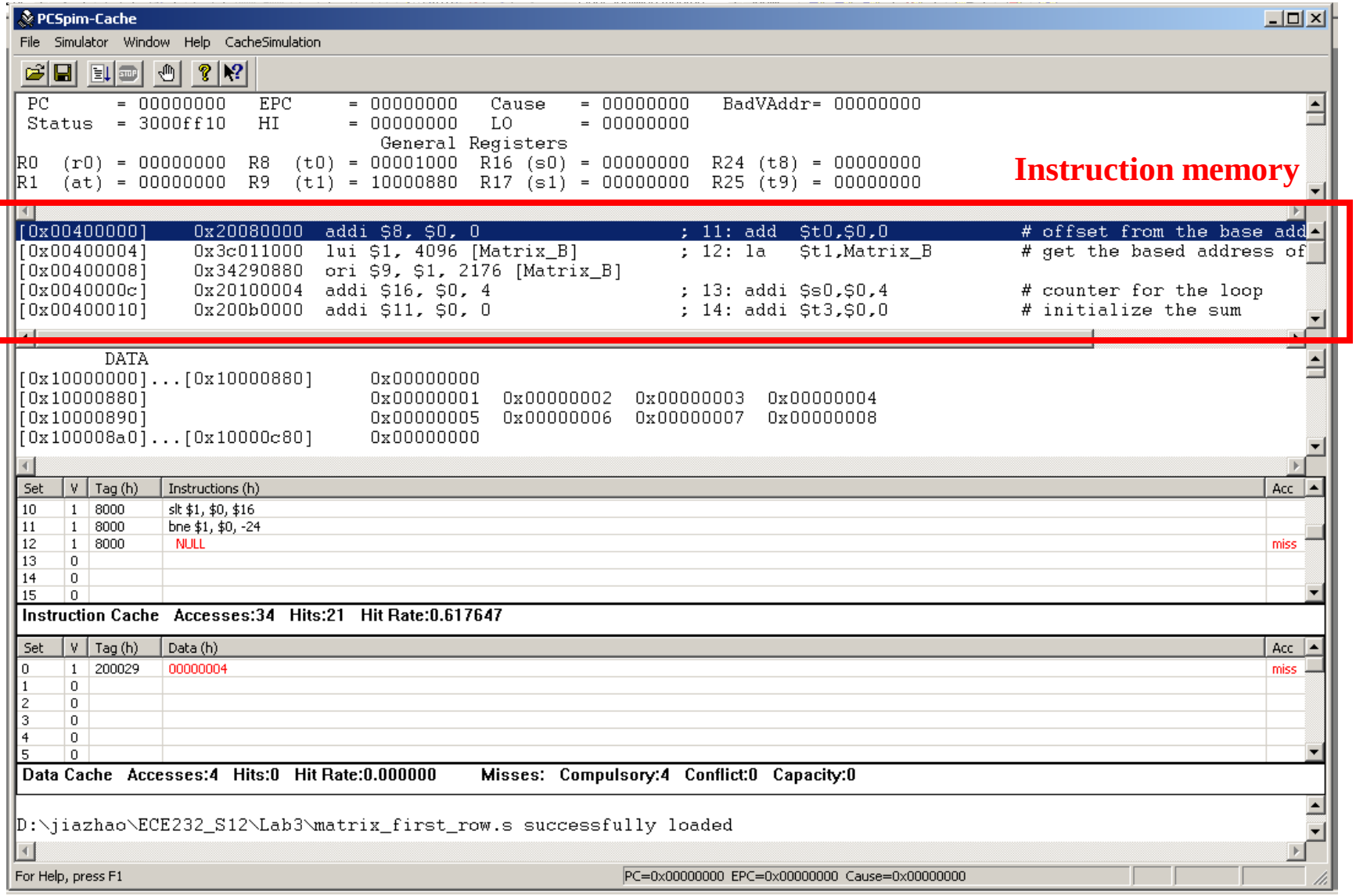
Simulate an Assembly Code Program



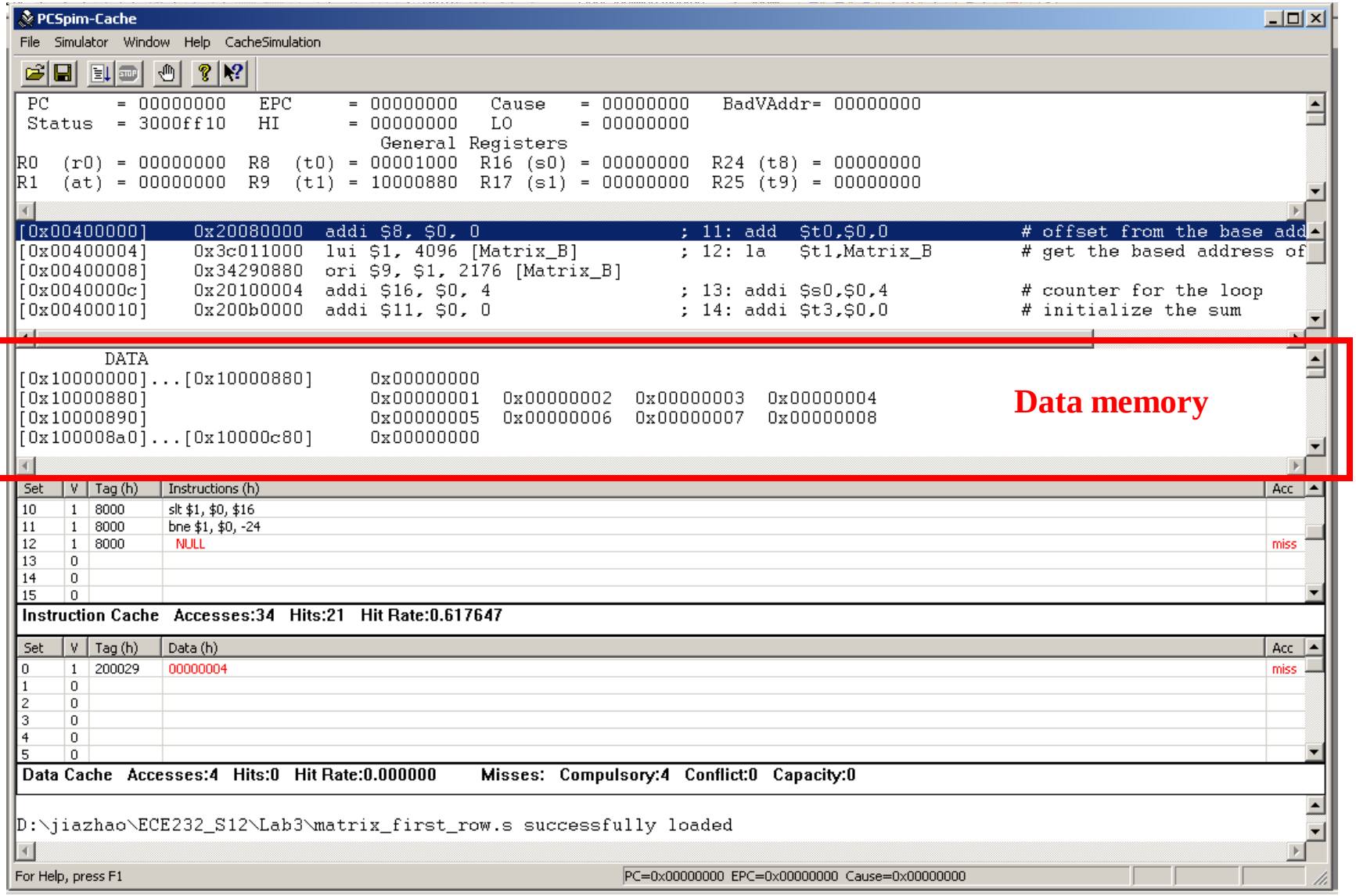
Monitor Cache Behaviors and Statistics



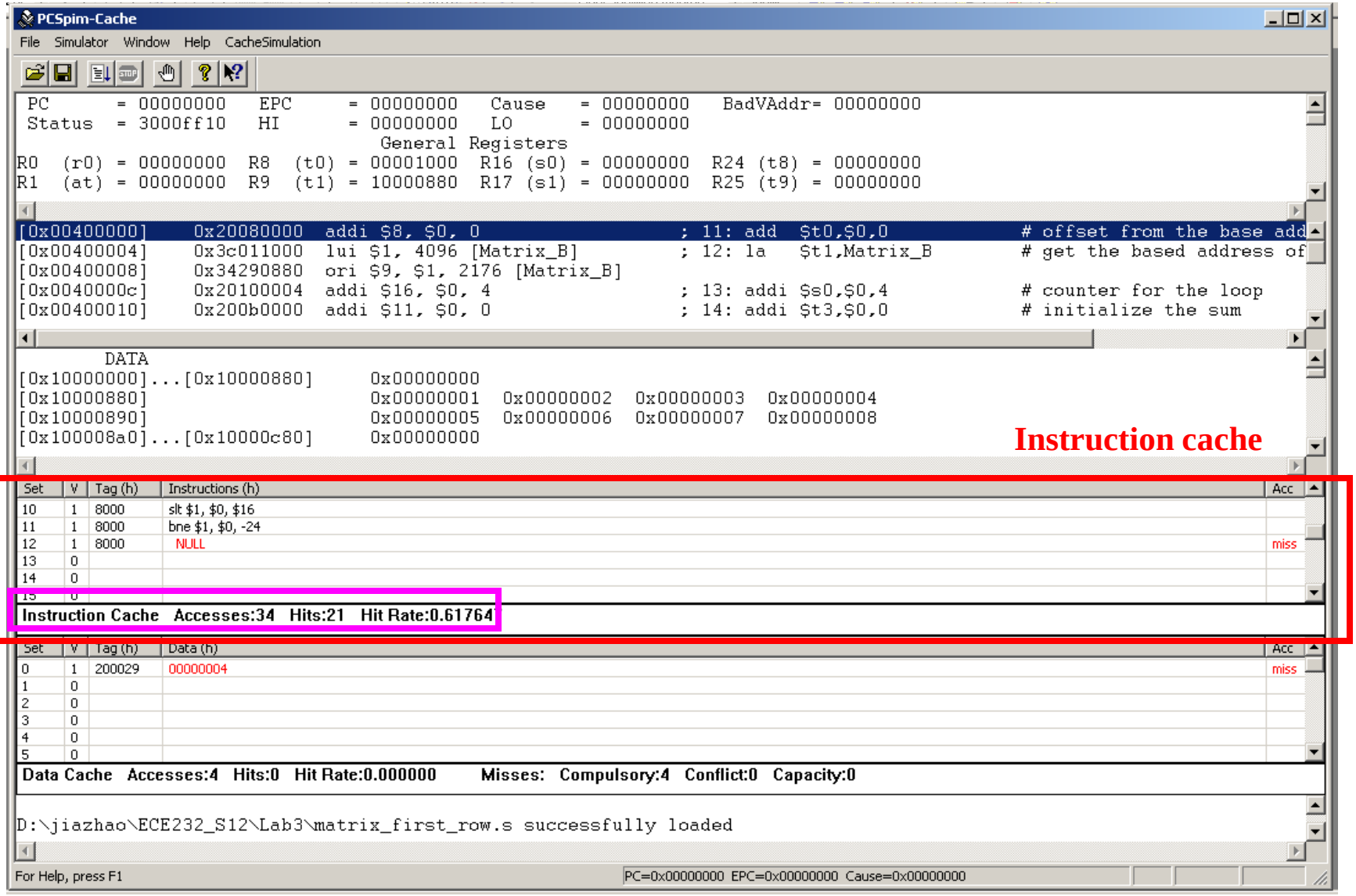
Monitor Cache Behaviors and Statistics



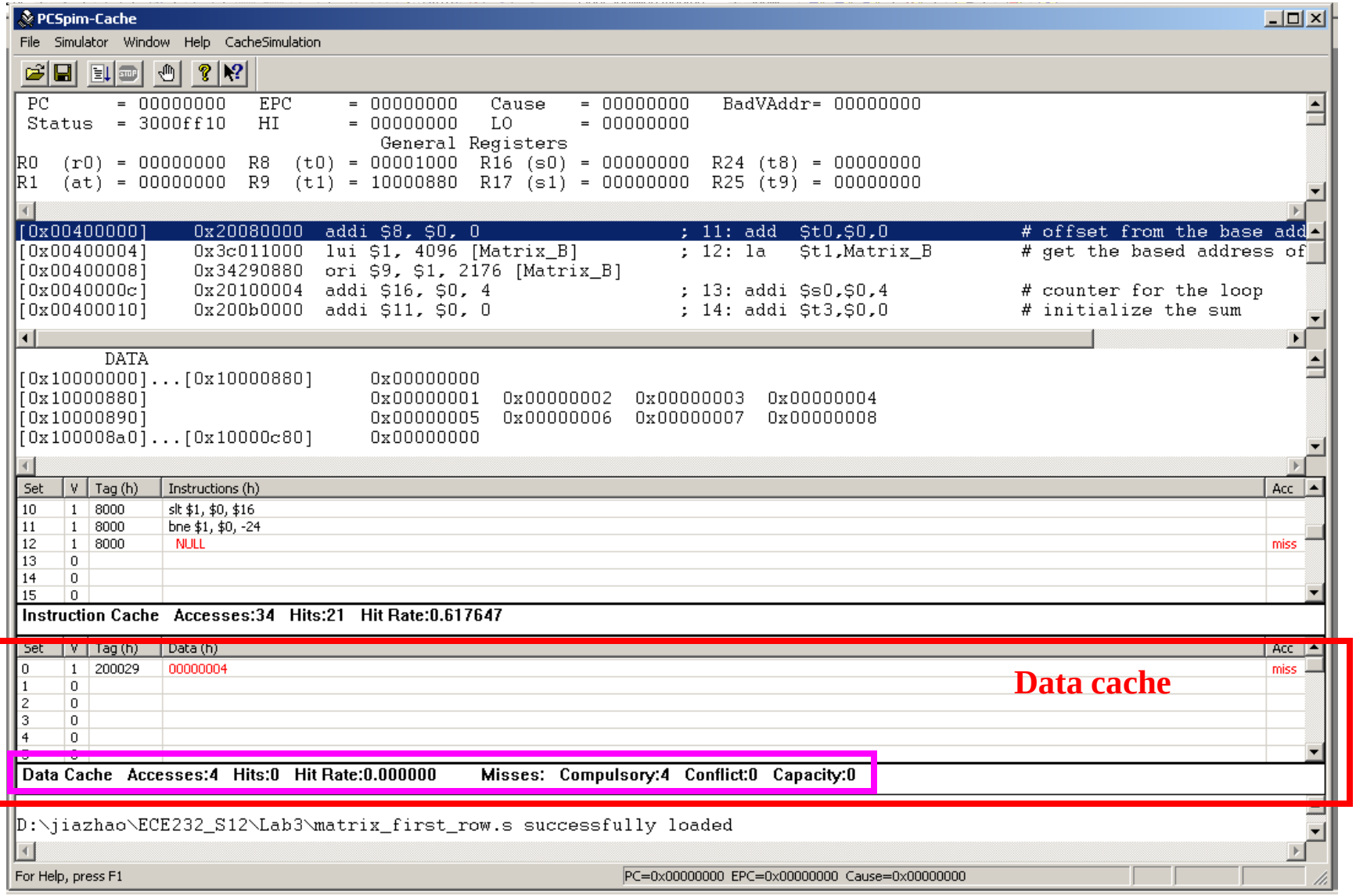
Monitor Cache Behaviors and Statistics



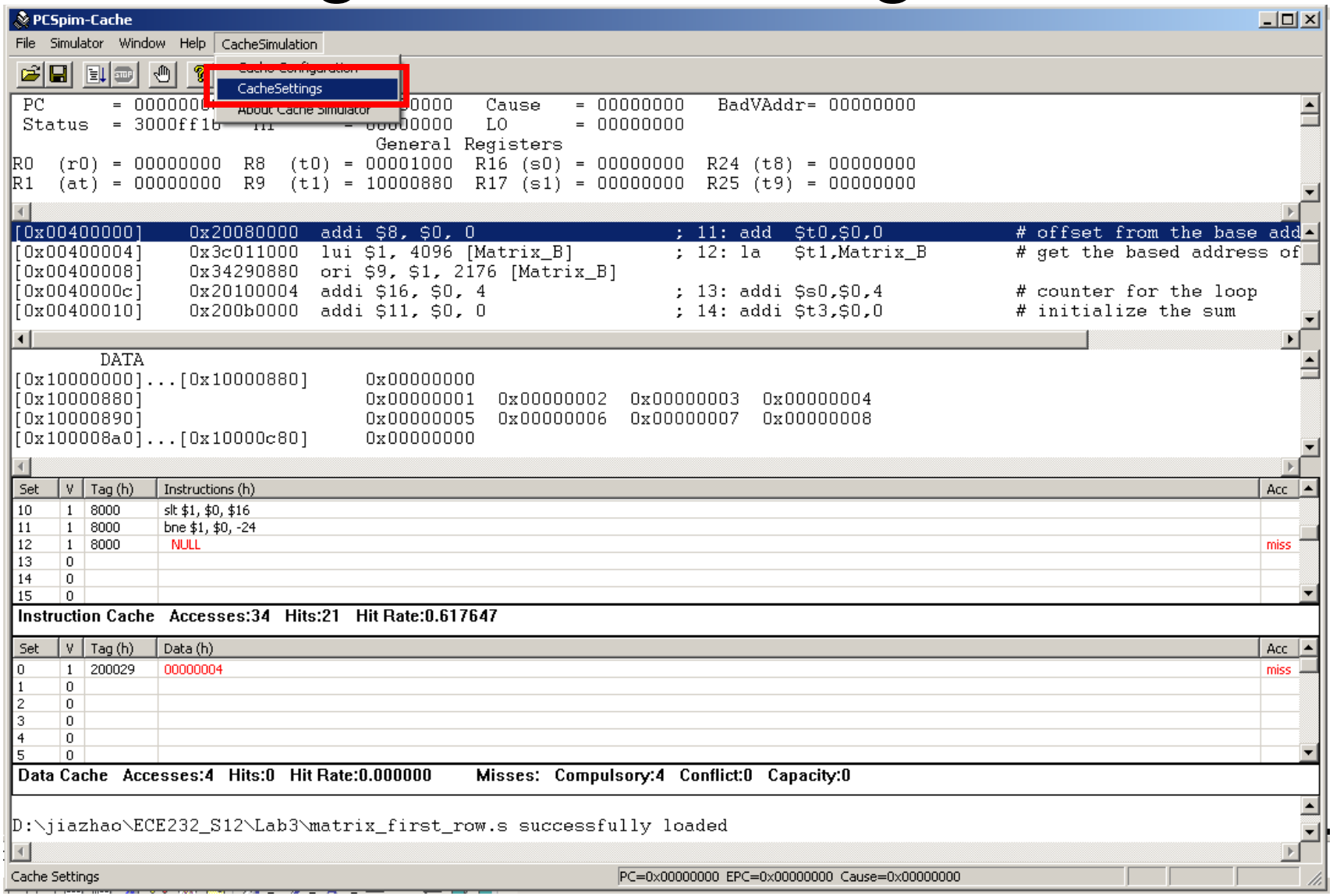
Monitor Cache Behaviors and Statistics



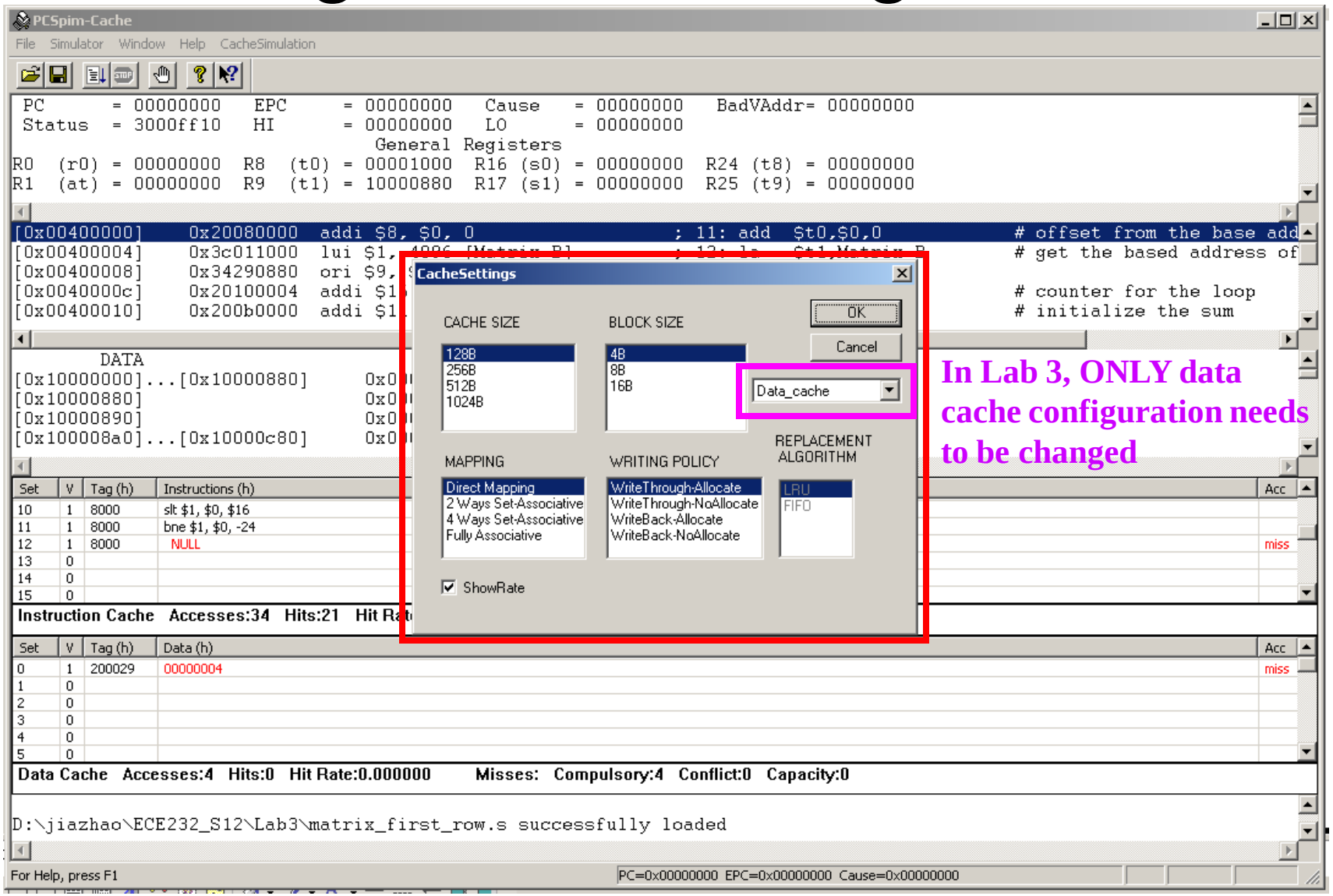
Monitor Cache Behaviors and Statistics



Change Cache Configuration



Change Cache Configuration



In Lab 3, ONLY data cache configuration needs to be changed