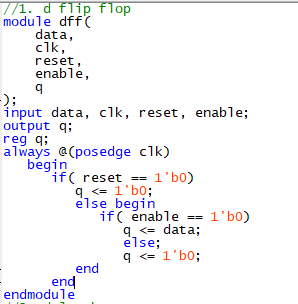
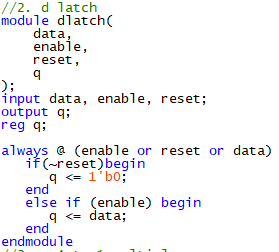
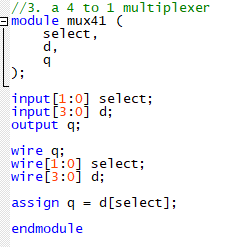
//1. d flip flop



//2. d latch



//3. a 4 to 1 multiplexer



//4. a 4 bit counter

