ABB	Software Design Review (Modbus Design Review)			5	SDR	
Responsibility:	Date:	Language:	Filing system :	Revision:	Page:	
APR/IIGF	2016.06.15	en	TFS	0.1	1/2	
Issued by:	Approved:		Released:	Are	a of validity:	
G. Horst	-		-	Al	PR/IIGF	

Project:	Cor_IF + 4-wire
Document under Review:	4WCTW Modbus Slave Subsystem Design Description V1.2.doc
Revision:	1.2
Review Date:	2016.06.15

Review-Participant:

Place	Dept.	Name	
Gö	R&D	Georg Horst	

Decision of the Review:

	Decision	next steps
	Inspection passed without restrictions	Phase finished
	Inspection passed with restrictions	some changes must be done
Х	Inspection <i>not</i> passed	Inspection must be repeated

Changes are proved: The Reviewer confirms that all changes are done:

proved Rev:	Date:	Reviewer:
1.3	26.07.2016	G. Horst

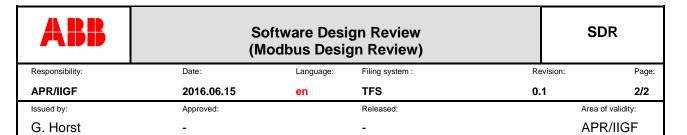
Check list:

		yes	no
1.	Is the software architecture distinct and documented?	X	
2.	Fit the modules together?	X	
3.	Are complex algorithms/procedures explained?		X
4.	Is a strategy for error handling designated?		X
5.	Is the configuration management system well prepared?	X	
6.	Are all open issues transferred to the defects table?	X	

Remarks:

Point 5:

TFS is used as configuration management system.



Defects

No.	Checkpoint	Description	Major defect	done Date
1	4	Under Data Sheet (2): - SW-Development Environment to be extended by IAR RX210 IAR C/CE Compiler Version V2.42.3 and IAR ELF Linker V2.42.2 - Operating System extend by embOS for RX210 Version 3.86e		26.07.2016
2	4	Scan Registers are not described in the Design Spec.	Х	26.07.2016
3	4	Update Modbus Uart Settings are not described in the Design Spec.	Х	26.07.2016
4	Other	Under Acronyms and Abbreviations (1.3): No 4-wire related definitions.		26.07.2016
5	Other	Interface to Option Card Modbus is missing as rough description and as reference	Х	26.07.2016
6	Other	Detailed description of t_data_obj_modbus_diag & t_data_obj_modbus_diag_history missing		26.07.2016