ABB	**Software Design Review**	**SDR**

Responsibility:	Date:	Language:	Filing system :	Revision:	Page:
Instruments		**en**	**-**	**-**	**1/2**

Template Issued by:	Template Approved:	Template Released:	Area of validity:
A. Stelter	PSM AP2.2	10/2004	APR/I

Project:	**NV_mem**
Document under Review:	**Software Design Description NV-Handler IIMS_V2_1**
Revision:	**2.1**
Review Date:	**29-06-2009**

Review-Participant:

Place	*Dept.*	*Name*	
Bangalore	INCRC	Ashwin Herur R	
Bangalore	INCRC	Ganapathi R	

Decision of the Review:

Decision	*next steps*
❑ Inspection passed *without restrictions*	Phase finished
■ Inspection passed *with restrictions*	some changes must be done
❑ Inspection *not* passed	Inspection must be repeated

Changes are proved: The Reviewer confirms that all changes are done:

proved Rev:	Date:	Reviewer:
2.2	10-07-2009	Ashwin Herur R

Check list:

		yes	no
1.	Is the software architecture distinct and documented?	**Y**	
2.	Fit the modules together?	**Y**	
3.	Are complex algorithms/procedures explained?	**Y**	
4.	Is a strategy for error handling designated?	**Y**	
5.	Is the configuration management system well prepared?	**Y**	
6.	Are all open issues transferred to the defects table?	**Y**	

Remarks:

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Instruments		**en**	**-**	**-**	**2/2**

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Defects

No.	Check point	Description	Major defect	done Date
1		Document name has no numbering **Answer: the document is an external document (Minden), so it a part of an external package of documents called 266_FE_NV_MEM**	N	No Action
2		Area of Validity need to be checked **Answer: the document is an external document (Minden), area of validity is referred to Germany**	N	No Action
3		Acronyms need to be added.	Y	
4		4.2.3 Conclusion: starts with Sl. No. 2, Number 1 is missing.	Y	09/07/2009
5		4.3 Protocol SPI-Bus is not applicable for MiLe2. So it can be deleted or the design should be customized for different project.	N	09/07/2009
6		4.6.1 I²C-Bus via interrupt or polling: 1 Byte is given as 9 bits. Please check **Answer: modified in 8 bits + Acknowledge**	N	09/07/2009
7		Page 23 is left blank.	N	09/07/2009
		5.3 Static Modeling: Mismatch in the methods in code and Class Diagram.	Y	09/07/2009
8		5.5.6 Load: in the flow chart what is Task-Wechsel?	N	09/07/2009
9		For the Functions CpyPageToPage, FileToNV,InitializeNV, CheckRam, Save, Load, Format etc use return instead of end in the flow charts.	N	09/07/2009
10		Class design missing for NV_Service.	Y	09/07/2009
11		Sequence diagrams can be added to show the Read and Write operations to NV. **Answer: too complex to be in only one diagram**	N	No Action

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