

YUANZHI ZHU

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EDUCATION BACKGROUND

- 01/2025- **Ecole Polytechnique, Paris, France**
- PhD student
- 10/2020-05/2024 **Swiss Federal Institute of Technology (ETH Zurich), Zurich, Switzerland**
- Master in **Electrical Engineering and Information Technology**
 - GPA: 5.616/6.0
- 10/2019-04/2020 **Technical University of Munich (TUM), Munich, Germany**
- Exchange Program in **Electrical and Computer Engineering**
 - GPA: 4.0/4.0 (1.0/1.0)
- 09/2016-06/2020 **Beihang University (BUAA), Beijing, China**
- Bachelor of Engineering in **Electrical Engineering**
 - GPA: 3.762/4.0 (90.73/100)

INTERNSHIP EXPERIENCE

- 07/2024-11/2024 Research Intern at **01.ai**, (remote)
- 10/2023-07/2024 Research Assistant at **University of Texas at Austin**, (remote)
- with Prof. Qiang Liu

RESEARCH INTERESTS **Computer Vision and Generative Models:** Diffusion Models; Efficient Generative Models; Multi-model Generation; Conditional Generation and Image Restoration

SELECTED PUBLICATIONS ([google scholar](#))

- [1] **Yuanzhi Zhu**, Xi Wang, Stéphane Lathuilière, Vicky Kalogeiton. [Soft-Di\[M\]O: Improving One-Step Discrete Image Generation with Soft Embeddings](#), *ICLR* (2026)
- [2] **Yuanzhi Zhu**, Xi Wang, Stéphane Lathuilière, Vicky Kalogeiton. [Di\[M\]O: Distilling Masked Diffusion Models into One-step Generator](#), *ICCV* (2025)
- [3] **Yuanzhi Zhu**, Ruiqing Wang, Shilin Lu, Junnan Li, Hanshu Yan, Kai Zhang. [OFTSR: One-Step Flow for Image Super-Resolution with Tunable Fidelity-Realism Trade-offs](#), *ICLR* (2026)
- [4] **Yuanzhi Zhu**, Xingchao Liu, Qiang Liu. [SlimFlow: Training Smaller One-Step Diffusion Models with Rectified Flow](#), *ECCV* (2024)
- [5] **Yuanzhi Zhu**, Kai Zhang, Jingyun Liang, Jie Zhang Cao, Bihan Wen, Radu Timofte, Luc Van Gool. [Denoising Diffusion Models for Plug-and-Play Image Restoration](#), *CVPRW* (2023) (300+ citations & 400+ [GitHub](#) stars)
- [6] Zhizhong Zhang*, **Yuanzhi Zhu***, Yue Zhang, Weisheng Zhao, et al. [Skyrmion-based Ultra-low Power Electric-field-controlled Reconfigurable \(SUPER\) Logic Gate](#), *IEEE Electron Device Letters* (Published as cover in 2019) (* These authors contributed equally to this work)
- [7] Hayato Mizuno, Hironari Isshiki, Kouta Kondou, **Yuanzhi Zhu**, and Yoshichika Otani. [Influence of Planar Hall Effect on the Output signal in a T-shaped Spin Conversion Device](#), *Appl. Phys. Lett.* 119, 092401 (2021)

SELECTED HONORS & AWARDS

- 11/2018,11/2019 Academic Competition Scholarship, Beihang University (Twice)
- 11/2018,11/2019 Academic Excellence Scholarship, Beihang University (Twice)
- 09/2017, 09/2018 Second Prize in China Undergraduate Mathematical Contest in Model (Twice)
- 03/2018 China Undergraduate Physics Tournament (CUPT) (Ranked 47/305)
- 02/2018 Meritorious Winner in The Mathematical Contest in Modeling
- 12/2017 First Prize in Beijing Undergraduate Physics Competition (Ranked 59/1023, Beijing)
- 10/2017 First-Class Scholarship, Beihang University
- 09/2017 First Prize in Beijing Undergraduate Mathematics Competition (Ranked 87/1276, Beijing)

LANGUAGE PROFICIENCY Chinese (Native); English (C1); German (A2)