

1 Surface Code

The Surface Code, first introduced by Kitaev [1], is the most promising error correction code for quantum computing. Using this code, universal computation can be performed with magic state distillation. In this section, we will first introduce the stabilizers of the Surface Code on the torus and then on the planar surface.

1.1 Surface Code on the Torus

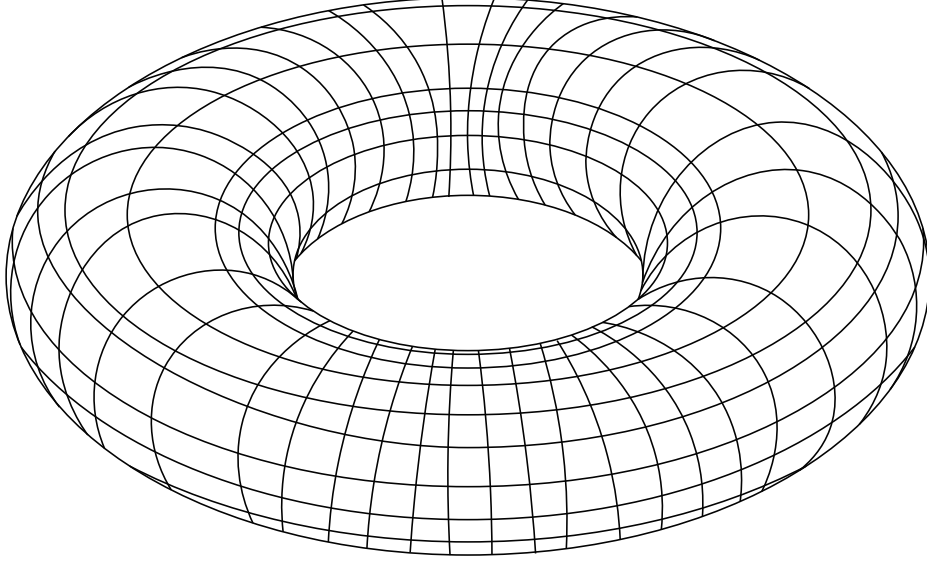


Fig. 1

Usually, the Surface Code is defined on a 1-genus torus, but it can also be defined on an n -genus torus in the same way. The lattice on the torus, shown in Fig. 1, has the following property:

$$V - E + F = 2 - 2g \quad (1)$$

where V is the number of vertices, E is the number of edges, F is the number of faces of the lattice on the torus, and g is the genus. For $g = 1$, the 1-genus case, Eq. 1 equals 0. now we introduce data qubits on the edges, X stabilizers on the vertices and Z stabilizers on the faces shown in Fig.2.

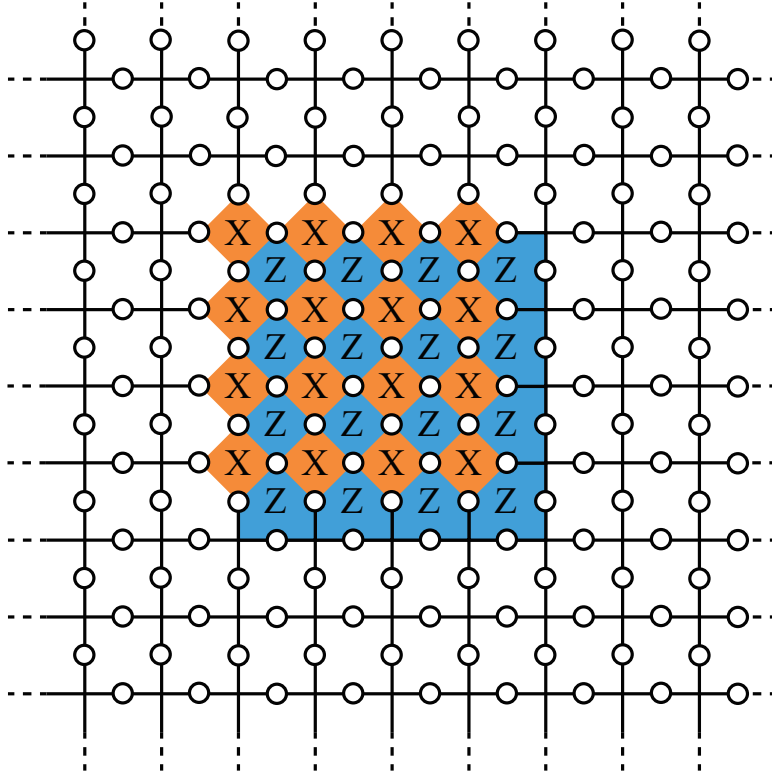


Fig. 2

In Fig. 2, we show some of the stabilizers of the Surface Code, but others exist in the remaining parts of the lattice. Thus, we have $V - 1$ X stabilizer generators because the product of all X stabilizers on the vertices is the identity. From the same discussion, we have $F - 1$ Z stabilizer generators. Therefore, the number of logical qubits k that can be encoded in the Surface Code is:

$$k = E - (V - 1) - (F - 1) = 2 \quad (2)$$

using Eq. 1. From these results, we can identify four logical operators corresponding to the non-trivial cycles on the torus. The logical operators are shown in Fig. 3, where the subscripts 1 and 2 indicate the qubit numbers of the two logical qubits. From Fig. 3, one can confirm that $L_X^i L_Z^i = -L_Z^i L_X^i$, that all logical operators commute with the stabilizers, and that the code distance is \sqrt{n} , which is the least weight of a logical operator.

1.2 Surface Code on the Planar

The Surface Code on the planar is different from that on the torus because it has boundaries, as shown in Fig. 4. One can observe that there exist 3-weight stabilizers at the boundaries. Additionally, the Surface Code on the planar can encode one logical qubit.

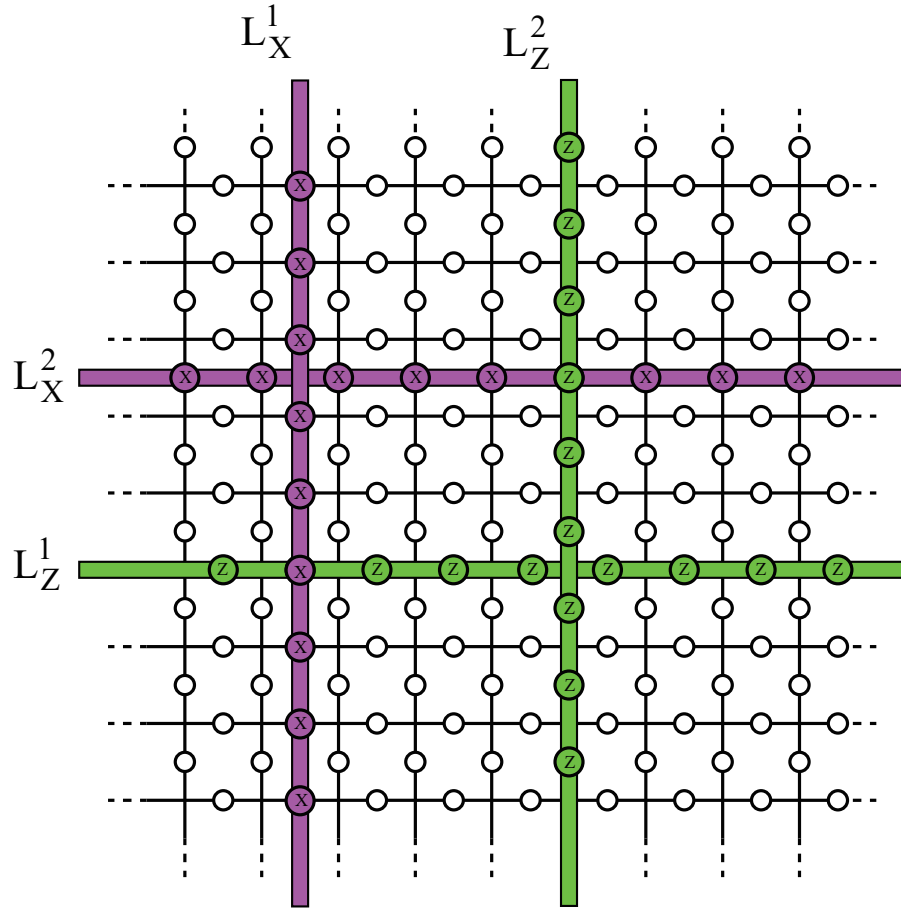


Fig. 3

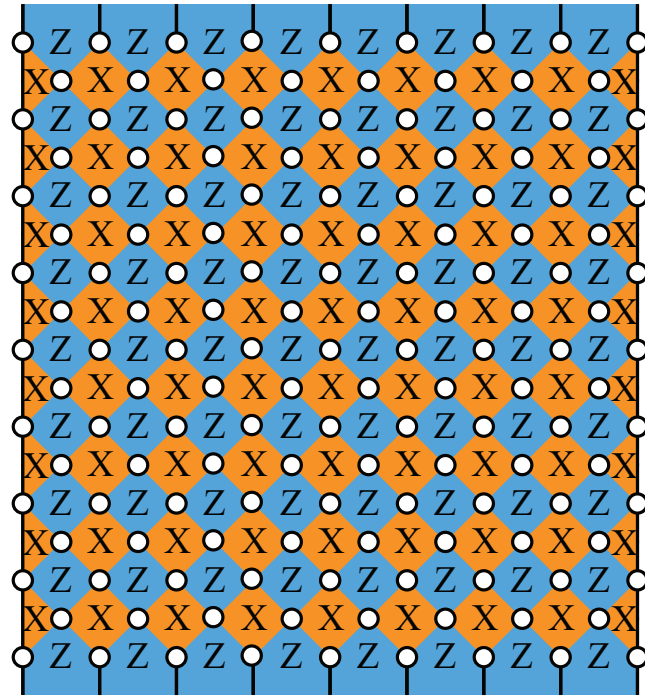


Fig. 4

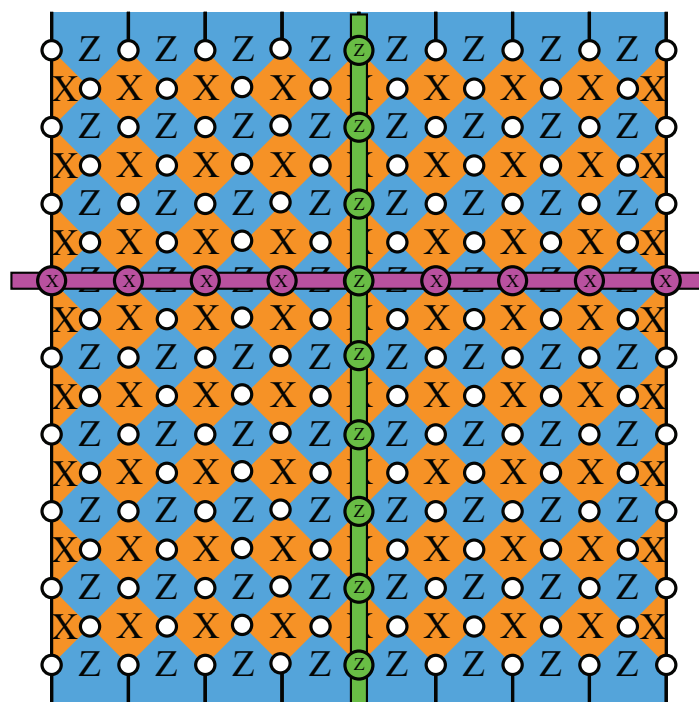


Fig. 5