

1 Looped Pipeline Architecture

Many quantum computing platforms are based on a two-dimensional physical layout. We focus on "looped pipelines," [1] which offer the advantages of a three-dimensional lattice while being restricted to two-dimensional space. This architecture leverages qubit shuttling, where qubits are moved around on the chip, enabling long-range interactions between qubits that are far apart.

1.1 Classical Linear Pipeline

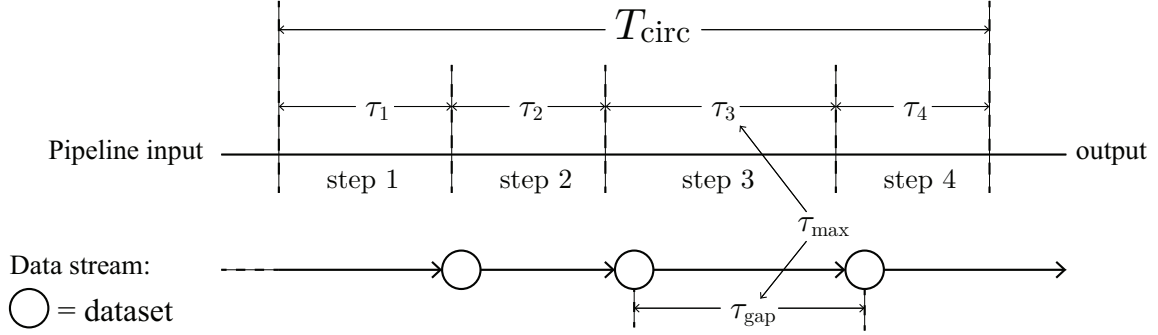


Fig. 1

Let us first introduce "data-processing pipeline," we divide data-processing circuit into multiple steps with each step able to operate only one dataset at a time shown in Fig. 1. Now instead of inputting second dataset after the processing the first dataset on whole circuit, we input it as soon as finishing the process of the first dataset on step 1, similarly all subsequent datasets and subsequent steps. In this way, all data-processing steps can be working on different datasets in parallel, increasing the throughput of the system. this is a concept of pipelining. The processing time of the m th step is denoted as τ_m , and suppose there exist M steps in the circuit. Then, the total time T_{circ} taken for the entire circuit to process one dataset is given by:

$$T_{\text{circ}} = \sum_{m=1}^M \tau_m. \quad (1)$$

This is the time required for every dataset without pipelining, and it is also the time required to process the first dataset in pipelining. For the second dataset in pipelining, the additional time required for processing is given by:

$$\tau_{\text{max}} = \max_i \tau_i. \quad (2)$$

From the second dataset onward, the time required for processing each dataset equals the maximum of τ_i . Hence, the total time for processing the k th datasets are given by:

$$T_{\text{pipe}} = T_{\text{circ}} + (k - 1)\tau_{\text{extmax}}. \quad (3)$$

The data stream can flow through the entire pipeline without requiring modifications to the time gap between adjacent datasets or being put on hold at any point along the pipeline.

1.2 Looped Qubit Pipeline

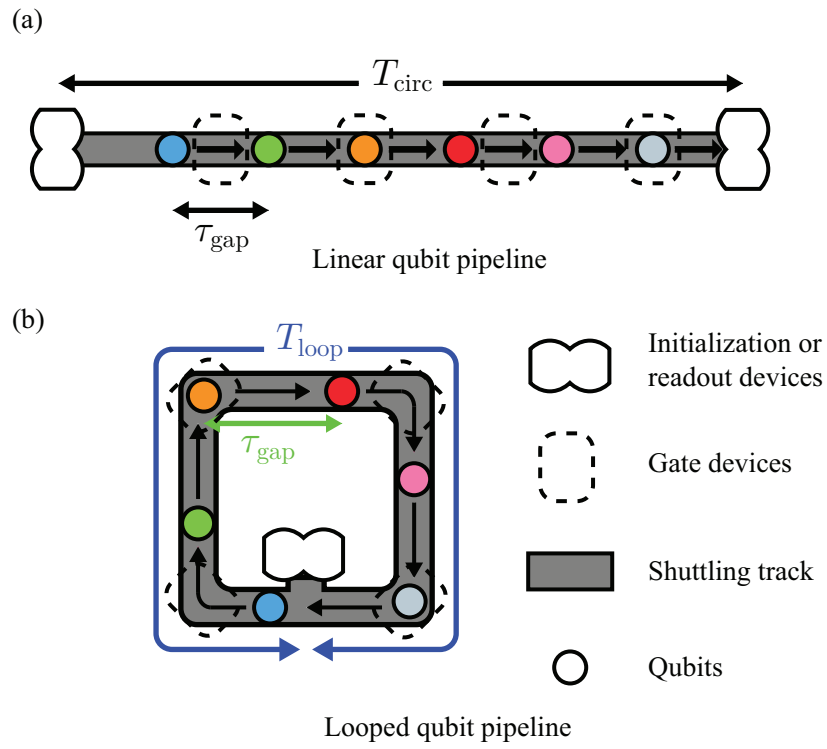


Fig. 2