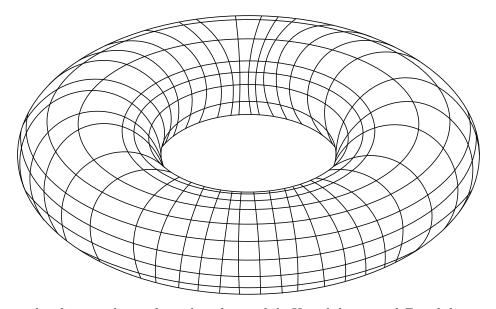
## 1 Surface Code

The Surface Code, first introduced by Kitaev [1], is the most promising error correction code for quantum computing. Using this code, universal computation can be performed with magic state distillation. In this section, we will first introduce the stabilizers of the Surface Code on the torus and then on the planar surface.

## 1.1 Surface Code on the Torus



**Fig. 1** Physical qubits are located on the edges, while X-stabilizers and Z-stabilizers are placed on the vertices of the lattice.

Usually, the Surface Code is defined on a 1-genus torus, but it can also be defined on an n-genus torus in the same way. The lattice on the torus, shown in Fig. 1, has the following property:

$$V - E + F = 2 - 2g \tag{1}$$

where V is the number of vertices, E is the number of edges, F is the number of faces of the lattice on the torus, and g is the genus. For g=1, the 1-genus case, Eq. 1 equals 0. We now introduce data qubits on the edges, X stabilizers on the vertices, and Z stabilizers on the faces, as shown in Fig. 2. Incidentally, in Fig. 2, the left and right sides are identical, and similarly, the upper and bottom sides are identical. Ancilla qubits for syndrome measurements are allocated at the center of the faces for the Zstabilizers and on the vertices for the X stabilizers.

In Fig. 2, we show some of the stabilizers of the Surface Code, but others exist in the remaining parts of the lattice. Thus, we have V-1 X stabilizer generators because the product of all X stabilizers on the vertices is the identity. From the same discussion, we have F-1 Z stabilizer generators. Therefore, the number of logical qubits k that can be encoded in the Surface Code is:

$$k = E - (V - 1) - (F - 1) = 2 (2)$$

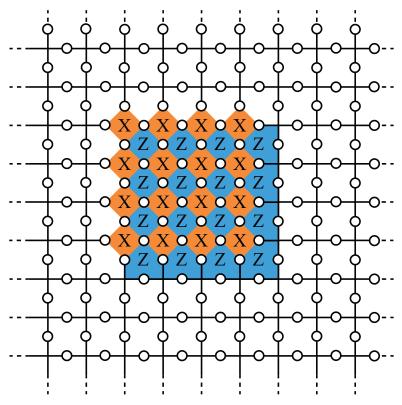


Fig. 2 Physical qubits, X-stabilizer generators and Z-stabilizer generators on the torus. The right and left sides of the lattice are identical, and the top and bottom sides of the lattice are identical.

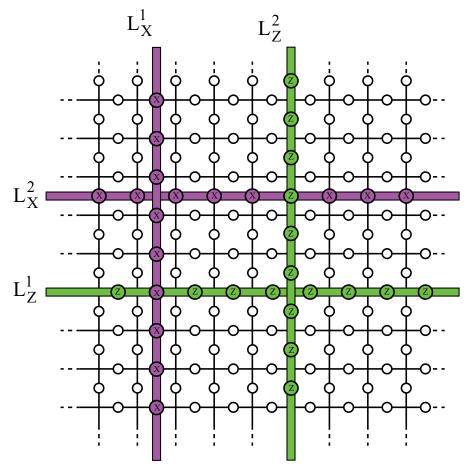
using Eq. 1. From these results, we can identify four logical operators corresponding to the non-trivial cycles on the torus. The logical operators are shown in Fig. 3, where the subscripts 1 and 2 indicate the qubit numbers of the two logical qubits. From Fig. 3, one can confirm that  $L_X^i L_Z^i = -L_Z^i L_X^i$ , that all logical operators commute with the stabilizers, and that the code distance is  $\sqrt{n}$ , which is the least weight of a logical operator.

## 1.2 Surface Code on the Planar

The Surface Code on the planar is different from that on the torus because it has boundaries, as shown in Fig. 4. One can observe that there exist 3-weight stabilizers at the boundaries.

Additionally, the Surface Code on the planar can encode one logical qubit (Fig. 5). In the same way of the torus, one can confirm that code distance of the surface code in Fig. 5 is  $\sqrt{n}$  where there exists n qubits. The two boundaries that are connected to each other by the logical Z operator are called rough boundaries, while the remaining boundaries are called smooth boundaries.

There exist excess qubits in the previous Surface Code. As shown in Fig. 6, we can cut along the green line, which contains both data qubits and ancilla qubits, trim off the excess parts, and rotate the lattice by 45 degrees to obtain the rotated Surface Code. With this modification, the code distance of the Surface Code is preserved by defining new logical operators on the rotated Surface Code that connect the rough boundaries or the smooth boundaries.



**Fig. 3** Logical operators of the surface code on the torus, which encode logical qubits. Purple lines represent logical X operators, and green lines represent logical Z operators.

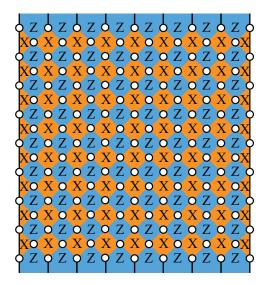
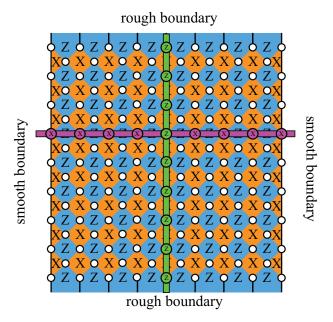
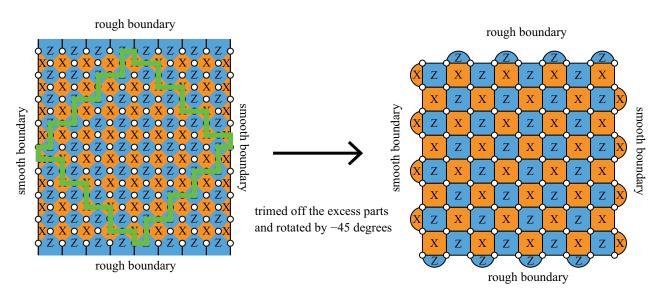


Fig. 4 Surface code on the plane. Orange shapes are X-stabilizers, and blue shapes are Z-stabilizers.



**Fig. 5** A horizontal boundary, which consists of 3-weight Z-stabilizers, is called a rough boundary, and a vertical boundary, which consists of 3-weight X-stabilizers, is called a smooth boundary.



**Fig. 6** By trimming the excess parts of the previous surface code, we obtained a new surface code while preserving its distance.