CSC 252: Computer Organization Spring 2023: Lecture 9

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Announcement

• Programming assignment 2 is out. It's in x86 assembly language. Details at: https://www.cs.rochester.edu/courses/252/spring2023/labs/assignment2.html.

22	23	24	25	26	27	28
29	30	31	Feb 1	2	3	4
5	6	7	Today	9	10	11
12	Due	14	15	16	17	18

Announcement

- You might still have three slip days.
- Read the instructions before getting started!!!
 - You get 1/4 point off for every wrong answer
 - Maxed out at 10
- TAs are best positioned to answer your questions about programming assignments!!!
- Programming assignments do NOT repeat the lecture materials. They ask you to synthesize what you have learned from the lectures and work out something new.
- Logics and arithmetics problem set: https://www.cs.rochester.edu/courses/252/spring2023/ handouts.html.
 - Not to be turned in.

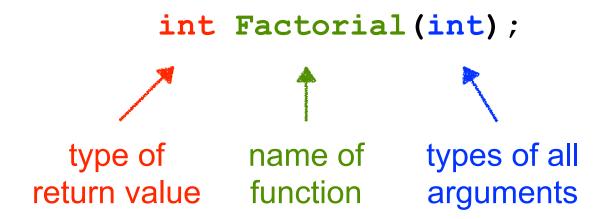
Today: How to Implement Function Call

- What are functions and why do we use them?
- General idea of implementing functions: Stack
- Passing control
- Passing data
- Managing local data

Functions Declaration in C

Declaration (also called prototype)

States return type, name, types of arguments



Function Definition

- Must match function declaration
- Implement the functionality of the function

```
int Factorial(int n)
{
  int i;
  int result = 1;
  for (i = 1; i <= n; i++)
    result *= i;
  return result;
}</pre>
```

gives control back to calling function and returns value

```
P(...) {
...
    y = Q(x);
    print(y)
...
}
```

```
int Q(int i)
{
   int t = 3*i;
   int v[10];
...
   return v[t];
}
```

Passing control

- To beginning of procedure code
- Back to return point

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P(...) {
...
y = Q(x);
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int Q(int i)
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  print(y)
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Passing control

- To beginning of procedure code
- Back to return point

Passing data

- Procedure arguments
- Return value

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P(...) {
  y = Q(x);
print(y)
int Q(int i)
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  int v[10];
  return v[t];
```

Passing control

- To beginning of procedure code
- Back to return point

Passing data

- Procedure arguments
- Return value

```
P(...) {
  y = Q(x);
  print(y)
int Q(int i)
  int t = 3*i;
  int v[10];
  return v[t];
```

Passing control

- To beginning of procedure code
- Back to return point

Passing data

- Procedure arguments
- Return value

```
P(...) {
  y = Q(x);
  print(y)
int Q(int i)
  int t + 3*i;
  int v[10];
  return v[t];
```

Passing control

- To beginning of procedure code
- Back to return point

Passing data

- Procedure arguments
- Return value

Local Memory management

- Allocate during procedure execution
- Deallocate upon return

```
P(...) {
...
y = Q(x);
print(y)
...
}
```

```
int Q(int i)
{
   int t = 3*i;
   int v[10];
...
   return v[t];
}
```

Passing control

- To beginning of procedure code
- Back to return point

Passing data

- Procedure arguments
- Return value

Local Memory management

- Allocate during procedure execution
- Deallocate upon return
- The ISA must provide ways (instructions) to realize all these.

```
P(...) {
...
y = Q(x);
print(y)
...
}
```

```
int Q(int i)
{
   int t = 3*i;
   int v[10];
...
   return v[t];
}
```

Today: How to Implement Function Call

- What are functions and why do we use them?
- General idea of implementing functions: Stack
- Passing control
- Passing data
- Managing local data

- Frame (Active Record)
 - A frame refers to a piece of memory that contains (almost) all the information needed to execute a function, e.g., arguments and local variables

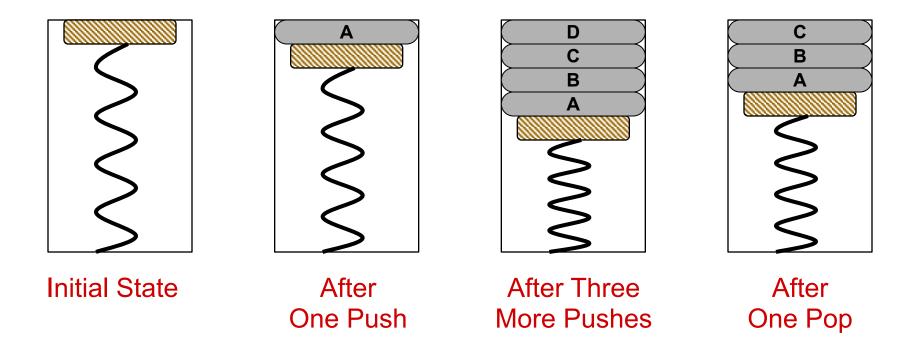
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- When a function is called, create a frame, and push it to the memory
- When a function is returned, pop the frame out of the memory
- Frames are stored in memory in a stack fashion

A Physical Stack: A Coin Holder

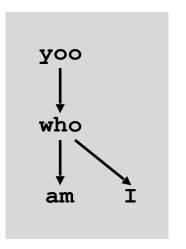
First quarter out is the last quarter in.

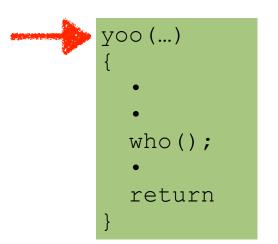


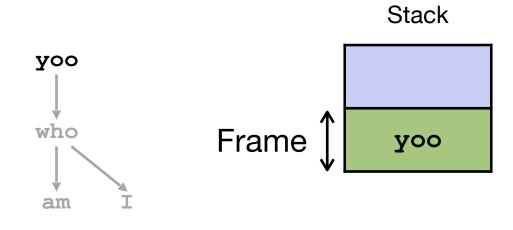
- Stack is the right data structure for function call / return
 - If A calls B, then B returns before A

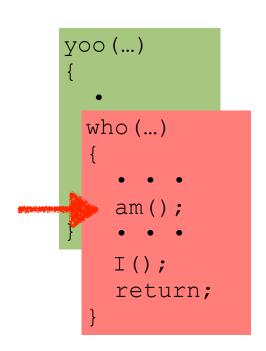
```
who(...)
{
    am();
    if ();
    return;
}
```

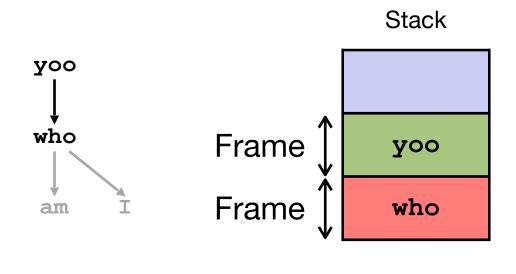
Example Call Chain

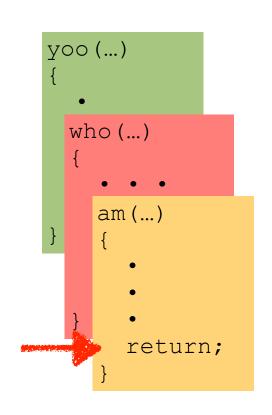


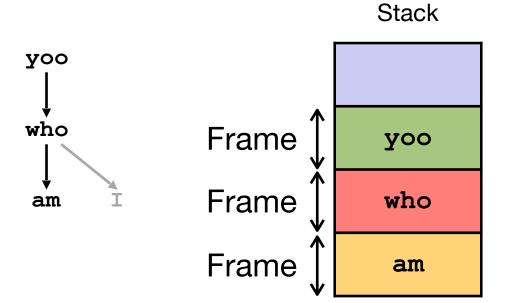


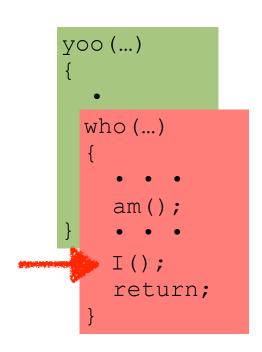


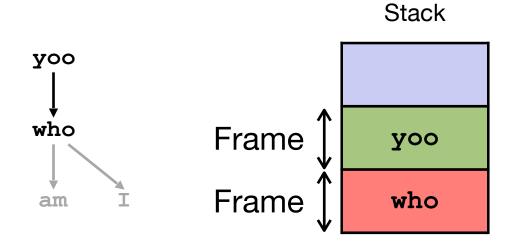


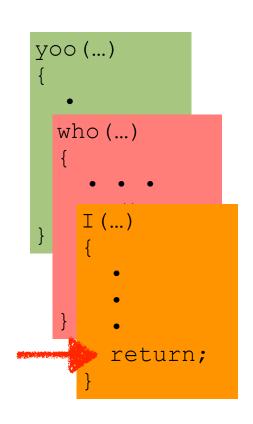


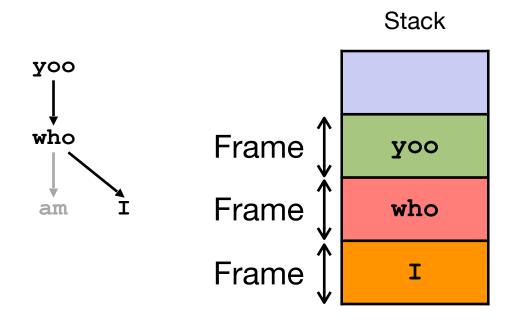


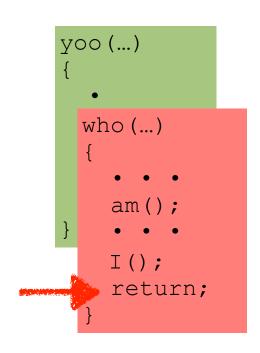


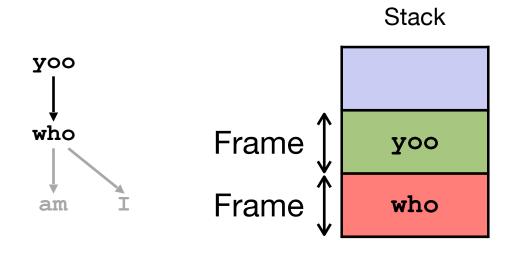


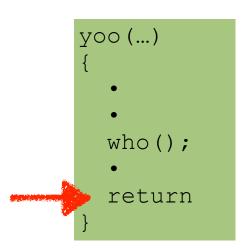


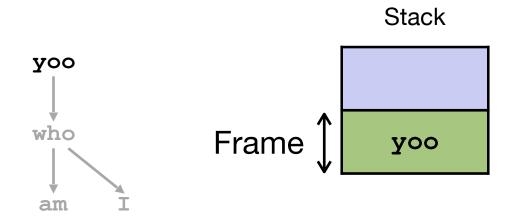












Stack in X86-64

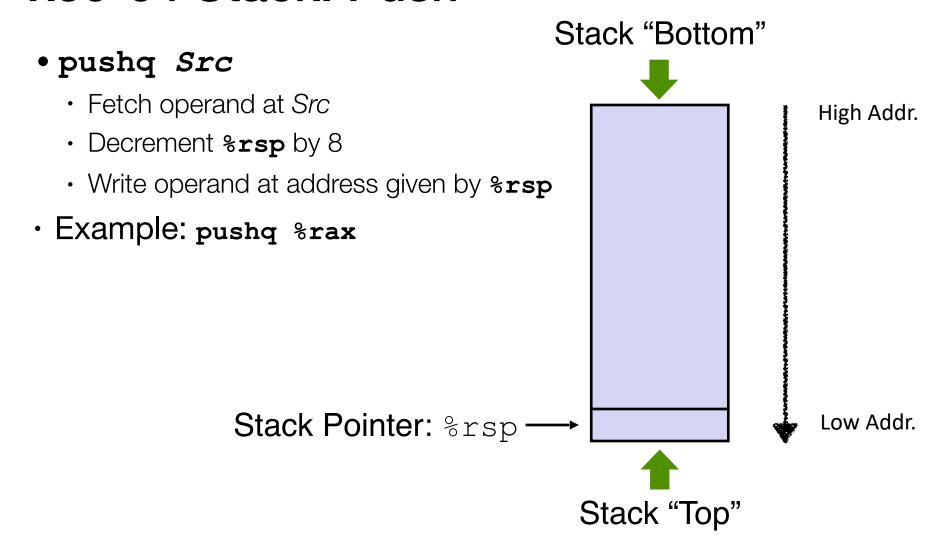
- Region of memory managed with stack discipline
- Grows toward lower addresses
- Register %rsp contains address of "top" element, i.e., lowest stack address

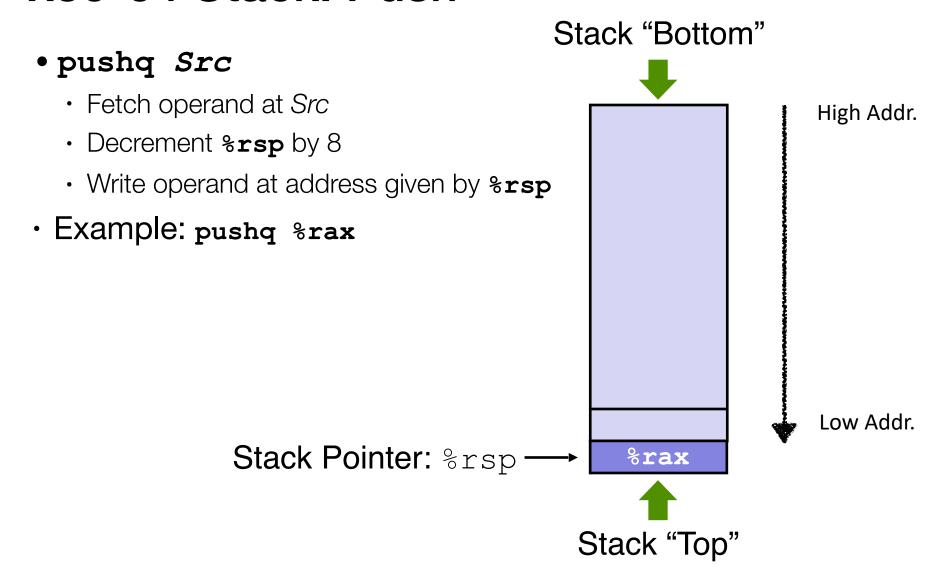
Low Addr. Stack Pointer: %rsp Stack "Top"

Stack "Bottom"

High Addr.

Stack "Bottom" • pushq Src Fetch operand at Src High Addr. Decrement %rsp by 8 Write operand at address given by %rsp Low Addr. Stack Pointer: %rsp Stack "Top"



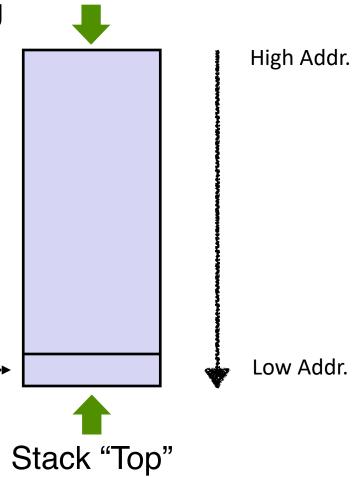


Stack "Bottom" • pushq Src Fetch operand at Src High Addr. Decrement %rsp by 8 Write operand at address given by %rsp · Example: pushq %rax Same as (in functionality): subq \$0x08, %rsp movq %rax, (%rsp) Low Addr. %rax Stack Pointer: %rsp Stack "Top"

 Sometimes instead of keep pushing multiple items, we could first reserve space on the stack then move items in:

- subq 0x18, %rsp
- movq %rax, (%rsp)
- movq %rbx, 8(%rsp)
- movq %rcx, 16(%rsp)

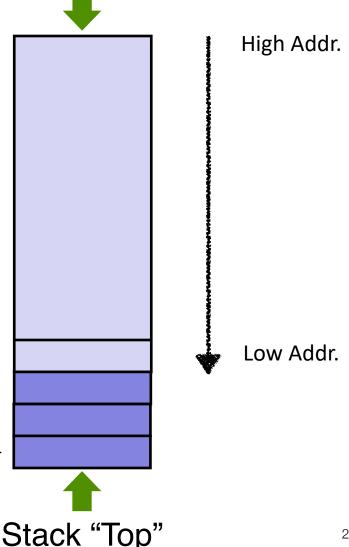
Stack Pointer: %rsp —



Stack "Bottom"

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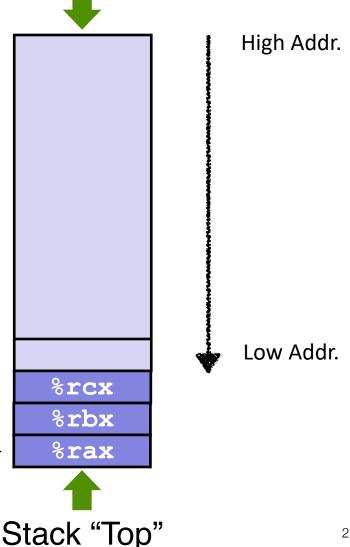
Stack "Bottom"

Stack Pointer: %rsp

x86-64 Stack: Push

 Sometimes instead of keep pushing multiple items, we could first reserve space on the stack then move items in:

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- movq %rbx, 8(%rsp)
- movq %rcx, 16(%rsp)



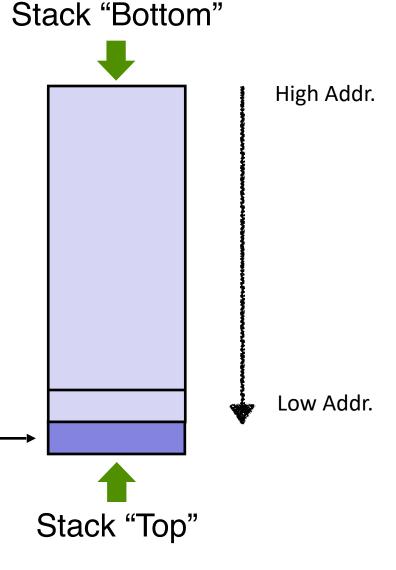
Stack "Bottom"

Stack Pointer: %rsp

• popq Dest

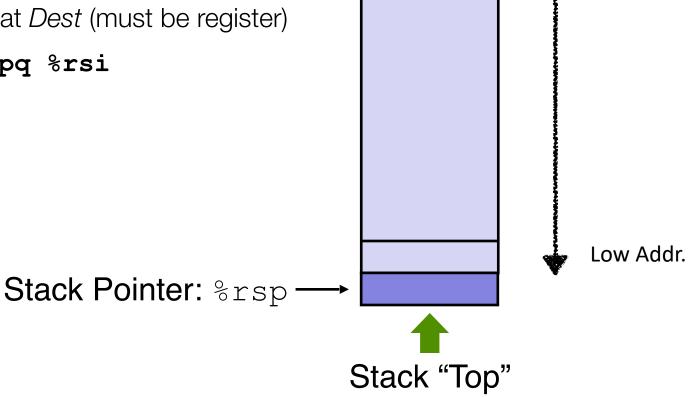
- Read value at address given by %rsp
- Increment %rsp by 8
- Store value at Dest (must be register)

Stack Pointer: %rsp



• popq Dest

- Read value at address given by %rsp
- Increment %rsp by 8
- Store value at Dest (must be register)
- · Example: popq %rsi



Stack "Bottom"

High Addr.

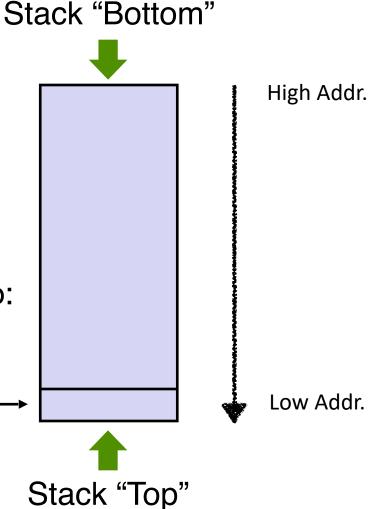
Stack "Bottom" • popq Dest Read value at address given by %rsp High Addr. • Increment %rsp by 8 Store value at Dest (must be register) · Example: popq %rsi Stack Pointer: %rsp Low Addr.

Stack "Top"

• popq Dest

- Read value at address given by %rsp
- Increment %rsp by 8
- Store value at Dest (must be register)
- Example: popq %rsi
- If you don't care about saving the popped value, you could simply do:
 - addq \$0x08, %rsp

Stack Pointer: %rsp —



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Code Examples

```
void multstore
 (long x, long y, long *dest)
    long t = mult2(x, y);
    *dest = t;
long mult2 (long a, long b)
  long s = a * b;
  return s;
```

Code Examples

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    long t = mult2(x, y);
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long mult2 (long a, long b)
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  return s;
```

```
400540 <multstore>:
 400540: push %rbx
 400541: mov %rdx, %rbx
 400544: callq 400550 <mult2>
 400549: mov %rax, (%rbx)
 40054c: pop %rbx
 40054d: retq
400550 <mult2>:
 400550: mov
                %rdi,%rax
 400553: imul
                 %rsi,%rax
 400557: retq
```

Code Examples

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void multstore
 (long x, long y, long *dest)
    long t = mult2(x, y);
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 400550: mov
                %rdi,%rax
 400553: imul
                 %rsi,%rax
 400557: retq
```

retq returns to (by changing the PC) 400549. But how would retq know where to return?

Non-Solution

- Replace callq with jmp
- assign a label to the instruction next to callq (e.g., .L1)
- replace retq with jmq .L1

```
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                %rdx,%rbx
 400544: callq 400550 <mult2>
 400549: mov %rax, (%rbx)
 40054c: pop %rbx
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 400550: mov
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                 %rsi,%rax
 400557:
          retq
```

Non-Solution

- Replace callq with jmp
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```
400540 <multstore>:
   400540: push
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   400544:
                   400550 <mult2>
           dmt
.L1 400549: mov
                   %rax, (%rbx)
   40054c: pop
                  %rbx
   40054d: retq
 400550 <mult2>:
   400550:
                    %rdi,%rax
             mov
   400553:
            imul
                    %rsi,%rax
   400557:
             jmp
                    .L1
```

Non-Solution

- Replace callq with jmp
- assign a label to the instruction next to callq (e.g., .L1)
- replace retq with jmq .L1
- Will this work?!
- How about when other functions call mult2?

```
400540 <multstore>:
   400540: push
                    %rbx
   400541: mov
                    %rdx,%rbx
   400544:
                   400550 <mult2>
            qmj
.L1 400549: mov
                   %rax, (%rbx)
   40054c: pop
                   %rbx
   40054d: retq
 400550 <mult2>:
   400550:
                     %rdi,%rax
             mov
   400553:
             imul
                     %rsi,%rax
   400557:
             jmp
                     .L1
```

Using Stack for Function Call and Return

- Procedure call: call label
 - Push return address on stack
 - Jump to label
- Return address:
 - Address of the next instruction right after call (400549 here)
- Procedure return: ret
 - Pop address from stack
 - Jump to address

```
400540 <multstore>:
 400540: push
                %rbx
 400541: mov
                %rdx,%rbx
 400544: callq
                400550 <mult2>
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400550 <mult2>:
 400550:
                 %rdi,%rax
          mov
 400553:
          imul
                 %rsi,%rax
 400557:
          retq
```

```
(Memory)
400540 <multstore>:
                                        0x138
                                        0x130
  400544: callq 400550 <mult2>
                                        0x128
  400549: mov
                  %rax, (%rbx)
                                        0x120
400550 <mult2>:
                                                  0x120
                                         %rsp
  400550:
                   %rdi,%rax
          mov
                                         %rip
                                                0 \times 400544
  400557:
           reta
```

```
(Memory)
400540 <multstore>:
                                          0x138
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                                          0x120
                                          0x118
                                                  0 \times 400549
400550 <mult2>:
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                                                   0x118
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                    %rdi,%rax
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                                           %rip
                                                  0 \times 400544
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            reta
```

```
400540 <multstore>:
                                         0x138
                                         0x130
  400544: callq 400550 <mult2>
                                         0x128
  400549: mov
                  %rax, (%rbx)
                                         0x120
                                         0x118
                                                 0x400549
400550 <mult2>:
                                                   0x118
                                           %rsp
  400550:
                   %rdi,%rax
          mov
                                           %rip
                                                 0 \times 400544
  400557:
            reta
```

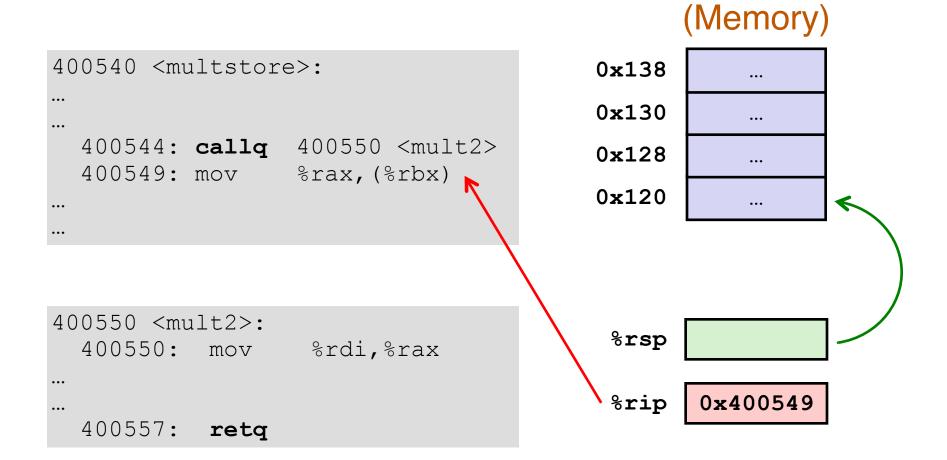
Stack

(Memory)

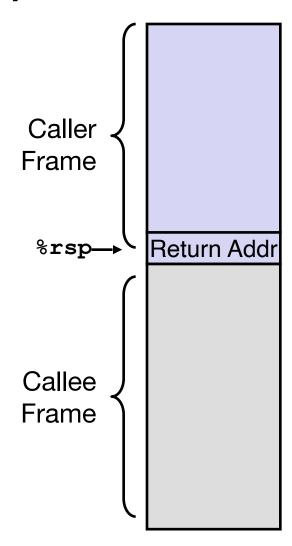
(Memory) 400540 <multstore>: 0x1380x130400544: callq 400550 <mult2> 0x128400549: mov %rax, (%rbx) 0x1200x118 0×400549 400550 <mult2>: 0x118 %rsp %rdi,%rax 400550: mov %rip 0×400550 400557: reta

(Memory) 400540 <multstore>: 0x1380x130400544: callq 400550 <mult2> 0x128400549: mov %rax, (%rbx) 0x1200x118 0×400549 400550 <mult2>: 0x118 %rsp 400550: %rdi,%rax mov 0×400557 %rip 400557: retq

```
(Memory)
400540 <multstore>:
                                         0x138
                                         0x130
  400544: callq
                  400550 <mult2>
                                         0x128
  400549: mov
                  %rax, (%rbx)
                                         0x120
400550 <mult2>:
                                                  0x120
                                          %rsp
  400550:
                   %rdi,%rax
          mov
                                                 0 \times 400549
                                          %rip
  400557:
           retq
```



Stack Frame (So Far...)



Today: How to Implement Function Call

- What are functions and why do we use them?
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Passing Function Arguments

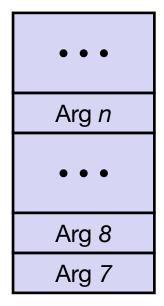
- Two choices: memory or registers
 - Registers are faster, but have limited amount

Passing Function Arguments

- Two choices: memory or registers
 - Registers are faster, but have limited amount
- x86-64 convention (Part of the Calling Conventions):
 - First 6 arguments in registers, in specific order
 - The rest are pushed to stack
 - Return value is always in %rax

Registers

%rdi %rsi %rdx %rcx %r8 %r9

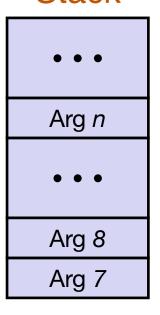


Passing Function Arguments

- Two choices: memory or registers
 - Registers are faster, but have limited amount
- x86-64 convention (Part of the Calling Conventions):
 - First 6 arguments in registers, in specific order
 - The rest are pushed to stack
 - Return value is always in %rax
- Just conventions, not laws
 - Not necessary if you write both caller and callee as long as the caller and callee agree
 - But is necessary to interface with others' code

Registers

%rdi
%rsi
%rdx
%rcx
%r8
%r9



Function Call Data Flow Example

```
void multstore
  (long x, long y, long *res) {
    long t = mult2(x, y);
    *res = t;
}
...
long mult2
  (long a, long b)
{
    long s = a * b;
    return s;
}
```

%rdi %rsi %rdx %rcx %r8

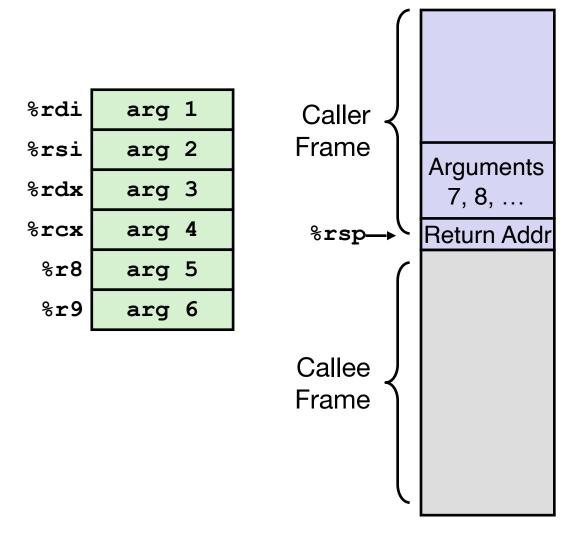
Function Call Data Flow Example

```
%rdi
%rsi
%rdx
%rcx
%r8
%r9
```

```
void multstore
  (long x, long y, long *res) {
    long t = mult2(x, y);
    *res = t;
}
...
long mult2
    (long a, long b)
    {
    long s = a * b;
    return s;
    # t i
```

```
0000000000400540 <multstore>:
 # x in %rdi, y in %rsi, res in %rdx
 400541: movq %rdx, %rbx
 400544: callq 400550 <mult2>
 # t in %rax
 400549: movq %rax, (%rbx)
0000000000400550 <mult2>:
 # a in %rdi, b in %rsi
 400550: movq %rdi,%rax
 400553: imul %rsi,%rax
 # s in %rax
 400557: retq
```

Stack Frame (So Far...)



Today: How to Implement Function Call

- What are functions and why do we use them?
- General idea of implementing functions: Stack
- Passing control
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Managing Function Local Variables

- Two ways: registers and memory (stack)
- Registers are faster, but limited. Memory is slower, but large. Smart compilers will optimize the usage.
- We will show different uses.
 Compiler optimizations later in the course. Take 255/455.

```
long incr(long *p, long val) {
    long x = *p;
    long y = x + val;
    *p = y;
    return x;
}
```

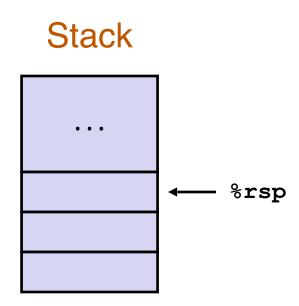
Register Example: incr

Register	Use(s)
%rdi	Argument p
%rsi	Argument val , y
%rax	x, Return value

```
long incr(long *p, long val) {
    long x = *p;
    long y = x + val;
    *p = y;
    return x;
}
```

```
incr:
  movq (%rdi), %rax
  addq %rax, %rsi
  movq %rsi, (%rdi)
  ret
```

```
long call_add() {
    long v1 = 15213;
    long v2 = 3000;
    long v3 = add(&v1, &v2);
    return v2+v3;
}
```



```
long call_add() {
    long v1 = 15213;
    long v2 = 3000;
    long v3 = add(&v1, &v2);
    return v2+v3;
}
```



```
long call_add() {
    long v1 = 15213;
    long v2 = 3000;
    long v3 = add(&v1, &v2);
    return v2+v3;
}
```

```
call_add:
    subq $16, %rsp
    movq $15213, (%rsp)
    movq $3000, 8(%rsp)
    leaq (%rsp), %rdi
    leaq 8(%rsp), %rsi
    call add
    addq 8(%rsp), %rax
    addq $16, %rsp
    ret
```

Stack ... 3000 ← %rsp+8 15213 ← %rsp

```
long call_add() {
    long v1 = 15213;
    long v2 = 3000;
    long v3 = add(&v1, &v2);
    return v2+v3;
}
```

```
call_add:
    subq $16, %rsp

movq $15213, (%rsp)

movq $3000, 8(%rsp)

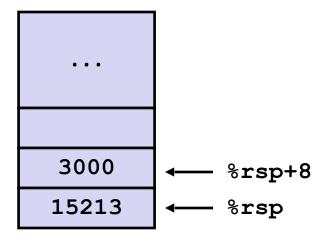
leaq (%rsp), %rdi

leaq 8(%rsp), %rsi

call add
    addq 8(%rsp), %rax

addq $16, %rsp

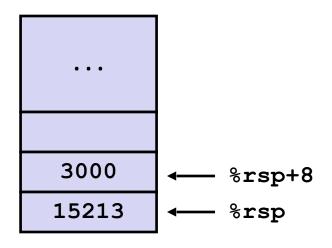
ret
```



Register	Value(s)
%rdi	&v1
%rsi	&v2

```
long call_add() {
    long v1 = 15213;
    long v2 = 3000;
    long v3 = add(&v1, &v2);
    return v2+v3;
}
```

```
call_add:
    subq $16, %rsp
    movq $15213, (%rsp)
    movq $3000, 8(%rsp)
    leaq (%rsp), %rdi
    leaq 8(%rsp), %rsi
    call add
    addq 8(%rsp), %rax
    addq $16, %rsp
    ret
```

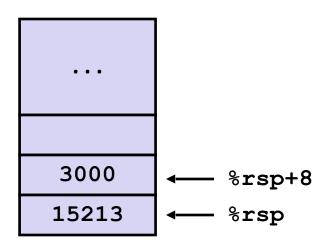


Register	Value(s)
%rdi	&v1
%rsi	&v2
%rax	18213

Stack Example: call add

```
long call_add() {
    long v1 = 15213;
    long v2 = 3000;
    long v3 = add(&v1, &v2);
    return v2+v3;
}
```

```
call_add:
    subq $16, %rsp
    movq $15213, (%rsp)
    movq $3000, 8(%rsp)
    leaq (%rsp), %rdi
    leaq 8(%rsp), %rsi
    call add
    addq 8(%rsp), %rax
    addq $16, %rsp
    ret
```



Register	Value(s)
%rdi	&v1
%rsi	&v2
%rax	21213

Stack Example: call add

```
long call_add() {
    long v1 = 15213;
    long v2 = 3000;
    long v3 = add(&v1, &v2);
    return v2+v3;
}
```

```
call_add:
    subq $16, %rsp
    movq $15213, (%rsp)
    movq $3000, 8(%rsp)
    leaq (%rsp), %rdi
    leaq 8(%rsp), %rsi
    call add
    addq 8(%rsp), %rax
    addq $16, %rsp
    ret
```

Stack 3000 ← %rsp+8

15213

%rsp

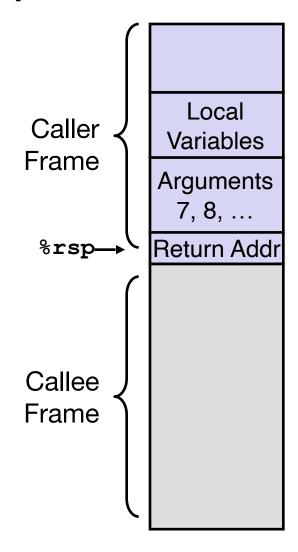
Stack Example: call add

```
long call_add() {
    long v1 = 15213;
    long v2 = 3000;
    long v3 = add(&v1, &v2);
    return v2+v3;
}
```

```
call_add:
    subq $16, %rsp
    movq $15213, (%rsp)
    movq $3000, 8(%rsp)
    leaq (%rsp), %rdi
    leaq 8(%rsp), %rsi
    call add
    addq 8(%rsp), %rax
    addq $16, %rsp
    ret
```

Stack ... --- %rsp 3000 15213

Stack Frame (So Far...)



Any issue with using registers for temporary storage?

Caller

```
yoo:
...
movq $15213, %rdx
call who
addq %rdx, %rax
...
ret
```

Callee

```
who:
...
subq $18213, %rdx
...
ret
```

- Any issue with using registers for temporary storage?
 - Contents of register %rdx overwritten by who ()

Caller

```
yoo:
    ...
    movq $15213, %rdx
    call who
    addq %rdx, %rax
    ...
    ret
```

Callee

```
who:
...
subq $18213, %rdx
...
ret
```

- Any issue with using registers for temporary storage?
 - Contents of register %rdx overwritten by who ()
 - This could be trouble → Need some coordination.

Caller

```
yoo:
...
movq $15213, %rdx
call who
addq %rdx, %rax
...
ret
```

Callee

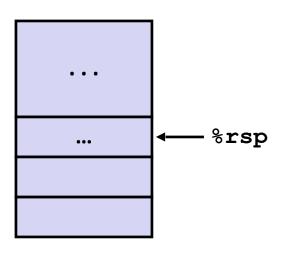
```
who:
...
subq $18213, %rdx
...
ret
```

Common conventions

- "Caller Saved"
 - Caller saves temporary values in its frame (on the stack) before the call
 - Callee is then free to modify their values
- "Callee Saved"
 - Callee saves temporary values in its frame before using
 - Callee restores them before returning to caller
 - Caller can safely assume that register values won't change after the function call

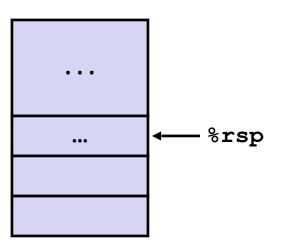
- Conventions used in x86-64 (Part of the Calling Conventions)
 - · Some registers are saved by caller, some are by callee.
 - Caller saved: %rdi, %rsi, %rdx, %rcx, %r8, %r9, %r10, %r11
 - Callee saved: %rbx, %rbp, %r12, %r13, %14, %r15
 - %rax holds return value, so implicitly caller saved
 - %rsp is the stack pointer, so implicitly callee saved

```
long call_incr2(long x) {
    long v1 = 15213;
    long v2 = incr(&v1, 3000);
    return x+v2;
}
```



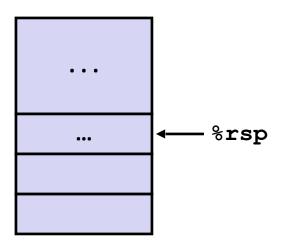
```
long call_incr2(long x) {
    long v1 = 15213;
    long v2 = incr(&v1, 3000);
    return x+v2;
}
```

```
call_incr2:
  pushq %rbx
  pushq $15213
  movq %rdi, %rbx
  movl $3000, %esi
  leaq (%rsp), %rdi
  call incr
  addq %rbx, %rax
  addq $8, %rsp
  popq %rbx
  ret
```



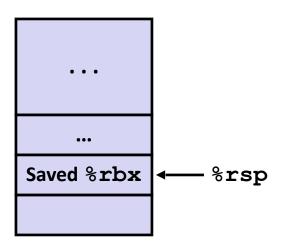
```
long call_incr2(long x) {
    long v1 = 15213;
    long v2 = incr(&v1, 3000);
    return x+v2;
}
```

```
call_incr2:
  pushq %rbx
  pushq $15213
  movq %rdi, %rbx
  movl $3000, %esi
  leaq (%rsp), %rdi
  call incr
  addq %rbx, %rax
  addq $8, %rsp
  popq %rbx
  ret
```



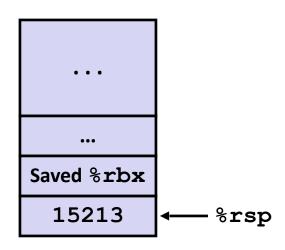
```
long call_incr2(long x) {
    long v1 = 15213;
    long v2 = incr(&v1, 3000);
    return x+v2;
}
```

```
call_incr2:
  pushq %rbx
  pushq $15213
  movq %rdi, %rbx
  movl $3000, %esi
  leaq (%rsp), %rdi
  call incr
  addq %rbx, %rax
  addq $8, %rsp
  popq %rbx
  ret
```



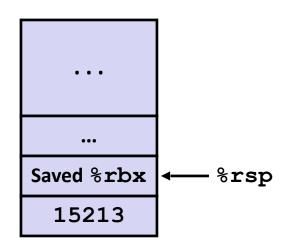
```
long call_incr2(long x) {
    long v1 = 15213;
    long v2 = incr(&v1, 3000);
    return x+v2;
}
```

```
call_incr2:
  pushq %rbx
  pushq $15213
  movq %rdi, %rbx
  movl $3000, %esi
  leaq (%rsp), %rdi
  call incr
  addq %rbx, %rax
  addq $8, %rsp
  popq %rbx
  ret
```



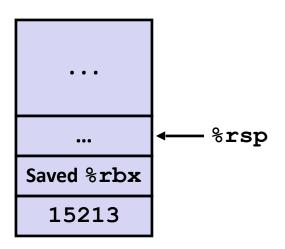
```
long call_incr2(long x) {
    long v1 = 15213;
    long v2 = incr(&v1, 3000);
    return x+v2;
}
```

```
call_incr2:
  pushq %rbx
  pushq $15213
  movq %rdi, %rbx
  movl $3000, %esi
  leaq (%rsp), %rdi
  call incr
  addq %rbx, %rax
  addq $8, %rsp
  popq %rbx
  ret
```



```
long call_incr2(long x) {
    long v1 = 15213;
    long v2 = incr(&v1, 3000);
    return x+v2;
}
```

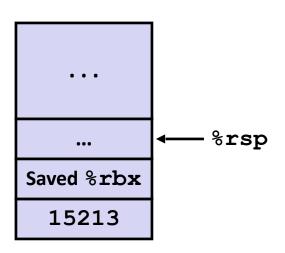
```
call_incr2:
  pushq %rbx
  pushq $15213
  movq %rdi, %rbx
  movl $3000, %esi
  leaq (%rsp), %rdi
  call incr
  addq %rbx, %rax
  addq $8, %rsp
  popq %rbx
  ret
```



```
long call_incr2(long x) {
    long v1 = 15213;
    long v2 = incr(&v1, 3000);
    return x+v2;
}
```

```
call_incr2:
  pushq %rbx
  pushq $15213

movq %rdi, %rbx
  movl $3000, %esi
  leaq (%rsp), %rdi
  call incr
  addq %rbx, %rax
  addq $8, %rsp
  popq %rbx
  ret
```



- call_incr2 needs to save %rbx (callee-saved) because it will modify its value
- It can safely use %rbx after call incr because incr will have to save %rbx if it needs to use it (again, %rbx is callee saved)

Stack Frame: Putting It Together

