

CSC 252: Computer Organization

Spring 2026: Lecture 8

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University of Rochester

Announcement

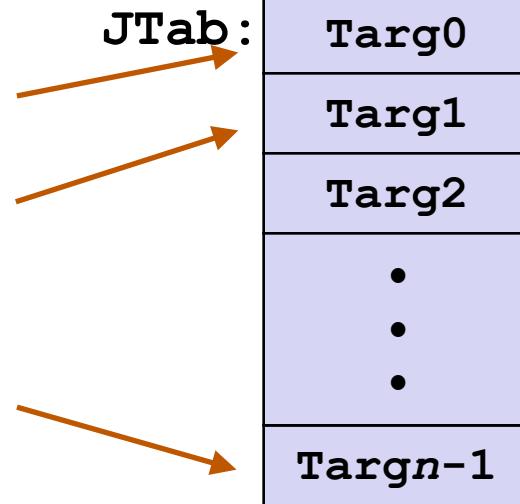
- You might still have three slip days.
- Read the instructions before getting started!!!
 - You get 1/4 point off for every wrong answer
 - Maxed out at 10
- TAs are best positioned to answer your questions about programming assignments!!!
- Programming assignments do NOT repeat the lecture materials. They ask you to synthesize what you have learned from the lectures and work out something new.
- Logics and arithmetics problem set: <https://www.cs.rochester.edu/courses/252/spring2026/handouts.html>.
 - Not to be turned in.

Summary

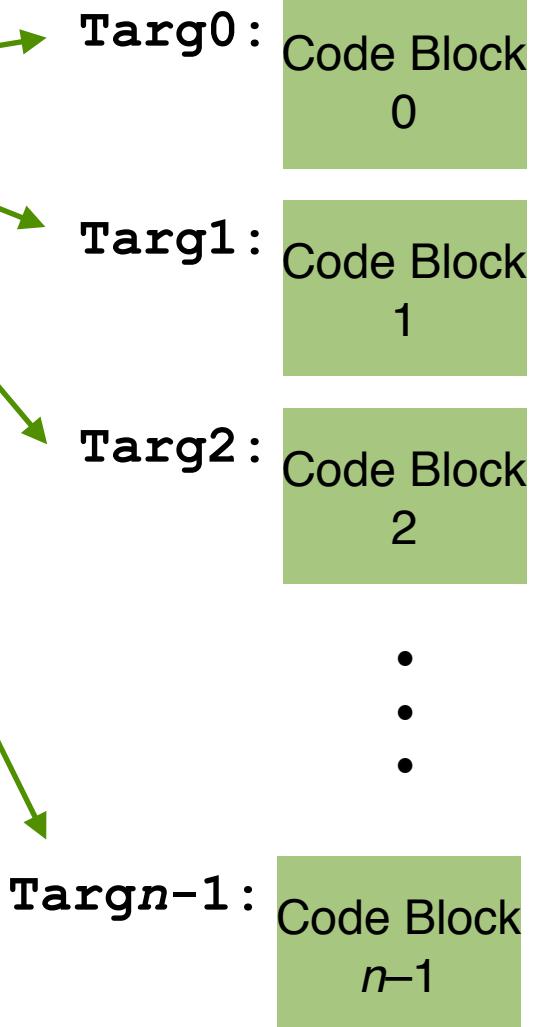
Switch Form

```
switch(x) {  
    case val_0:  
        Block 0  
    case val_1:  
        Block 1  
    ....  
    case val_n-1:  
        Block n-1  
}
```

Jump Table



Jump Targets



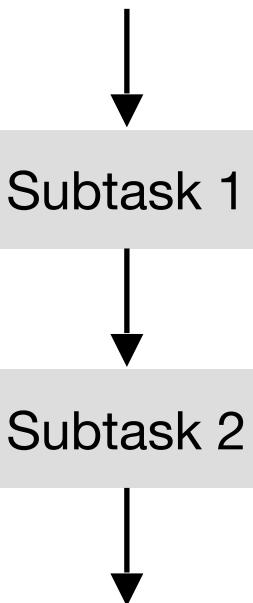
- Each code block starts from a unique address (**Targ 0** , **Targ 1** , ...)
- Jump table stores all the target address
- Use the case value to index into the jump table to find where to jump to

Not The Only Way

- Jump table might not the most efficient implementation; certainly not the only way to implement switch-case.
- What if x can take a very large value range. Do we need to have a giant jump table?
- Let's say x can be any integer from 1 to 1 million, but anything between 8 and 1 million fall back to the default case. Can we avoid a 1 million entry jump table (which isn't too bad if you calculate the size)?
 - Have an if-else check first followed by an 8-entry table.

Summary

Sequential



```
a = x + y;  
y = a - c;  
...
```

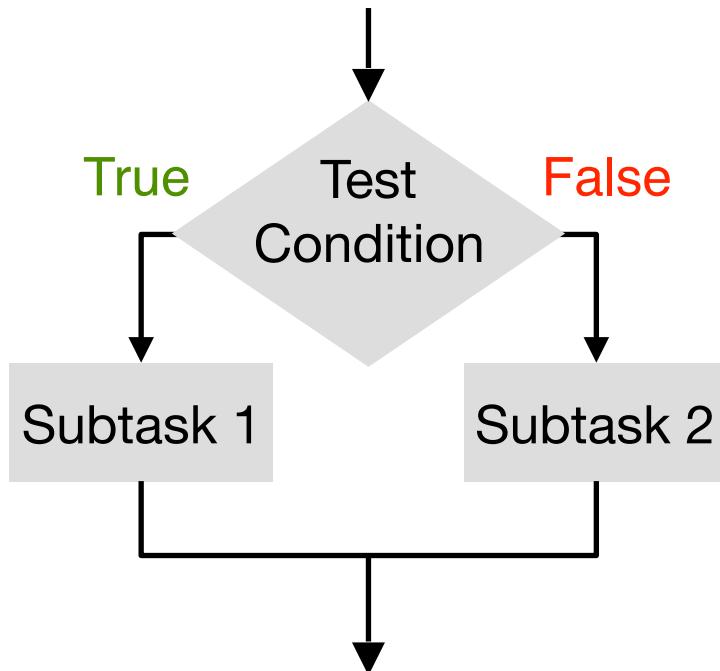
Memory

```
.section .text  
...  
<subtask 1>  
...  
...  
<subtask 2>  
...
```



Summary

Conditional



```
if (x > y) r = x - y;  
else r = y - x;
```

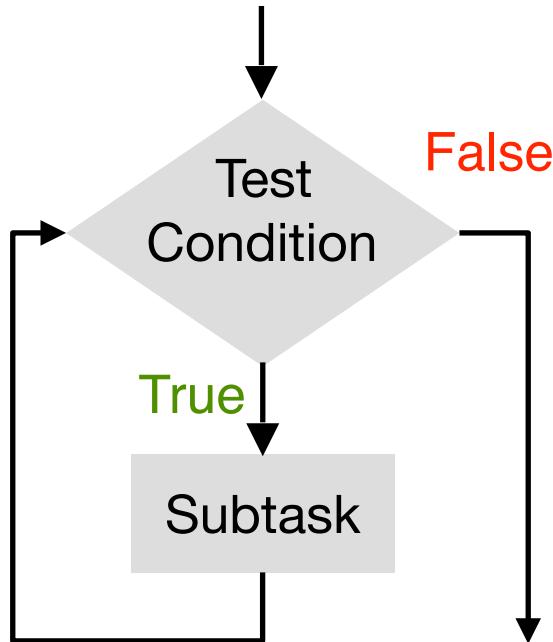
Memory

```
.section .text  
...  
cmpq  
jle .L2  
.L1 <subtask 1>  
...  
jmp .done  
.L2 <subtask 2>  
...  
.done
```



Summary

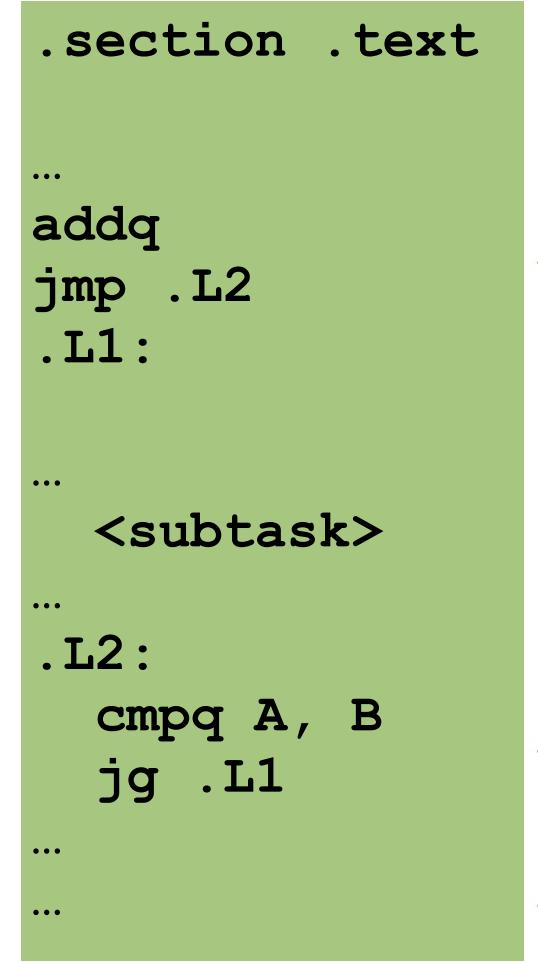
Iterative



```
while (x > 0) {  
    x--;  
}
```

Memory

```
.section .text  
...  
addq  
jmp .L2  
.L1:  
...  
<subtask>  
...  
.L2:  
    cmpq A, B  
    jg .L1  
...
```



Today: How to Implement Function Call

- What are functions and why do we use them?
- General idea of implementing functions: Stack
- Passing control
- Passing data
- Managing local data

Functions Declaration in C

Declaration (also called prototype)

- States return type, name, types of arguments

```
int Factorial(int);
```



type of return value name of function types of all arguments

The diagram illustrates the structure of the function declaration `int Factorial(int);`. Three arrows point from labels below the code to specific parts of the declaration: a red arrow points to the `int` in `int Factorial`, a green arrow points to the word `Factorial`, and a blue arrow points to the `int` in `(int)`.

Function Definition

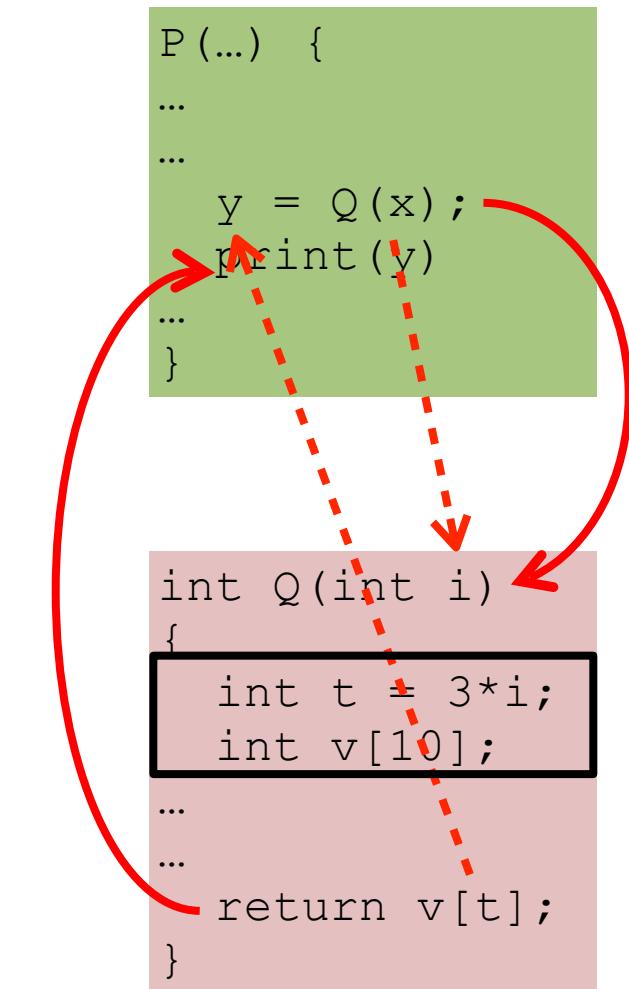
- Must match function declaration
- Implement the functionality of the function

```
int Factorial(int n)
{
    int i;
    int result = 1;
    for (i = 1; i <= n; i++)
        result *= i;
    return result;    ←
}
```

gives control back to
calling function and
returns value

Mechanisms in Procedures

- Passing control
 - To beginning of procedure code
 - Back to return point
- Passing data
 - Procedure arguments
 - Return value
- Local Memory management
 - Allocate during procedure execution
 - Deallocate upon return
- The ISA must provide ways (instructions) to realize all these.



Today: How to Implement Function Call

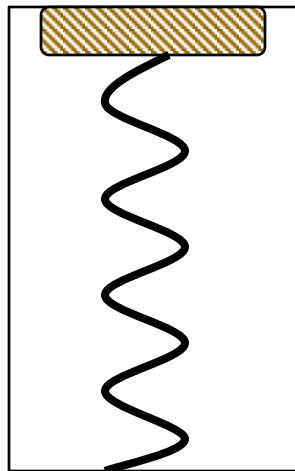
- What are functions and why do we use them?
- General idea of implementing functions: Stack
- Passing control
- Passing data
- Managing local data

General Idea

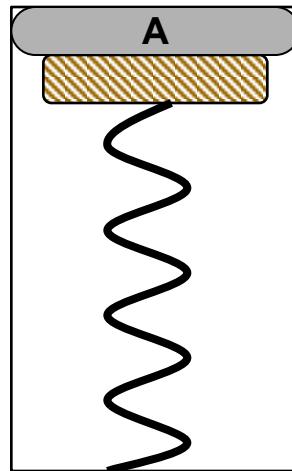
- Frame (Active Record)
 - A frame refers to a piece of memory that contains (almost) all the information needed to execute a function, e.g., arguments and local variables
- When a function is called, create a frame, and push it to the memory
- When a function is returned, pop the frame out of the memory
- Frames are stored in memory in a *stack* fashion

A Physical Stack: A Coin Holder

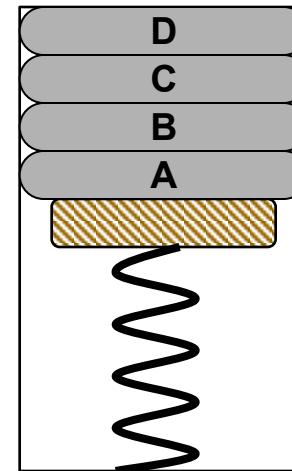
First quarter out is the last quarter in.



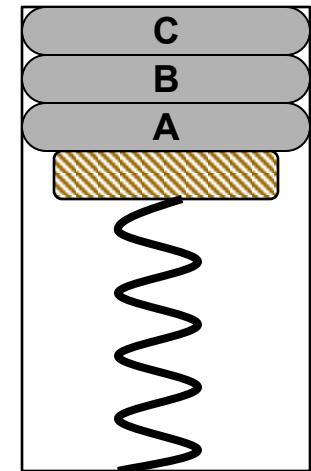
Initial State



After
One Push



After Three
More Pushes



After
One Pop

- Stack is the right data structure for function call / return
 - If A calls B, then B returns before A

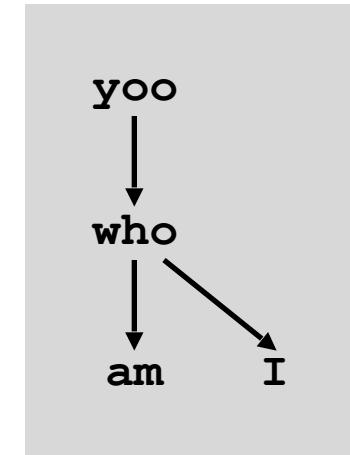
Run-Time Stack During Function Call

```
yoo (...)  
{  
    •  
    •  
    who () ;  
    •  
    return  
}
```

```
who (...)  
{  
    • • •  
    am () ;  
    • • •  
    I () ;  
    return;  
}
```

```
am (...)  
{  
    •  
    •  
    return;  
}
```

Example Call Chain

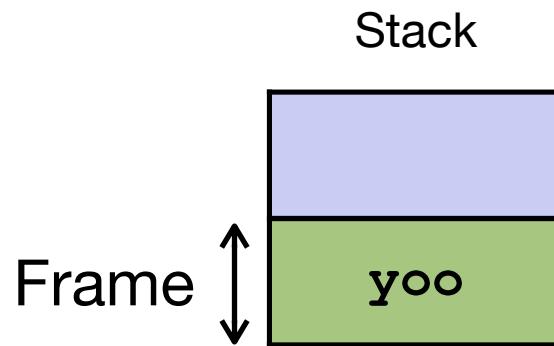
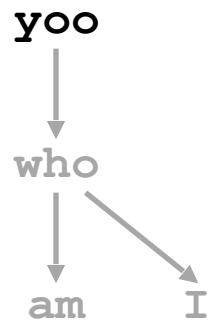


```
I (...)  
{  
    •  
    •  
    return;  
}
```

Run-Time Stack During Function Call

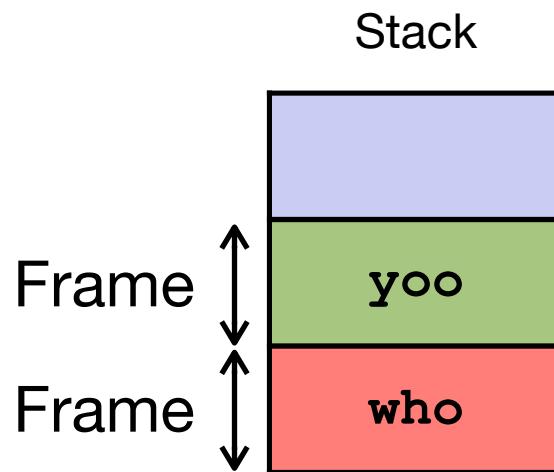
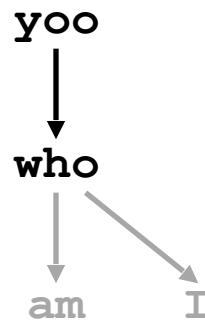


```
yoo (...)  
{  
    •  
    •  
    who () ;  
    •  
    return  
}
```



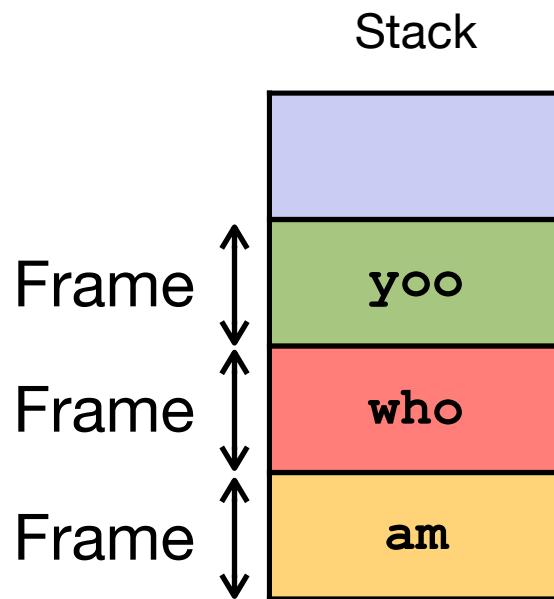
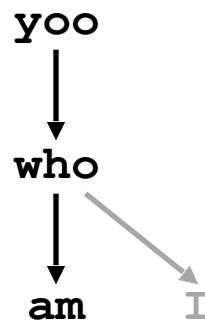
Run-Time Stack During Function Call

```
yoo (...)  
{  
    .  
    who (...)  
    {  
        . . .  
        am ();  
        . . .  
        I ();  
        return;  
    }  
}
```



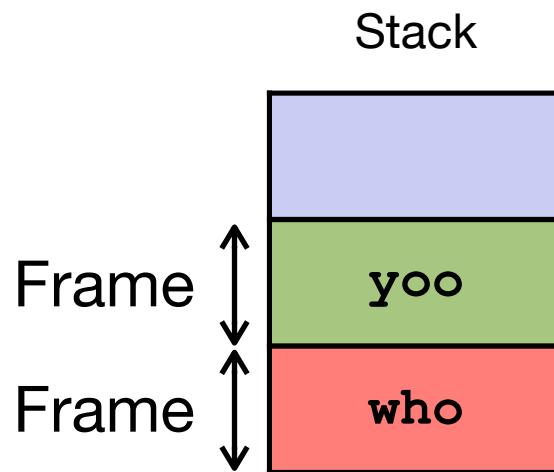
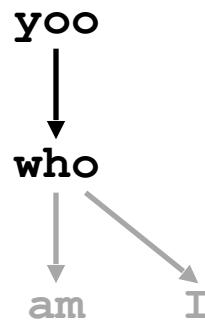
Run-Time Stack During Function Call

```
yoo (...)  
{  
    .  
    who (...)  
    {  
        . . .  
        am (...)  
        {  
            .  
            .  
            .  
        }  
    }  
    return;  
}
```



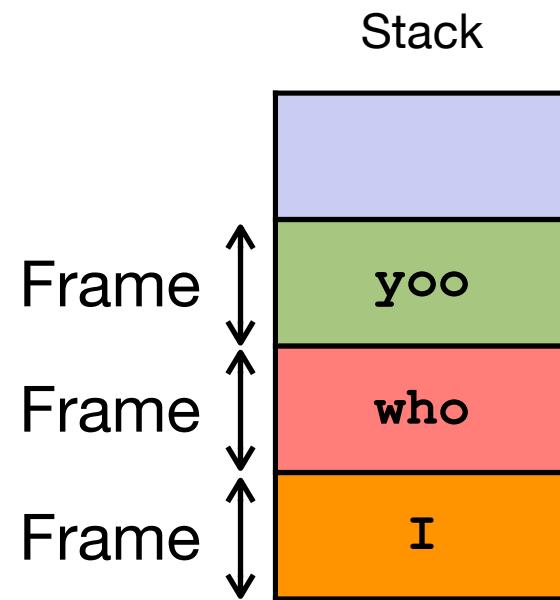
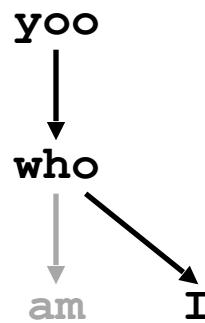
Run-Time Stack During Function Call

```
yoo (...)  
{  
    . . .  
    who (...)  
    {  
        . . .  
        am ();  
        . . .  
    }  
    I ();  
    return;  
}
```



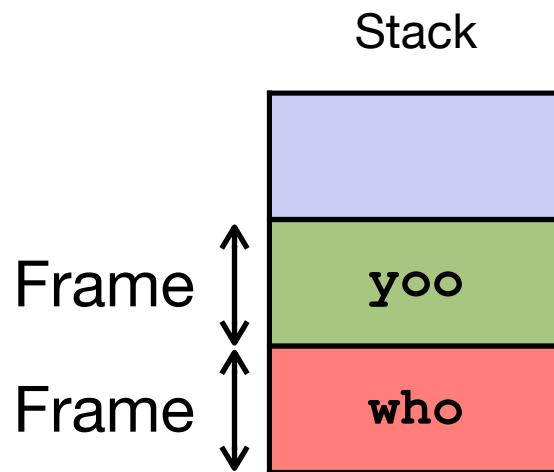
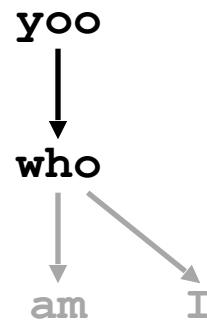
Run-Time Stack During Function Call

```
yoo (...)  
{  
    . . .  
    who (...)  
    {  
        . . .  
        I (...)  
        {  
            . . .  
        }  
    }  
    return;  
}
```



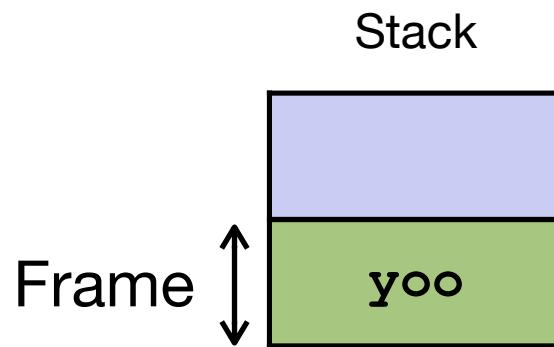
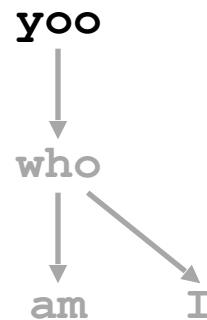
Run-Time Stack During Function Call

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{  
    . . .  
    who (...)  
    {  
        . . .  
        am ();  
        . . .  
        I ();  
        return;  
    }  
}
```



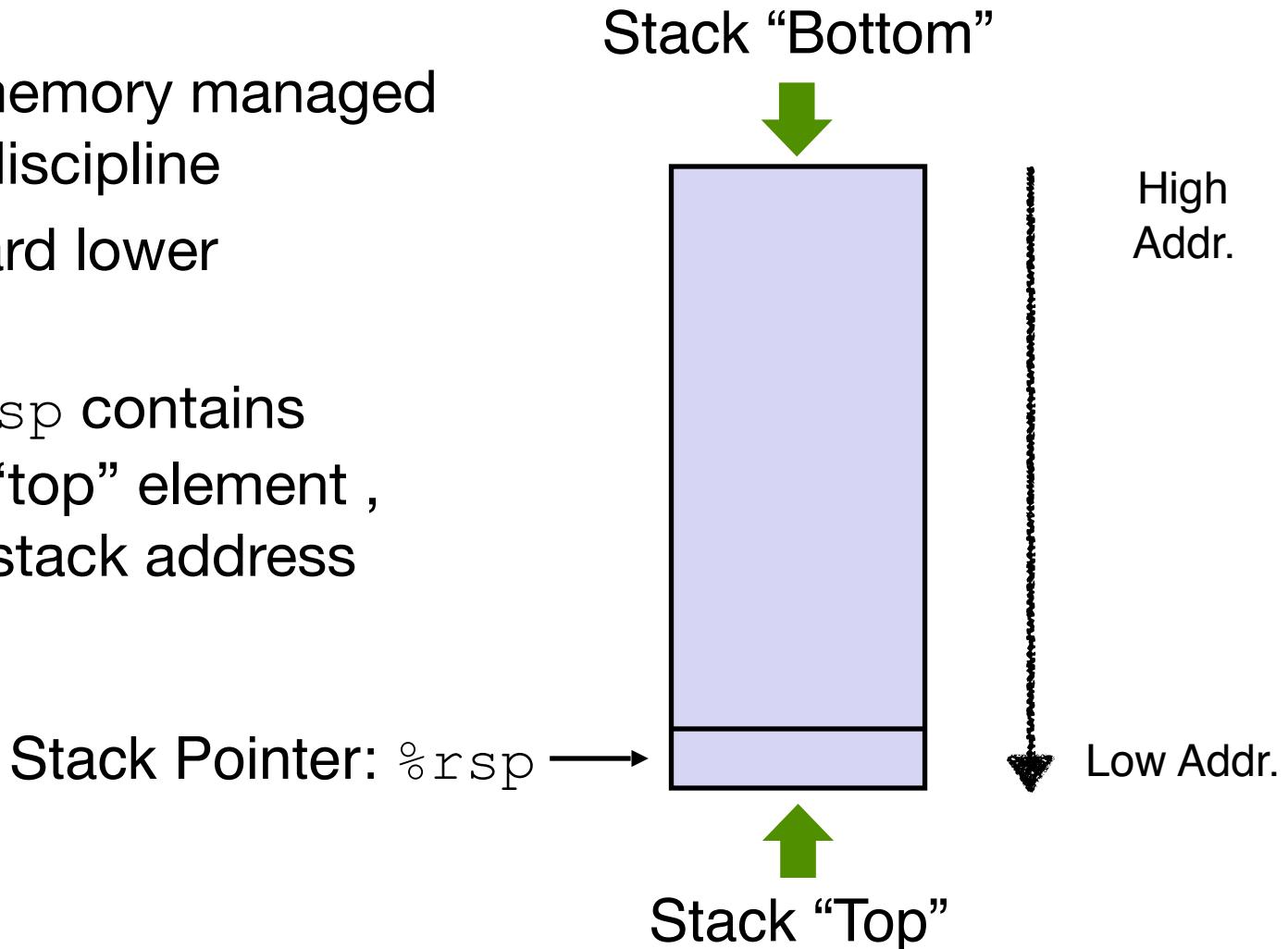
Run-Time Stack During Function Call

```
yoo (...)  
{  
    •  
    •  
    who () ;  
    •  
    return  
}
```



Stack in X86-64

- Region of memory managed with stack discipline
- Grows toward lower addresses
- Register `%rsp` contains address of “top” element , i.e., lowest stack address



x86-64 Stack: Push

- **pushq Src**

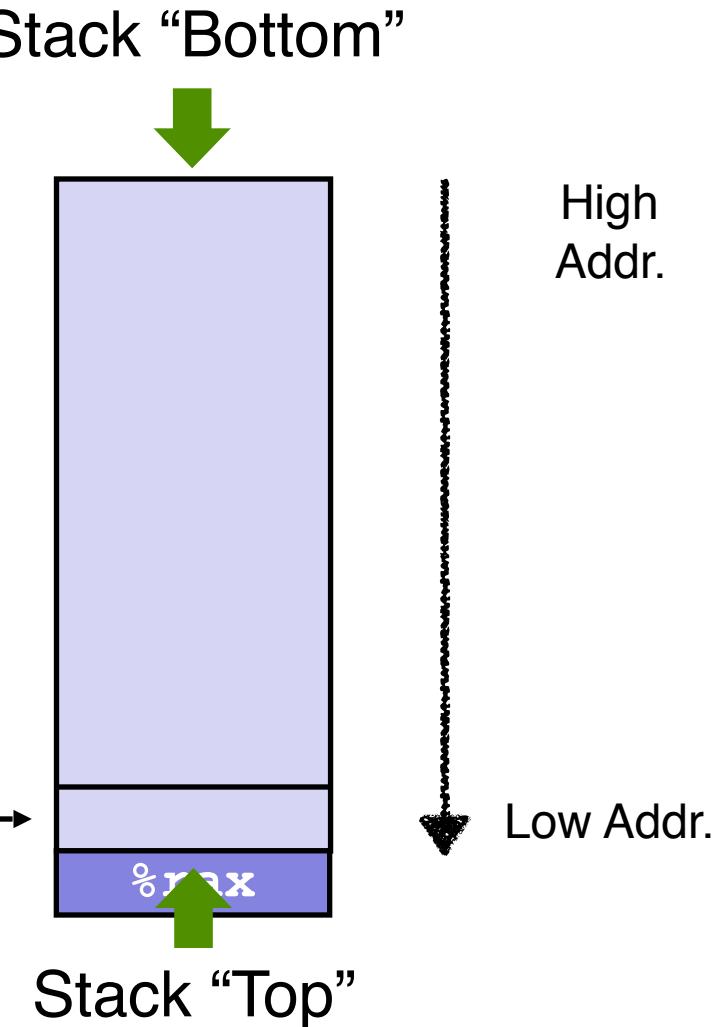
- Fetch operand at *Src*
- Decrement **%rsp** by 8
- Write operand at address given by **%rsp**

- Example: **pushq %rax**

- Same as (in functionality):

- `subq $0x08, %rsp`
- `movq %rax, (%rsp)`

Stack Pointer: **%rsp** →

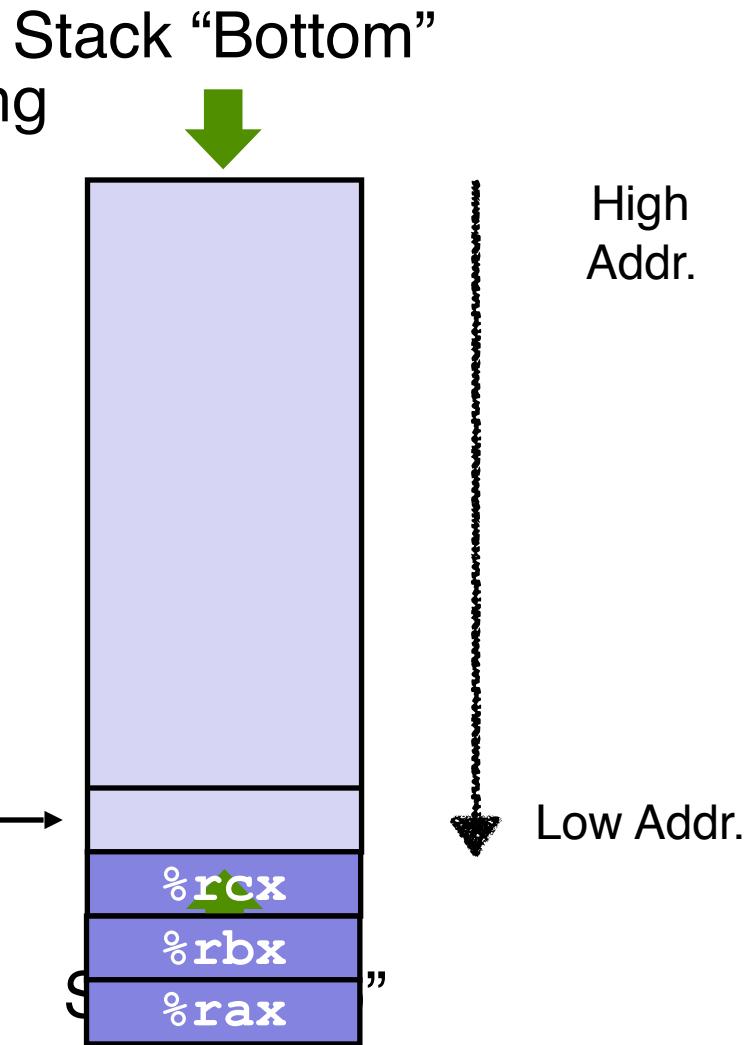


x86-64 Stack: Push

- Sometimes instead of keep pushing multiple items, we could first reserve space on the stack then move items in:

- `subq 0x18, %rsp`
- `movq %rax, (%rsp)`
- `movq %rbx, 8(%rsp)`
- `movq %rcx, 16(%rsp)`

Stack Pointer: `%rsp` →



x86-64 Stack: Pop

- **popq Dest**

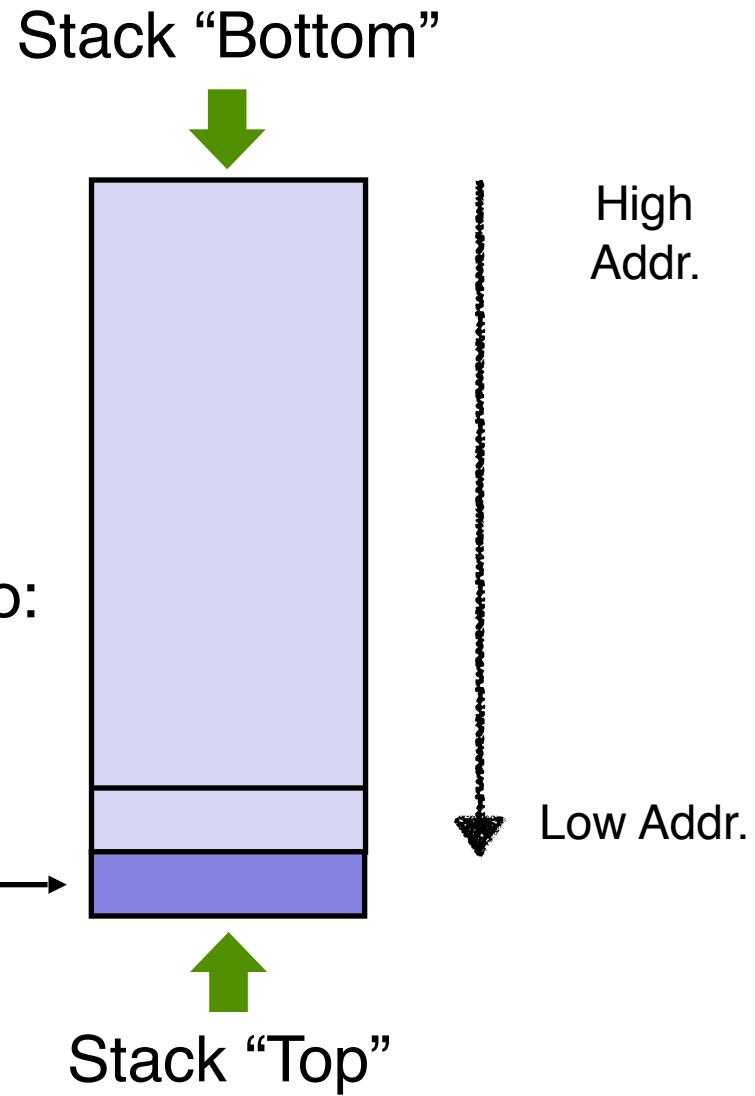
- Read value at address given by `%rsp`
- Increment `%rsp` by 8
- Store value at *Dest* (must be register)

- Example: `popq %rsi`

- If you don't care about saving the popped value, you could simply do:

- `addq $0x08, %rsp`

Stack Pointer: `%rsp` →



Today: How to Implement Function Call

- What are functions and why do we use them?
- General idea of implementing functions: Stack
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Code Examples

```
void multstore
  (long x, long y, long *dest)
{
    long t = mult2(x, y);
    *dest = t;
}

...
long mult2 (long a, long b)
{
    long s = a * b;
    return s;
}
```

```
400540 <multstore>:
400540: push    %rbx
400541: mov     %rdx, %rbx
400544: callq   400550 <mult2>
400549: mov     %rax, (%rbx)
40054c: pop    %rbx
40054d: retq

...
400550 <mult2>:
400550: mov     %rdi, %rax
400553: imul   %rsi, %rax
400557: retq
```

`retq` returns to (by changing the PC) 400549.
But how would `retq` know where to return?

Non-Solution

- Replace `callq` with `jmp`
- assign a label to the instruction next to `callq` (e.g., `.L1`)
- replace `retq` with `jmp .L1`
- Will this work?!
- How about when other functions call `mult2`?

```
400540 <multstore>:  
400540: push    %rbx  
400541: mov     %rdx, %rbx  
400544: jmp     .L1  
400549: mov     %rax, (%rbx)  
40054c: pop    %rbx  
40054d: retq  
  
...  
  
400550 <mult2>:  
400550: mov     %rdi, %rax  
400553: imul    %rsi, %rax  
400557: jmp     .L1
```

Using Stack for Function Call and Return

- **Procedure call:** `call label`
 - Push return address on stack
 - Jump to label
- **Return address:**
 - Address of the next instruction right after call (400549 here)
- **Procedure return:** `ret`
 - Pop address from stack
 - Jump to address

```
400540 <multstore>:  
400540: push    %rbx  
400541: mov     %rdx, %rbx  
400544: callq   400550 <mult2>  
400549: mov     %rax, (%rbx)  
40054c: pop    %rbx  
40054d: retq  
  
...  
  
400550 <mult2>:  
400550: mov     %rdi, %rax  
400553: imul   %rsi, %rax  
400557: retq
```

Function Call Example

```
400540 <multstore>:
```

```
...
```

```
...
```

```
400544: callq 400550 <mult2>  
400549: mov    %rax, (%rbx)
```

```
...
```

```
...
```

```
400550 <mult2>:
```

```
400550: mov    %rdi, %rax
```

```
...
```

```
...
```

```
400557: retq
```

Stack
(Memory)

0x138

...

0x130

...

0x128

...

0x120

...

0x118

0x400549

0x118

0x400549

%rsp

0x120

%rip

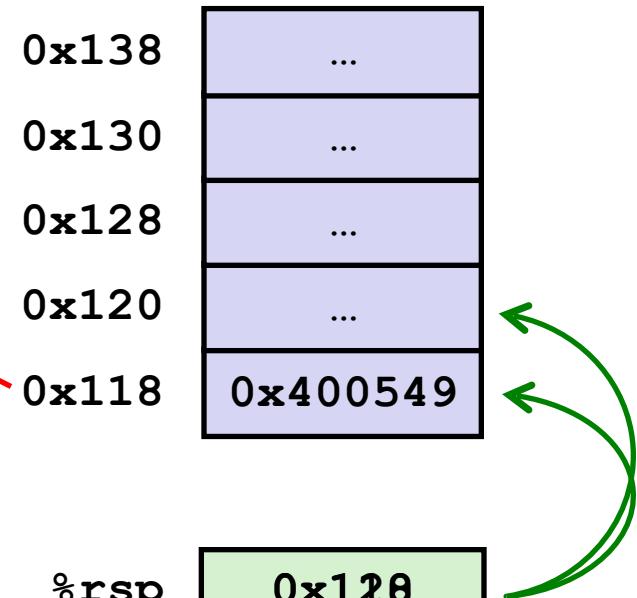
0x400540



Function Call Example

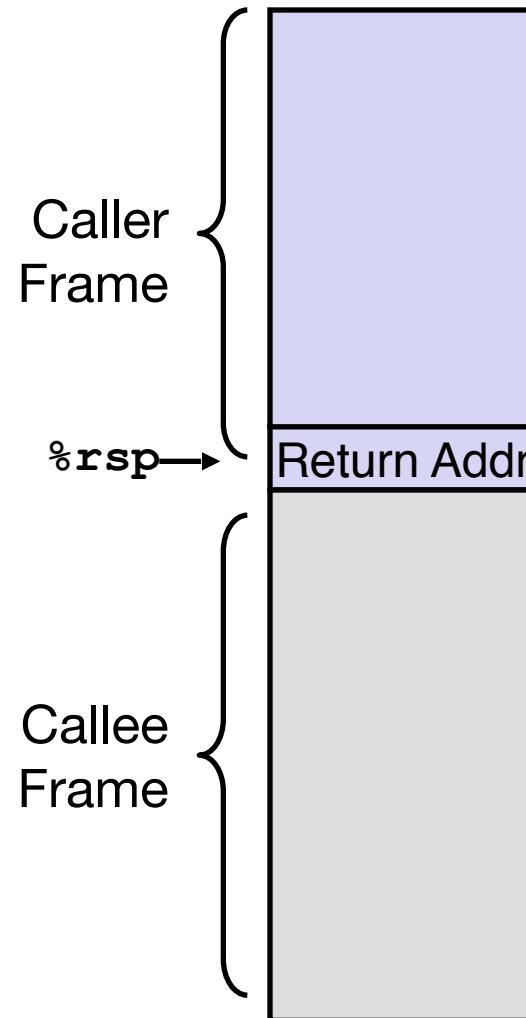
```
400540 <multstore>:  
...  
...  
400544: callq 400550 <mult2>  
400549: mov    %rax, (%rbx)  
...  
...
```

Stack
(Memory)



```
400550 <mult2>:  
400550: mov    %rdi, %rax  
...  
...  
400557: retq
```

Stack Frame (So Far...)



Today: How to Implement Function Call

- What are functions and why do we use them?
- General idea of implementing functions: Stack
- Passing control
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Passing Function Arguments

- Two choices: memory or registers
 - Registers are faster, but have limited amount
- x86-64 convention (Part of the *Calling Conventions*):
 - First 6 arguments in registers, in specific order
 - The rest are pushed to stack
 - *Return value* is always in `%rax`
- Just conventions, not laws
 - Not necessary if you write both caller and callee as long as the caller and callee agree
 - But is necessary to interface with others' code

`%rdi`

`%rsi`

`%rdx`

`%rcx`

`%r8`

`%r9`

Stack

• • •

Arg n

• • •

Arg 8

Arg 7

Function Call Data Flow Example

```
void multstore
  (long x, long y, long *res) {
    long t = mult2(x, y);
    *res = t;
}

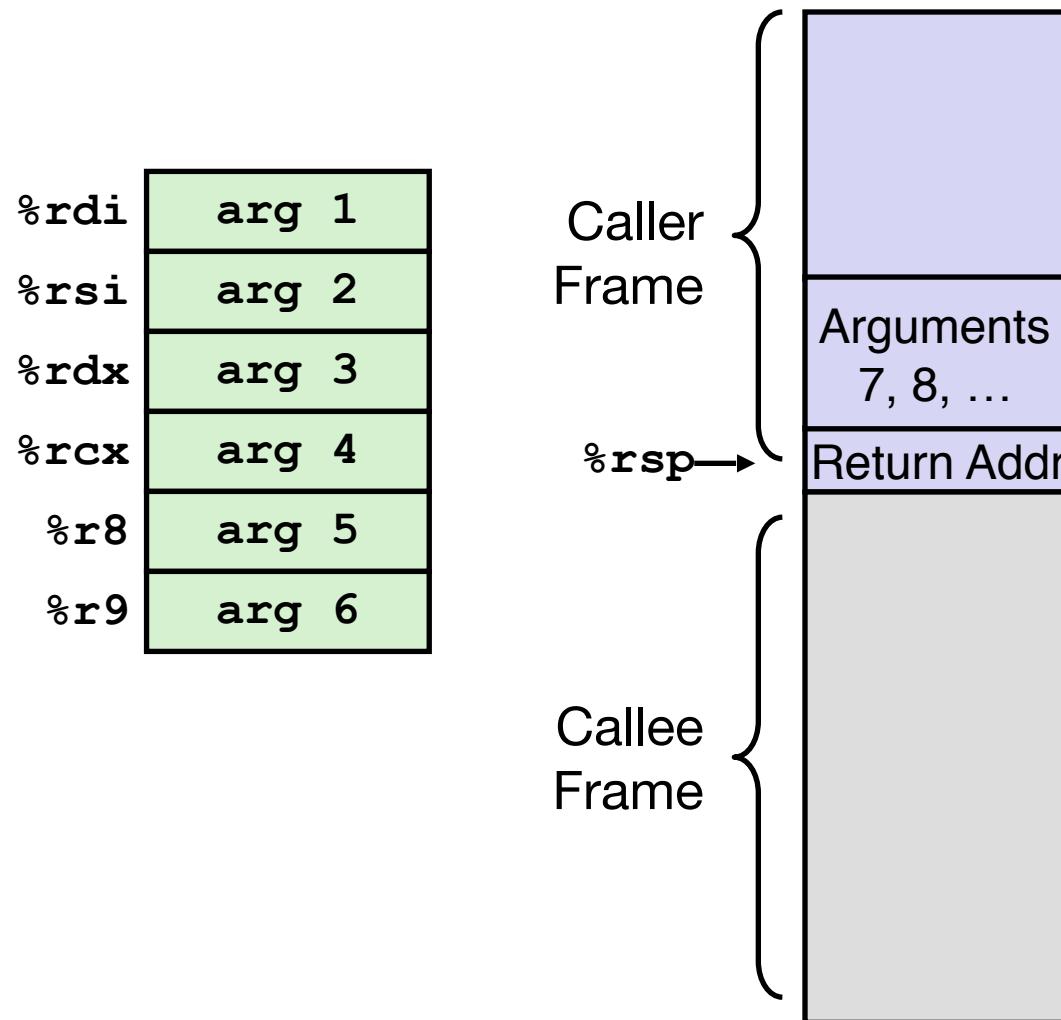
...
long mult2
  (long a, long b)
{
  long s = a * b;
  return s;
}
```

%rdi
%rsi
%rdx
%rcx
%r8
%r9

```
0000000000400540 <multstore>:
  # x in %rdi, y in %rsi, res in %rdx
...
  400541: movq    %rdx,%rbx
  400544: callq   400550 <mult2>
  # t in %rax
  400549: movq    %rax,(%rbx)
...

0000000000400550 <mult2>:
  # a in %rdi, b in %rsi
  400550: movq    %rdi,%rax
  400553: imul    %rsi,%rax
  # s in %rax
  400557: retq
```

Stack Frame (So Far...)



Today: How to Implement Function Call

- What are functions and why do we use them?
- General idea of implementing functions: Stack
- Passing control
- Passing data
- Managing local data

Managing Function Local Variables

- Two ways: registers and memory (stack)
- Registers are faster, but limited. Memory is slower, but large. Smart compilers will optimize the usage.
- We will show different uses. Compiler optimizations later in the course. Take 255/455.

```
long incr(long *p, long val) {  
    long x = *p;  
    long y = x + val;  
    *p = y;  
    return x;  
}
```

Register Example: incr

Register	Use(s)
%rdi	Argument p
%rsi	Argument val , y
%rax	x , Return value

```
long incr(long *p, long val) {  
    long x = *p;  
    long y = x + val;  
    *p = y;  
    return x;  
}
```

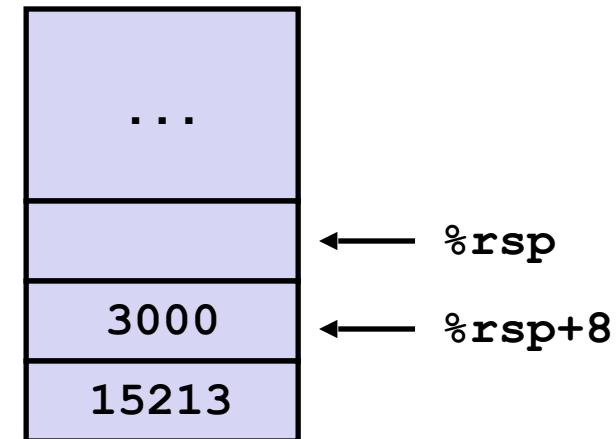
```
incr:  
    movq    (%rdi), %rax  
    addq    %rax, %rsi  
    movq    %rsi, (%rdi)  
    ret
```

Stack Example: call_add

```
long call_add() {  
    long v1 = 15213;  
    long v2 = 3000;  
    long v3 = add(&v1, &v2);  
    return v2+v3;  
}
```

```
call_add:  
    subq    $16, %rsp  
    movq    $15213, (%rsp)  
    movq    $3000, 8(%rsp)  
    leaq    (%rsp), %rdi  
    leaq    8(%rsp), %rsi  
    call    add  
    addq    8(%rsp), %rax  
    addq    $16, %rsp  
    ret
```

Stack

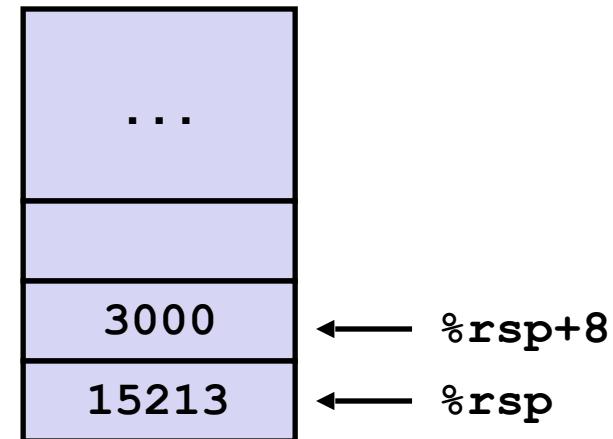


Stack Example: call_add

```
long call_add() {  
    long v1 = 15213;  
    long v2 = 3000;  
    long v3 = add(&v1, &v2);  
    return v2+v3;  
}
```

```
call_add:  
    subq    $16, %rsp  
    movq    $15213, (%rsp)  
    movq    $3000, 8(%rsp)  
    leaq    (%rsp), %rdi  
    leaq    8(%rsp), %rsi  
    call    add  
    addq    8(%rsp), %rax  
    addq    $16, %rsp  
    ret
```

Stack



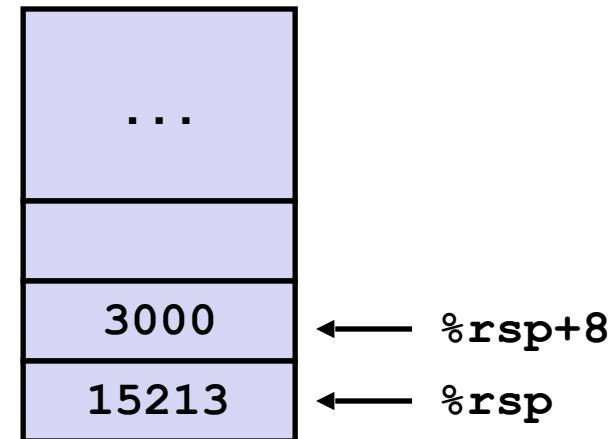
Register	Value(s)
%rdi	&v1
%rsi	&v2

Stack Example: call_add

```
long call_add() {  
    long v1 = 15213;  
    long v2 = 3000;  
    long v3 = add(&v1, &v2);  
    return v2+v3;  
}
```

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call_add:  
    subq    $16, %rsp  
    movq    $15213, (%rsp)  
    movq    $3000, 8(%rsp)  
    leaq    (%rsp), %rdi  
    leaq    8(%rsp), %rsi  
    call    add  
    addq    8(%rsp), %rax  
    addq    $16, %rsp  
    ret
```

Stack



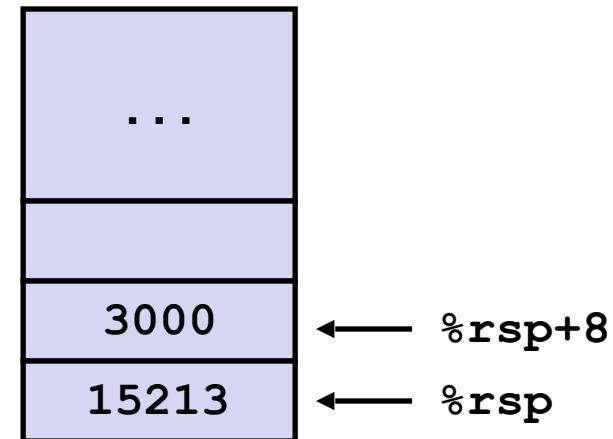
Register	Value(s)
%rdi	&v1
%rsi	&v2
%rax	18213

Stack Example: call_add

```
long call_add() {  
    long v1 = 15213;  
    long v2 = 3000;  
    long v3 = add(&v1, &v2);  
    return v2+v3;  
}
```

```
call_add:  
    subq    $16, %rsp  
    movq    $15213, (%rsp)  
    movq    $3000, 8(%rsp)  
    leaq    (%rsp), %rdi  
    leaq    8(%rsp), %rsi  
    call    add  
    addq    8(%rsp), %rax  
    addq    $16, %rsp  
    ret
```

Stack



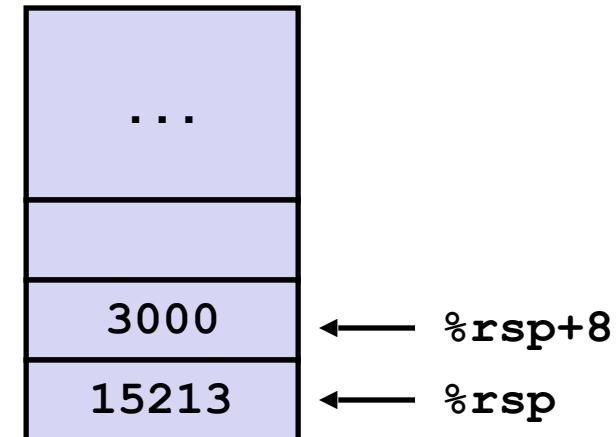
Register	Value(s)
%rdi	&v1
%rsi	&v2
%rax	21213

Stack Example: call_add

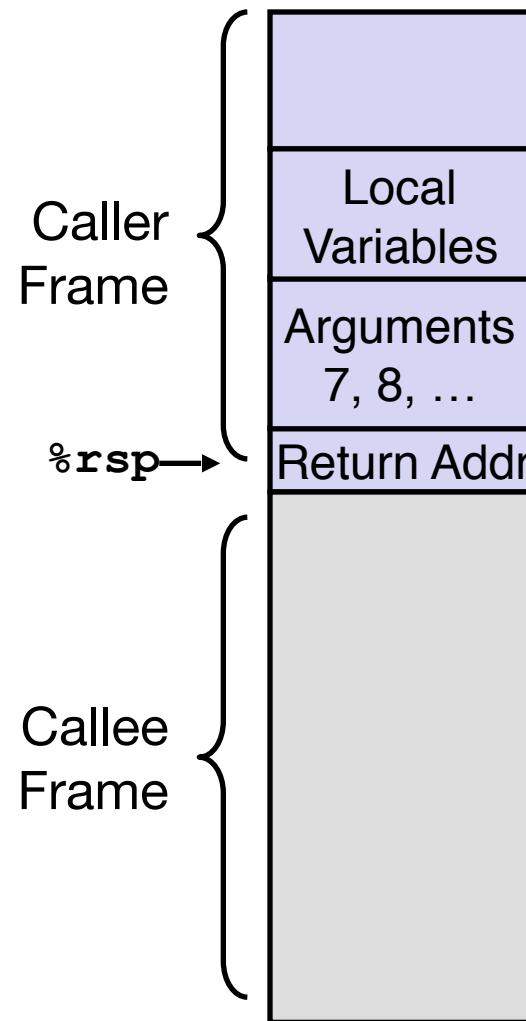
```
long call_add() {  
    long v1 = 15213;  
    long v2 = 3000;  
    long v3 = add(&v1, &v2);  
    return v2+v3;  
}
```

```
call_add:  
    subq    $16, %rsp  
    movq    $15213, (%rsp)  
    movq    $3000, 8(%rsp)  
    leaq    (%rsp), %rdi  
    leaq    8(%rsp), %rsi  
    call    add  
    addq    8(%rsp), %rax  
    addq    $16, %rsp  
    ret
```

Stack



Stack Frame (So Far...)



Register Saving Conventions

- Any issue with using registers for temporary storage?
 - Contents of register `%rdx` overwritten by `who()`
 - This could be trouble → Need some coordination

Caller

```
yoo:  
...  
    movq $15213, %rdx  
    call who  
    addq %rdx, %rax  
...  
    ret
```

Callee

```
who:  
...  
    subq $18213, %rdx  
...  
    ret
```

Register Saving Conventions

- Common conventions
 - “*Caller Saved*”
 - Caller saves temporary values in its frame (on the stack) before the call
 - Callee is then free to modify their values
 - “*Callee Saved*”
 - Callee saves temporary values in its frame before using
 - Callee restores them before returning to caller
 - Caller can safely assume that register values won’t change after the function call

Register Saving Conventions

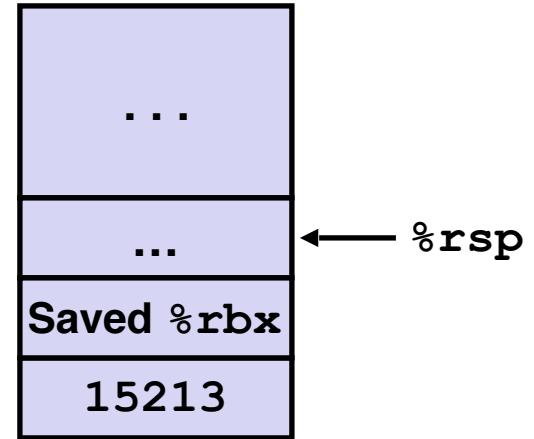
- Conventions used in x86-64 (*Part of the Calling Conventions*)
 - Some registers are saved by caller, some are by callee.
 - Caller saved: `%rdi`, `%rsi`, `%rdx`, `%rcx`, `%r8`, `%r9`, `%r10`, `%r11`
 - Callee saved: `%rbx`, `%rbp`, `%r12`, `%r13`, `%r14`, `%r15`
 - `%rax` holds return value, so implicitly caller saved
 - `%rsp` is the stack pointer, so implicitly callee saved

Example

```
long call_incr2(long x) {  
    long v1 = 15213;  
    long v2 = incr(&v1, 3000);  
    return x+v2;  
}
```

```
call_incr2:  
    pushq  %rbx    ←  
    pushq  $15213  
    movq  %rdi, %rbx  
    movl  $3000, %esi  
    leaq  (%rsp), %rdi  
    call  incr  
    addq  %rbx, %rax  
    addq  $8, %rsp    ←  
    popq  %rbx  
    ret
```

Stack



- `call_incr2` needs to save `%rbx` (callee-saved) because it will modify its value
- It can safely use `%rbx` after `call incr` because `incr` will have to save `%rbx` if it needs to use it (again, `%rbx` is callee saved)

Stack Frame: Putting It Together

