


































PopNames3111

Element	Missed Instructions	Cov.	Missed Branches	Cov.	Missed	Cxty	Missed	Lines	Missed	Methods	Missed	Cla
 comp3111.popnames		72%		51%	60	100	94	339	7	29	2	
 comp3111.popnames.controllers		84%		59%	59	145	112	557	8	78	0	
 comp3111.popnames.metrics		87%		73%	43	165	47	308	3	65	0	
 comp3111.popnames.record		87%		78%	20	87	31	216	6	42	0	
 comp3111.popnames.applications		86%		63%	13	44	13	119	3	26	0	
 comp3111.popnames.Task5		93%		84%	10	37	8	104	2	12	0	
 comp3111.popnames.Task1		98%		100%	2	43	5	201	2	31	0	
 comp3111.popnames.utils		94%		91%	4	29	6	85	2	17	0	
 comp3111.popnames.Task4		98%		88%	7	38	5	130	4	25	0	
 comp3111.popnames.Task2		98%		93%	3	40	3	136	1	24	0	
 comp3111.popnames.util		89%		81%	4	14	4	18	1	6	0	
Total	1,294 of 10,645	87%	225 of 772	70%	225	742	328	2,213	39	355	2	