# Formal Method on Hardware Security

#### September 2022

## 1 Compare the Property in Rosette and in Coq

### 1.1 Property in Rosette.

**Property to verify.** Last week, we have a state machine and want to verify a property hold for each state of the machine. Formally,

$$\forall \text{ input } in_0, in_1, \dots, in_n,$$

$$\text{Let } S_0 \xrightarrow{in_0} S_1 \xrightarrow{in_1} \cdots \xrightarrow{in_n} S_{n+1}$$

$$\text{If No smash in the } in_0, in_1, \dots, in_n$$

$$\text{We have } PropertyHold(S_0), PropertyHold(S_1), \dots, PropertyHold(S_n)$$

**Property for induction step.** The property I verify for the induction step is:

$$\forall \ \ \text{state} \ S_i, \ \text{input} \ in_i, \ in_{i+1},$$
 Let  $S_i \xrightarrow{in_i} S_{i+1} \xrightarrow{in_{i+1}} S_{i+2}$  If No smash in the  $in_i, in_{i+1}$  We have  $PropertyHold(S_{i+1}) \Rightarrow PropertyHold(S_{i+2})$ 

It is a little weird since why not just use:

$$\forall \text{ state } S_i, \text{ input } in_i,$$

$$\text{Let } S_i \xrightarrow{in_i} S_{i+1}$$

$$\text{If No smash in the } in_i$$

$$\text{We have } PropertyHold(S_i) \Rightarrow PropertyHold(S_{i+1})$$

### 1.2 Property in Coq.

Property to verify. Let's translate the property in following way:

$$\forall \text{ input } in_0, in_1, \dots, in_n,$$
 We have  $PropertyHold'(in_0, in_1, \dots, in_n)$ 

Property for induction step.

$$\forall$$
 input  $in_0, in_1, \dots, in_i, in_{i+1}$ ,  
We have  $PropertyHold'(in_0, in_1, \dots, in_i) \Rightarrow PropertyHold'(in_0, in_1, \dots, in_i, in_{i+1})$ 

Relate the coq property with Rosette property. We can try to expand this property a little bit.

$$\forall \text{ input } in_0, in_1, \dots, in_i, in_{i+1},$$

$$\text{Let } S_0 \xrightarrow{in_0} S_1 \xrightarrow{in_1} \dots \xrightarrow{in_n} S_n \xrightarrow{in_{n+1}} S_{n+2} \xrightarrow{in_{n+1}} S_{n+2}$$
We have  $PropertyHold(S_{n+1}) \Rightarrow PropertyHold(S_{n+2})$ 

To further become identical to the Rosette property we are verifying, we actually conservatively think the  $S_n$  here can be an arbitrary state.