Curriculum Vitae

Yuhui Xu
Shanghai Jiao Tong University
Phone: (+86) 18217193359
Email: yuhuixu@sjtu.edu.cn
Homepage: https://yuhuixu1993.github.io

Education

- **Johns Hopkins University** — Baltimore, USA
  Department of Computer Science, Visiting Scholar — *2019.12–Now*
  - Advisor: Prof. Alan Yuille
  - Research areas: Deep Neural Network Compression, Neural Architecture Search
- **Shanghai Jiao Tong University** — Shanghai, China
  Department of Electronic Information and Electrical Engineering, Ph.D. — *2016.09–Now*
  - Advisor: Prof. Hongkai Xiong
  - Research areas: Deep Neural Network Compression, Computer Vision
- **Southeast University** — Nanjing, China
  Chien-Shiung Wu College, B.S. in Information and Communication Engineering — *2012.09–2016.06*

Research:

My research is mainly focus on the compression and acceleration of deep neural networks and neural architecture search. One DNN quantization methods is accepted by AAAI18 and Low-rank decomposition method is accepted by IJCAI20. One NAS method PC-DARTS is accepted as a spotlight presentation in ICLR2020.

Experience

- **Huawei Noah's Ark Lab** — *September, 2018 - November, 2019*
  Research on Neural Architecture Search
- **Qualcomm Greater China University Research (UR) Cooperation Project** — *April, 2017 - April, 2019*
  Research on Deep Neural Network Compression and Acceleration
- **Research Engineer Intern at Tencent, Inc** — *July - Sept, 2018*
  Research on Compression and Acceleration on Large-scale Online Advertising

List of Articles:

1. Y. Xu, L. Xie, X. Zhang, X. Chen, G.-J. Qi, Q. Tian, H. Xiong
   PC-DARTS: Partial Channel Connections for Memory-Efficient Differentiable Architecture Search.
   ICLR 2020.
2. Y. Xu, Y. Li, S. Zhang, W. Wen, B. Wang, Y. Qi, Y. Chen, W. Lin, H. Xiong
   Trained Rank Pruning for Efficient Deep Neural Networks.
   IJCAI 2020.
3. Y. Xu, Y. Li, S. Zhang, W. Wen, B. Wang, Y. Qi, Y. Chen, W. Lin, H. Xiong
   Trained Rank Pruning for Efficient Deep Neural Networks.
   Neurips2019 EMC2 workshop.
4. Y. Xu, Y. Wang, A. Zhou, W. Lin, H. Xiong
   Deep Neural Network Compression with Single and Multiple Level Quantization.
   AAAI 18.

5. Y. Xu, S. Zhang, Y. Qi, J. Guo, W. Lin, H. Xiong
   DNQ: Dynamic Network Quantization.
   IEEE DCC 2019.
6. Y. Xu, W. Dai, Y. Qi, J. Zou, H. Xiong,
   Iterative Deep Neural Network Quantization with Lipschitz Constraint.
   IEEE Transactions on Multimedia.
7. Y. Xu, L. Xie, X. Zhang, X. Chen, Q. Tian, H. Xiong
   Latency-Aware Differentiable Neural Architecture Search.
   In Submission.

Awards

National Encouragement Scholarship 2013,2015
Cyrus Tang Scholarship Cyrus Tang Foundation 2012–2016
Yun Ying Scholarship 2015
Meritorious Winner in The Interdisciplinary Contest in Modeling (ICM) 2015
First Prize in Jiangsu Electronic Design Contest 2014

Paper Reviews

ACM MM 2018
ICME 2018,2019
IJCAI 2019
Pattern Recognition
IEEE T-ITS
IEEE T-PAMI
IEEE T-CSVT