

9 附录 B 指令码一览

		31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
CLO.W	rd, rj	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CLZ.W	rd, rj	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CTO.W	rd, rj	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CTZ.W	rd, rj	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CLO.D	rd, rj	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CLZ.D	rd, rj	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CTO.D	rd, rj	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CTZ.D	rd, rj	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
REVB.2H	rd, rj	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
REVB.4H	rd, rj	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
REVB.2W	rd, rj	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
REVB.D	rd, rj	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
REVB.2W	rd, rj	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
REVB.D	rd, rj	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
REVB.2W	rd, rj	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
REVB.D	rd, rj	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
BITREV.4B	rd, rj	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
BITREV.8B	rd, rj	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
BITREV.W	rd, rj	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
BITREV.D	rd, rj	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
EXT.W.H	rd, rj	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
EXT.W.B	rd, rj	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RDTIMEL.W	rd, rj	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RDTIMEH.W	rd, rj	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
RDTIME.D	rd, rj	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
CPUCFG	rd, rj	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ASRTLE.D	rj, rk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ASRTGT.D	rj, rk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ALSL.W	rd, rj, rk, sa2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ALSL.WU	rd, rj, rk, sa2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
BYTEPICK.W	rd, rj, rk, sa2	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
BYTEPICK.D	rd, rj, rk, sa3	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
ADD.W	rd, rj, rk	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0

		3	3	2	2	2	2	2	2	2	2	2	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0							
		1	0	9	8	7	6	5	4	3	2	1	0	9	8	7	6	5	4	3	2	1	0	9	8	7	6	5	4	3	2	1	0			
MASKNEZ	rd, rj, rk	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	1	1		rk		rj		rd												
NOR	rd, rj, rk	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	0	0		rk		rj		rd												
AND	rd, rj, rk	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	0	1		rk		rj		rd												
OR	rd, rj, rk	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	1	0		rk		rj		rd												
XOR	rd, rj, rk	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	1	1		rk		rj		rd												
ORN	rd, rj, rk	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	0	0		rk		rj		rd												
ANDN	rd, rj, rk	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	0	1		rk		rj		rd												
SLL.W	rd, rj, rk	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	0		rk		rj		rd												
SRL.W	rd, rj, rk	0	0	0	0	0	0	0	0	0	0	0	1	0	1	1	1	1		rk		rj		rd												
SRA.W	rd, rj, rk	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0	0	0		rk		rj		rd												
SLL.D	rd, rj, rk	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0	0	1		rk		rj		rd												
SRL.D	rd, rj, rk	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0	1	0		rk		rj		rd												
SRA.D	rd, rj, rk	0	0	0	0	0	0	0	0	0	0	0	1	1	0	0	1	1		rk		rj		rd												
ROTR.W	rd, rj, rk	0	0	0	0	0	0	0	0	0	0	0	1	1	0	1	1	0		rk		rj		rd												
ROTR.D	rd, rj, rk	0	0	0	0	0	0	0	0	0	0	0	1	1	0	1	1	1		rk		rj		rd												
MUL.W	rd, rj, rk	0	0	0	0	0	0	0	0	0	0	0	1	1	1	0	0	0		rk		rj		rd												
MULH.W	rd, rj, rk	0	0	0	0	0	0	0	0	0	0	0	1	1	1	0	0	1		rk		rj		rd												
MULH.WU	rd, rj, rk	0	0	0	0	0	0	0	0	0	0	0	1	1	1	0	1	0		rk		rj		rd												
MUL.D	rd, rj, rk	0	0	0	0	0	0	0	0	0	0	0	1	1	1	0	1	1		rk		rj		rd												
MULH.D	rd, rj, rk	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	0		rk		rj		rd												
MULH.DU	rd, rj, rk	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	0	1		rk		rj		rd												
MULW.D.W	rd, rj, rk	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	0		rk		rj		rd												
MULW.D.WU	rd, rj, rk	0	0	0	0	0	0	0	0	0	0	0	1	1	1	1	1	1		rk		rj		rd												
DIV.W	rd, rj, rk	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0		rk		rj		rd												
MOD.W	rd, rj, rk	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1		rk		rj		rd											
DIV.WU	rd, rj, rk	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	1	0		rk		rj		rd											
MOD.WU	rd, rj, rk	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	1	1		rk		rj		rd											
DIV.D	rd, rj, rk	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	0		rk		rj		rd											
MOD.D	rd, rj, rk	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	0	1		rk		rj		rd											
DIV.DU	rd, rj, rk	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	1	0		rk		rj		rd											
MOD.DU	rd, rj, rk	0	0	0	0	0	0	0	0	0	0	0	1	0	0	0	1	1	1		rk		rj		rd											
CRC.W.B.W	rd, rj, rk	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0		rk		rj		rd											
CRC.W.H.W	rd, rj, rk	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	1		rk		rj		rd											
CRC.W.W.W	rd, rj, rk	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	1	0		rk		rj		rd											
CRC.W.D.W	rd, rj, rk	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	1	1		rk		rj		rd											
CRCC.W.B.W	rd, rj, rk	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	1	0	0		rk		rj		rd											
CRCC.W.H.W	rd, rj, rk	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	1	0	1		rk		rj		rd											
CRCC.W.W.W	rd, rj, rk	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	1	1	0		rk		rj		rd											
CRCC.W.D.W	rd, rj, rk	0	0	0	0	0	0	0	0	0	0	0	1	0	0	1	1	1	1		rk		rj		rd											
BREAK	code	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	1	0	0	code																
DBCL	code	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	1	0	1	code																
SYSCALL	code	0	0	0	0	0	0	0	0	0	0	0	1	0	1	0	1	1	0	code																

		3	3	2	2	2	2	2	2	2	2	2	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0		
		1	0	9	8	7	6	5	4	3	2	1	0	9	8	7	6	5	4	3	2	1	0	9	8	7	6	5	4	3	2	1	0
ALSL.D	rd, rj, rk, sa2	0	0	0	0	0	0	0	0	0	0	1	0	1	1	0	sa2		rk						rj							rd	
SLLI.W	rd, rj, ui5	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1						ui5							rj	rd
SLLI.D	rd, rj, ui6	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	0	1		ui6					rj							rd	
SRLI.W	rd, rj, ui5	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	1	0	0	1					ui5							rj	rd
SRLI.D	rd, rj, ui6	0	0	0	0	0	0	0	0	0	0	1	0	0	0	0	1	0	1		ui6				rj							rd	
SRAI.W	rd, rj, ui5	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	0	1		ui5				rj							rd	
SRAI.D	rd, rj, ui6	0	0	0	0	0	0	0	0	0	0	1	0	0	1	0	0	1		ui6				rj								rd	
ROTRI.W	rd, rj, ui5	0	0	0	0	0	0	0	0	0	0	1	0	0	1	1	0	0	1		ui5				rj							rd	
ROTRI.D	rd, rj, ui6	0	0	0	0	0	0	0	0	0	0	1	0	0	1	1	0	1		ui6				rj								rd	
BSTRINS.W	rd, rj, msbw, lsbw	0	0	0	0	0	0	0	0	0	1	1			msbw	0			lsbw					rj								rd	
BSTRPICK.W	rd, rj, msbw, lsbw	0	0	0	0	0	0	0	0	0	1	1			msbw	1			lsbw					rj								rd	
BSTRINS.D	rd, rj, msbd, lsbd	0	0	0	0	0	0	0	0	1	0				msbd				lsbd					rj								rd	
BSTRPICK.D	rd, rj, msbd, lsbd	0	0	0	0	0	0	0	0	1	1				msbd				lsbd					rj								rd	
FADD.S	fd, fj, fk	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	1		fk				fj								fd	
FADD.D	fd, fj, fk	0	0	0	0	0	0	0	1	0	0	0	0	0	0	0	0	1	0		fk			fj								fd	
FSUB.S	fd, fj, fk	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	0	1		fk				fj								fd	
FSUB.D	fd, fj, fk	0	0	0	0	0	0	0	1	0	0	0	0	0	0	1	1	0		fk				fj								fd	
FMUL.S	fd, fj, fk	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	0	1		fk				fj								fd	
FMUL.D	fd, fj, fk	0	0	0	0	0	0	0	1	0	0	0	0	0	1	0	1	0		fk				fj								fd	
FDIV.S	fd, fj, fk	0	0	0	0	0	0	0	1	0	0	0	0	0	1	1	0	1		fk				fj								fd	
FDIV.D	fd, fj, fk	0	0	0	0	0	0	0	1	0	0	0	0	0	1	1	1	0		fk				fj								fd	
FMAX.S	fd, fj, fk	0	0	0	0	0	0	0	1	0	0	0	0	1	0	0	0	1		fk				fj								fd	
FMAX.D	fd, fj, fk	0	0	0	0	0	0	0	1	0	0	0	0	1	0	0	1	0		fk				fj								fd	
FMIN.S	fd, fj, fk	0	0	0	0	0	0	0	1	0	0	0	0	1	0	1	0	1		fk				fj								fd	
FMIN.D	fd, fj, fk	0	0	0	0	0	0	0	1	0	0	0	0	1	0	1	1	0		fk				fj								fd	
FMAXA.S	fd, fj, fk	0	0	0	0	0	0	0	1	0	0	0	0	1	1	0	0	1		fk				fj								fd	
FMAXA.D	fd, fj, fk	0	0	0	0	0	0	0	1	0	0	0	0	1	1	0	1	0		fk				fj								fd	
FMINA.S	fd, fj, fk	0	0	0	0	0	0	0	1	0	0	0	0	1	1	1	0	1		fk				fj								fd	
FMINA.D	fd, fj, fk	0	0	0	0	0	0	0	1	0	0	0	0	1	1	1	1	0		fk				fj								fd	
FSCALEB.S	fd, fj, fk	0	0	0	0	0	0	0	1	0	0	0	1	0	0	0	0	1		fk				fj								fd	
FSCALEB.D	fd, fj, fk	0	0	0	0	0	0	0	1	0	0	0	1	0	0	0	1	0		fk				fj								fd	
FCOPYSIGN.S	fd, fj, fk	0	0	0	0	0	0	0	1	0	0	0	1	0	0	1	0	1		fk				fj								fd	
FCOPYSIGN.D	fd, fj, fk	0	0	0	0	0	0	0	1	0	0	0	1	0	0	1	1	0		fk				fj								fd	
FABS.S	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0	0	0	0	1		fj							fd	
FABS.D	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0	0	0	1	0		fj							fd	
FNEG.S	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0	0	1	0	1		fj							fd	
FNEG.D	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0	0	1	1	0		fj							fd	
FLOGB.S	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0	1	0	0	1		fj							fd	
FLOGB.D	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0	1	0	1	0		fj							fd	
FCLASS.S	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0	1	1	0	1		fj							fd	
FCLASS.D	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	0	1	1	1	0		fj							fd	
FSQRT.S	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	1	0	0	0	1		fj							fd	

		3 1	3 0	2 9	2 8	2 7	2 6	2 5	2 4	2 3	2 2	2 1	1 0	1 9	1 8	1 7	1 6	1 5	1 4	1 3	1 2	1 1	1 0	0 9	0 8	0 7	0 6	0 5	0 4	0 3	0 2	0 1	0 0
FSQRT.D	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	1	0	0	1	0		fj								fd
FRECIP.S	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	1	0	1	0	1		fj								fd
FRECIP.D	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	1	0	1	1	0		fj								fd
FRSQRT.S	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	1	1	0	0	1		fj								fd
FRSQRT.D	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	1	1	0	1	0		fj								fd
FRECIPE.S	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	1	1	1	0	1		fj								fd
FRECIPE.D	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	0	1	1	1	1	0		fj								fd
FRSQRTE.S	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	1	0	0	0	0	1		fj								fd
FRSQRTE.D	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	1	0	0	0	1	0		fj								fd
FMOV.S	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	1	0	0	1	0	1		fj								fd
FMOV.D	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	1	0	0	1	1	0		fj								fd
MOVGR2FR.W	fd, rj	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	1	0	1	0	0	1		rj								fd
MOVGR2FR.D	fd, rj	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	1	0	1	0	1	0		rj								fd
MOVGR2FRH.W	fd, rj	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	1	0	1	0	1	1		rj								fd
MOVFR2GR.S	rd, fj	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	1	0	1	1	0	1		fj								rd
MOVFR2GR.D	rd, fj	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	1	0	1	1	1	0		fj								rd
MOVFRH2GR.S	rd, fj	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	1	0	1	1	1	1		fj								rd
MOVGR2FCSR	fcsr, rj	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	1	1	0	0	0	0		rj								fcsr
MOVFCSR2GR	rd, fcsr	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	1	1	0	0	1	0		fcsr								rd
MOVFR2CF	cd, fj	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	1	1	0	1	0	0		fj				0	0			cd
MOVCF2FR	fd, cj	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	1	1	0	1	0	1	0	0	0	cj						fd
MOVGR2CF	cd, rj	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	1	1	0	1	1	0		rj				0	0			cd
MOVCF2GR	rd, cj	0	0	0	0	0	0	0	1	0	0	0	1	0	1	0	0	1	1	0	1	1	1	0	0	0	cj						rd
FCVT.S.D	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	1	0	0	1	0	0	0	1	1	0		fj								fd
FCVT.D.S	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	1	0	0	1	0	0	1	0	0	1		fj								fd
FTINTRM.W.S	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	1	0	1	0	0	0	0	0	0	1		fj								fd
FTINTRM.W.D	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	1	0	1	0	0	0	0	0	1	0		fj								fd
FTINTRM.L.S	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	1	0	1	0	0	0	1	0	0	1		fj								fd
FTINTRM.L.D	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	1	0	1	0	0	0	1	0	1	0		fj								fd
FTINTRP.W.S	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	1	0	1	0	0	1	0	0	0	1		fj								fd
FTINTRP.W.D	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	1	0	1	0	0	1	0	0	1	0		fj								fd
FTINTRP.L.S	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	1	0	1	0	0	1	1	0	0	1		fj								fd
FTINTRP.L.D	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	1	0	1	0	0	1	1	0	1	0		fj								fd
FTINTRZ.W.S	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	1	0	1	0	1	0	0	0	0	1		fj								fd
FTINTRZ.W.D	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	1	0	1	0	1	0	0	0	1	0		fj								fd
FTINTRZ.L.S	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	1	0	1	0	1	0	1	0	0	1		fj								fd
FTINTRZ.L.D	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	1	0	1	0	1	0	1	0	1	0		fj								fd
FTINTRNE.W.S	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	1	0	1	0	1	1	0	0	0	1		fj								fd
FTINTRNE.W.D	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	1	0	1	0	1	1	0	0	1	0		fj								fd
FTINTRNE.L.S	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	1	0	1	0	1	1	1	0	0	1		fj								fd
FTINTRNE.L.D	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	1	0	1	0	1	1	1	0	1	0		fj								fd
FTINT.W.S	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	1	0	1	1	0	0	0	0	0	1		fj								fd

		3	3	2	2	2	2	2	2	2	2	1	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0						
		1	0	9	8	7	6	5	4	3	2	1	0	9	8	7	6	5	4	3	2	1	0	9	8	7	6	5	4	3	2	1	0		
FTINT.W.D	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	1	0	1	1	0	0	0	0	1	0		fj			fd							
FTINT.L.S	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	1	0	1	1	0	0	1	0	0	1		fj			fd							
FTINT.L.D	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	1	0	1	1	0	0	1	0	1	0		fj			fd							
FFINT.S.W	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	1	1	0	1	0	0	0	1	0	0		fj			fd							
FFINT.S.L	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	1	1	0	1	0	0	0	1	1	0		fj			fd							
FFINT.D.W	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	1	1	0	1	0	0	1	0	0	0		fj			fd							
FFINT.D.L	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	1	1	0	1	0	0	1	0	1	0		fj			fd							
FRINT.S	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	1	1	1	0	0	1	0	0	0	1		fj			fd							
FRINT.D	fd, fj	0	0	0	0	0	0	0	1	0	0	0	1	1	1	1	0	0	1	0	0	1	0		fj			fd							
SLTI	rd, rj, si12	0	0	0	0	0	0	1	0	0	0	si12										rj	rd												
SLTUI	rd, rj, si12	0	0	0	0	0	0	1	0	0	1	si12										rj	rd												
ADDI.W	rd, rj, si12	0	0	0	0	0	0	1	0	1	0	si12										rj	rd												
ADDI.D	rd, rj, si12	0	0	0	0	0	0	1	0	1	1	si12										rj	rd												
LU52I.D	rd, rj, si12	0	0	0	0	0	0	1	1	0	0	si12										rj	rd												
ANDI	rd, rj, ui12	0	0	0	0	0	0	1	1	0	1	ui12										rj	rd												
ORI	rd, rj, ui12	0	0	0	0	0	0	1	1	1	0	ui12										rj	rd												
XORI	rd, rj, ui12	0	0	0	0	0	0	1	1	1	1	ui12										rj	rd												
CSR RD	rd, csr	0	0	0	0	0	1	0	0	csr												0	0	0	0	0	rd								
CSR WR	rd, csr	0	0	0	0	0	1	0	0	csr												0	0	0	0	1	rd								
CSR XCHG	rd, rj, csr	0	0	0	0	0	1	0	0	csr												rj!=0,1	rd												
CACOP	code, rj, si12	0	0	0	0	0	1	1	0	0	0	si12										rj	code												
LDDIR	rd, rj, level	0	0	0	0	0	1	1	0	0	1	0	0	0	0	level										rj	rd								
LDPTE	rj, seq	0	0	0	0	0	1	1	0	0	1	0	0	0	0	1	seq										rj	0	0	0	0	0			
IOCSR RD.B	rd, rj	0	0	0	0	0	1	1	0	0	1	0	0	1	0	0	0	0	0	0	0	0	0	0		rj			rd						
IOCSR RD.H	rd, rj	0	0	0	0	0	1	1	0	0	1	0	0	1	0	0	0	0	0	0	0	0	0	1		rj			rd						
IOCSR RD.W	rd, rj	0	0	0	0	0	1	1	0	0	1	0	0	1	0	0	0	0	0	0	0	0	1	0		rj			rd						
IOCSR RD.D	rd, rj	0	0	0	0	0	1	1	0	0	1	0	0	1	0	0	0	0	0	0	0	0	1	1		rj			rd						
IOCSR WR.B	rd, rj	0	0	0	0	0	1	1	0	0	1	0	0	1	0	0	0	0	0	0	0	1	0	0		rj			rd						
IOCSR WR.H	rd, rj	0	0	0	0	0	1	1	0	0	1	0	0	1	0	0	0	0	0	0	0	1	0	1		rj			rd						
IOCSR WR.W	rd, rj	0	0	0	0	0	1	1	0	0	1	0	0	1	0	0	0	0	0	0	0	1	1	0		rj			rd						
IOCSR WR.D	rd, rj	0	0	0	0	0	1	1	0	0	1	0	0	1	0	0	0	0	0	0	0	1	1	1		rj			rd						
TLBCLR		0	0	0	0	0	1	1	0	0	1	0	0	1	0	0	0	0	0	0	1	0	0	0		0	0	0	0	0	0	0	0	0	
TLBFLUSH		0	0	0	0	0	1	1	0	0	1	0	0	1	0	0	0	0	0	0	1	0	0	1		0	0	0	0	0	0	0	0	0	
TLBSRCH		0	0	0	0	0	1	1	0	0	1	0	0	1	0	0	0	0	0	0	1	0	1	0		0	0	0	0	0	0	0	0	0	
TLBRD		0	0	0	0	0	1	1	0	0	1	0	0	1	0	0	0	0	0	0	1	0	1	1		0	0	0	0	0	0	0	0	0	
TLBWR		0	0	0	0	0	1	1	0	0	1	0	0	1	0	0	0	0	0	0	0	1	1	0	0		0	0	0	0	0	0	0	0	0
TLBFILL		0	0	0	0	0	1	1	0	0	1	0	0	1	0	0	0	0	0	0	1	1	0	1		0	0	0	0	0	0	0	0	0	
ERTN		0	0	0	0	0	1	1	0	0	1	0	0	1	0	0	0	0	0	0	0	1	1	1	0		0	0	0	0	0	0	0	0	
IDLE	level	0	0	0	0	0	1	1	0	0	1	0	0	1	0	0	0	1	level																
INVTLB	op, rj, rk	0	0	0	0	0	1	1	0	0	1	0	0	1	0	0	1	1	rk	rj	op														
FMADD.S	fd, fj, fk, fa	0	0	0	0	1	0	0	0	0	0	0	1	fa				fk	fj	fd															
FMADD.D	fd, fj, fk, fa	0	0	0	0	1	0	0	0	0	0	1	0	fa				fk	fj	fd															

		3	3	2	2	2	2	2	2	2	2	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0					
		1	0	9	8	7	6	5	4	3	2	1	0	9	8	7	6	5	4	3	2	1	0	9	8	7	6	5	4	3	2	1	0
FMSUB.S	fd, fj, fk, fa	0	0	0	0	1	0	0	0	0	1	0	1	fa			fk			fj			fd										
FMSUB.D	fd, fj, fk, fa	0	0	0	0	1	0	0	0	0	1	1	0	fa			fk			fj			fd										
FNMADD.S	fd, fj, fk, fa	0	0	0	0	1	0	0	0	1	0	0	1	fa			fk			fj			fd										
FNMADD.D	fd, fj, fk, fa	0	0	0	0	1	0	0	0	1	0	1	0	fa			fk			fj			fd										
FNMSUB.S	fd, fj, fk, fa	0	0	0	0	1	0	0	0	1	1	0	1	fa			fk			fj			fd										
FNMSUB.D	fd, fj, fk, fa	0	0	0	0	1	0	0	0	1	1	1	0	fa			fk			fj			fd										
FCMP.cond.S	cd, fj, fk	0	0	0	0	1	1	0	0	0	0	0	1	cond			fk			fj			0	0	cd								
FCMP.cond.D	cd, fj, fk	0	0	0	0	1	1	0	0	0	0	1	0	cond			fk			fj			0	0	cd								
FSEL	fd, fj, fk, ca	0	0	0	0	1	1	0	1	0	0	0	0	0	0	0	ca	fk			fj			fd									
ADDU16I.D	rd, rj, si16	0	0	0	1	0	0	si16										rj			rd												
LU12I.W	rd, si20	0	0	0	1	0	1	0	si20										rd														
LU32I.D	rd, si20	0	0	0	1	0	1	1	si20										rd														
PCADDI	rd, si20	0	0	0	1	1	0	0	si20										rd														
PCALAU12I	rd, si20	0	0	0	1	1	0	1	si20										rd														
PCADDU12I	rd, si20	0	0	0	1	1	1	0	si20										rd														
PCADDU18I	rd, si20	0	0	0	1	1	1	1	si20										rd														
LL.W	rd, rj, si14	0	0	1	0	0	0	0	0	si14										rj			rd										
SC.W	rd, rj, si14	0	0	1	0	0	0	0	0	1	si14										rj			rd									
LL.D	rd, rj, si14	0	0	1	0	0	0	1	0	si14										rj			rd										
SC.D	rd, rj, si14	0	0	1	0	0	0	1	1	si14										rj			rd										
LDPTR.W	rd, rj, si14	0	0	1	0	0	1	0	0	si14										rj			rd										
STPTR.W	rd, rj, si14	0	0	1	0	0	1	0	1	si14										rj			rd										
LDPTR.D	rd, rj, si14	0	0	1	0	0	1	1	0	si14										rj			rd										
STPTR.D	rd, rj, si14	0	0	1	0	0	1	1	1	si14										rj			rd										
LD.B	rd, rj, si12	0	0	1	0	1	0	0	0	0	0	si12										rj			rd								
LD.H	rd, rj, si12	0	0	1	0	1	0	0	0	0	0	1	si12										rj			rd							
LD.W	rd, rj, si12	0	0	1	0	1	0	0	0	1	0	si12										rj			rd								
LD.D	rd, rj, si12	0	0	1	0	1	0	0	0	1	1	si12										rj			rd								
ST.B	rd, rj, si12	0	0	1	0	1	0	0	1	0	0	si12										rj			rd								
ST.H	rd, rj, si12	0	0	1	0	1	0	0	1	0	1	si12										rj			rd								
ST.W	rd, rj, si12	0	0	1	0	1	0	0	1	1	0	si12										rj			rd								
ST.D	rd, rj, si12	0	0	1	0	1	0	0	1	1	1	si12										rj			rd								
LD.BU	rd, rj, si12	0	0	1	0	1	0	1	0	0	0	si12										rj			rd								
LD.HU	rd, rj, si12	0	0	1	0	1	0	1	0	0	1	si12										rj			rd								
LD.WU	rd, rj, si12	0	0	1	0	1	0	1	0	1	0	si12										rj			rd								
PRELD	hint, rj, si12	0	0	1	0	1	0	1	0	1	1	si12										rj			hint								
FLD.S	fd, rj, si12	0	0	1	0	1	0	1	1	0	0	si12										rj			fd								
FST.S	fd, rj, si12	0	0	1	0	1	0	1	1	0	1	si12										rj			fd								
FLD.D	fd, rj, si12	0	0	1	0	1	0	1	1	1	0	si12										rj			fd								
FST.D	fd, rj, si12	0	0	1	0	1	0	1	1	1	1	si12										rj			fd								
LDX.B	rd, rj, rk	0	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	rk			rj			rd						
LDX.H	rd, rj, rk	0	0	1	1	1	0	0	0	0	0	0	0	0	0	1	0	0	0	0	rk			rj			rd						

		3	3	2	2	2	2	2	2	2	2	2	1	1	1	1	1	1	1	0	0	0	0	0	0	0	0	0	0				
		1	0	9	8	7	6	5	4	3	2	1	0	9	8	7	6	5	4	3	2	1	0	9	8	7	6	5	4	3	2	1	0
LDX.W	rd, rj, rk	0	0	1	1	1	0	0	0	0	0	0	1	0	0	0	0	rk	rj	rd													
LDX.D	rd, rj, rk	0	0	1	1	1	0	0	0	0	0	0	1	1	0	0	0	rk	rj	rd													
STX.B	rd, rj, rk	0	0	1	1	1	0	0	0	0	0	0	1	0	0	0	0	rk	rj	rd													
STX.H	rd, rj, rk	0	0	1	1	1	0	0	0	0	0	0	1	0	1	0	0	rk	rj	rd													
STX.W	rd, rj, rk	0	0	1	1	1	0	0	0	0	0	0	1	1	0	0	0	rk	rj	rd													
STX.D	rd, rj, rk	0	0	1	1	1	0	0	0	0	0	0	1	1	1	0	0	rk	rj	rd													
LDX.BU	rd, rj, rk	0	0	1	1	1	0	0	0	0	0	1	0	0	0	0	0	rk	rj	rd													
LDX.HU	rd, rj, rk	0	0	1	1	1	0	0	0	0	0	1	0	0	1	0	0	rk	rj	rd													
LDX.WU	rd, rj, rk	0	0	1	1	1	0	0	0	0	0	1	0	1	0	0	0	rk	rj	rd													
PRELDX	hint, rj, rk	0	0	1	1	1	0	0	0	0	0	1	0	1	1	0	0	rk	rj	hint													
FLDX.S	fd, rj, rk	0	0	1	1	1	0	0	0	0	0	1	1	0	0	0	0	rk	rj	fd													
FLDX.D	fd, rj, rk	0	0	1	1	1	0	0	0	0	0	1	1	0	1	0	0	rk	rj	fd													
FSTX.S	fd, rj, rk	0	0	1	1	1	0	0	0	0	0	1	1	1	0	0	0	rk	rj	fd													
FSTX.D	fd, rj, rk	0	0	1	1	1	0	0	0	0	0	1	1	1	1	0	0	rk	rj	fd													
SC.Q	rd,rk,rj	0	0	1	1	1	0	0	0	0	1	0	1	0	1	1	1	0	rk	rj	rd												
LLACQ.W	rd, rj	0	0	1	1	1	0	0	0	0	1	0	1	0	1	1	1	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0	0
SCREL.W	rd, rj	0	0	1	1	1	0	0	0	0	1	0	1	0	1	1	1	1	0	0	0	0	1	0	0	0	0	0	0	0	0	0	0
LLACQ.D	rd, rj	0	0	1	1	1	0	0	0	0	1	0	1	0	1	1	1	1	0	0	0	1	0	0	0	0	0	0	0	0	0	0	0
SCREL.D	rd, rj	0	0	1	1	1	0	0	0	0	1	0	1	0	1	1	1	1	0	0	0	1	1	0	0	0	0	0	0	0	0	0	0
AMCAS.B	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	0	1	1	0	0	0	0	rk	rj	rd												
AMCAS.H	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	0	1	1	0	0	0	1	rk	rj	rd												
AMCAS.W	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	0	1	1	0	0	1	0	rk	rj	rd												
AMCAS.D	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	0	1	1	0	0	1	1	rk	rj	rd												
AMCAS_DB.B	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	0	1	1	0	1	0	0	rk	rj	rd												
AMCAS_DB.H	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	0	1	1	0	1	0	1	rk	rj	rd												
AMCAS_DB.W	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	0	1	1	0	1	1	0	rk	rj	rd												
AMCAS_DB.D	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	0	1	1	0	1	1	1	rk	rj	rd												
AMSWAP.B	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	0	1	1	1	0	0	0	rk	rj	rd												
AMSWAP.H	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	0	1	1	1	0	0	1	rk	rj	rd												
AMADD.B	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	0	1	1	1	0	1	0	rk	rj	rd												
AMADD.H	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	0	1	1	1	0	1	1	rk	rj	rd												
AMSWAP_DB.B	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	0	1	1	1	1	0	0	rk	rj	rd												
AMSWAP_DB.H	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	0	1	1	1	1	0	1	rk	rj	rd												
AMADD_DB.B	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	0	1	1	1	1	1	0	rk	rj	rd												
AMADD_DB.H	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	0	1	1	1	1	1	1	rk	rj	rd												
AMSWAP.W	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	1	0	0	0	0	0	0	rk	rj	rd												
AMSWAP.D	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	1	0	0	0	0	0	1	rk	rj	rd												
AMADD.W	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	1	0	0	0	0	1	0	rk	rj	rd												
AMADD.D	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	1	0	0	0	0	1	1	rk	rj	rd												
AMAND.W	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	1	0	0	0	1	0	0	rk	rj	rd												
AMAND.D	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	1	0	0	0	1	0	1	rk	rj	rd												
AMOR.W	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	1	0	0	0	1	1	0	rk	rj	rd												

		31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0
AMOR.D	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	1	0	0	0	1	1	1		rk		rj		rd									
AMXOR.W	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	1	0	0	1	0	0	0		rk		rj		rd									
AMXOR.D	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	1	0	0	1	0	0	1		rk		rj		rd									
AMMAX.W	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	1	0	0	1	0	1	0		rk		rj		rd									
AMMAX.D	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	1	0	0	1	0	1	1		rk		rj		rd									
AMMIN.W	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	1	0	0	1	1	0	0		rk		rj		rd									
AMMIN.D	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	1	0	0	1	1	0	1		rk		rj		rd									
AMMAX.WU	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	1	0	0	1	1	1	0		rk		rj		rd									
AMMAX.DU	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	1	0	0	1	1	1	1		rk		rj		rd									
AMMIN.WU	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	1	0	1	0	0	0	0		rk		rj		rd									
AMMIN.DU	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	1	0	1	0	0	0	1		rk		rj		rd									
AMSWAP_DB.W	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	1	0	1	0	0	1	0		rk		rj		rd									
AMSWAP_DB.D	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	1	0	1	0	0	1	1		rk		rj		rd									
AMADD_DB.W	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	1	0	1	0	1	0	0		rk		rj		rd									
AMADD_DB.D	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	1	0	1	0	1	0	1		rk		rj		rd									
AMAND_DB.W	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	1	0	1	0	1	1	0		rk		rj		rd									
AMAND_DB.D	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	1	0	1	0	1	1	1		rk		rj		rd									
AMOR_DB.W	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	1	0	1	1	0	0	0		rk		rj		rd									
AMOR_DB.D	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	1	0	1	1	0	0	1		rk		rj		rd									
AMXOR_DB.W	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	1	0	1	1	0	1	0		rk		rj		rd									
AMXOR_DB.D	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	1	0	1	1	0	1	1		rk		rj		rd									
AMMAX_DB.W	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	1	0	1	1	1	0	0		rk		rj		rd									
AMMAX_DB.D	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	1	0	1	1	1	0	1		rk		rj		rd									
AMMIN_DB.W	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	1	0	1	1	1	1	0		rk		rj		rd									
AMMIN_DB.D	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	1	0	1	1	1	1	1		rk		rj		rd									
AMMAX_DB.WU	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	1	1	0	0	0	0	0		rk		rj		rd									
AMMAX_DB.DU	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	1	1	0	0	0	0	1		rk		rj		rd									
AMMIN_DB.WU	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	1	1	0	0	0	1	0		rk		rj		rd									
AMMIN_DB.DU	rd, rk, rj	0	0	1	1	1	0	0	0	0	1	1	1	0	0	0	1	1		rk		rj		rd									
DBAR	hint	0	0	1	1	1	0	0	0	0	1	1	1	0	0	1	0	0	hint														
IBAR	hint	0	0	1	1	1	0	0	0	0	1	1	1	0	0	1	0	1	hint														
FLDGT.S	fd, rj, rk	0	0	1	1	1	0	0	0	0	1	1	1	0	1	0	0	0		rk		rj		fd									
FLDGT.D	fd, rj, rk	0	0	1	1	1	0	0	0	0	1	1	1	0	1	0	0	1		rk		rj		fd									
FLDLE.S	fd, rj, rk	0	0	1	1	1	0	0	0	0	1	1	1	0	1	0	1	0		rk		rj		fd									
FLDLE.D	fd, rj, rk	0	0	1	1	1	0	0	0	0	1	1	1	0	1	0	1	1		rk		rj		fd									
FSTGT.S	fd, rj, rk	0	0	1	1	1	0	0	0	0	1	1	1	0	1	1	0	0		rk		rj		fd									
FSTGT.D	fd, rj, rk	0	0	1	1	1	0	0	0	0	1	1	1	0	1	1	0	1		rk		rj		fd									
FSTLE.S	fd, rj, rk	0	0	1	1	1	0	0	0	0	1	1	1	0	1	1	1	0		rk		rj		fd									
FSTLE.D	fd, rj, rk	0	0	1	1	1	0	0	0	0	1	1	1	0	1	1	1	1		rk		rj		fd									
LDGT.B	rd, rj, rk	0	0	1	1	1	0	0	0	0	1	1	1	1	0	0	0	0		rk		rj		rd									
LDGT.H	rd, rj, rk	0	0	1	1	1	0	0	0	0	1	1	1	1	0	0	0	1		rk		rj		rd									
LDGT.W	rd, rj, rk	0	0	1	1	1	0	0	0	0	1	1	1	1	0	0	1	0		rk		rj		rd									

		31	30	29	28	27	26	25	24	23	22	21	20	19	18	17	16	15	14	13	12	11	10	9	8	7	6	5	4	3	2	1	0	
LDGT.D	rd, rj, rk	0	0	1	1	1	0	0	0	0	1	1	1	1	0	0	1	1		rk														
LDLE.B	rd, rj, rk	0	0	1	1	1	0	0	0	0	1	1	1	1	0	1	0	0		rk														
LDLE.H	rd, rj, rk	0	0	1	1	1	0	0	0	0	1	1	1	1	0	1	0	1		rk														
LDLE.W	rd, rj, rk	0	0	1	1	1	0	0	0	0	1	1	1	1	0	1	1	0		rk														
LDLE.D	rd, rj, rk	0	0	1	1	1	0	0	0	0	1	1	1	1	0	1	1	1		rk														
STGT.B	rd, rj, rk	0	0	1	1	1	0	0	0	0	1	1	1	1	1	0	0	0		rk														
STGT.H	rd, rj, rk	0	0	1	1	1	0	0	0	0	1	1	1	1	1	0	0	1		rk														
STGT.W	rd, rj, rk	0	0	1	1	1	0	0	0	0	1	1	1	1	1	0	1	0		rk														
STGT.D	rd, rj, rk	0	0	1	1	1	0	0	0	0	1	1	1	1	1	0	1	1		rk														
STLE.B	rd, rj, rk	0	0	1	1	1	0	0	0	0	1	1	1	1	1	1	0	0		rk														
STLE.H	rd, rj, rk	0	0	1	1	1	0	0	0	0	1	1	1	1	1	1	0	1		rk														
STLE.W	rd, rj, rk	0	0	1	1	1	0	0	0	0	1	1	1	1	1	1	1	0		rk														
STLE.D	rd, rj, rk	0	0	1	1	1	0	0	0	0	1	1	1	1	1	1	1	1		rk														
BEQZ	rj, offs	0	1	0	0	0	0	offs[15:0]												rj		offs[20:16]												
BNEZ	rj, offs	0	1	0	0	0	1	offs[15:0]												rj		offs[20:16]												
BCEQZ	cj, offs	0	1	0	0	1	0	offs[15:0]												0	0	cj		offs[20:16]										
BCNEZ	cj, offs	0	1	0	0	1	0	offs[15:0]												0	1	cj		offs[20:16]										
JIRL	rd, rj, offs	0	1	0	0	1	1	offs[15:0]												rj		rd												
B	offs	0	1	0	1	0	0	offs[15:0]												offs[25:16]														
BL	offs	0	1	0	1	0	1	offs[15:0]												offs[25:16]														
BEQ	rj, rd, offs	0	1	0	1	1	0	offs[15:0]												rj		rd												
BNE	rj, rd, offs	0	1	0	1	1	1	offs[15:0]												rj		rd												
BLT	rj, rd, offs	0	1	1	0	0	0	offs[15:0]												rj		rd												
BGE	rj, rd, offs	0	1	1	0	0	1	offs[15:0]												rj		rd												
BLTU	rj, rd, offs	0	1	1	0	1	0	offs[15:0]												rj		rd												
BGEU	rj, rd, offs	0	1	1	0	1	1	offs[15:0]												rj		rd												