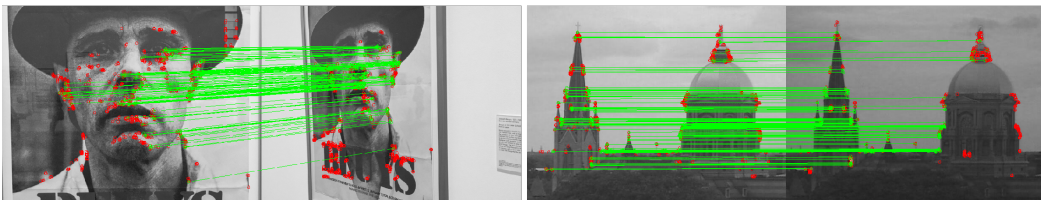
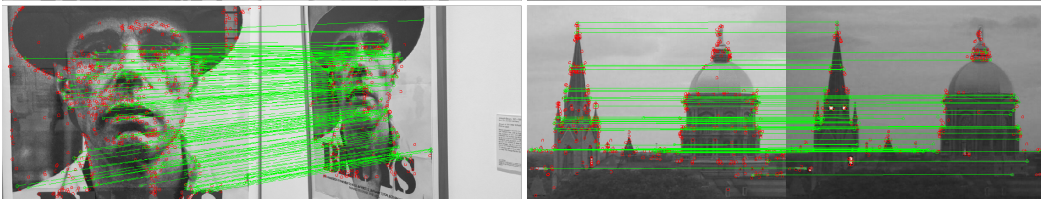


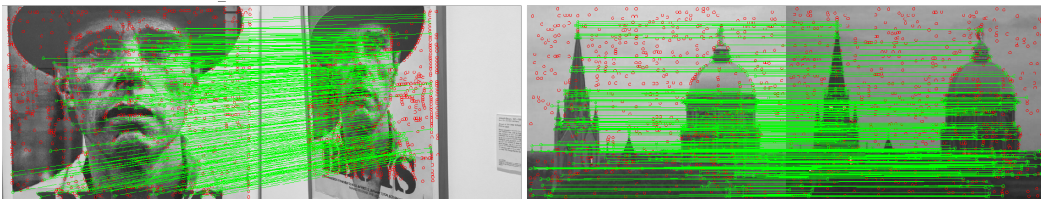
ORB



SIFT



SuperPoint
Origin



SuperPoint
on FPGA

