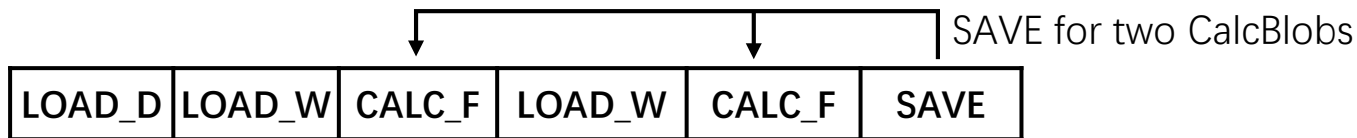


(a) Input instruction sequence. (VI-ISA from DDR)



(b) Executed sequence when no interrupt. (Original ISA to accelerator)
Virtual Instructions are deleted.



(c) Executed sequence when interrupt occurs. (Original ISA to accelerator)
Virtual Instructions (Blue) are executed. Normal SAVE (Red) is modified.