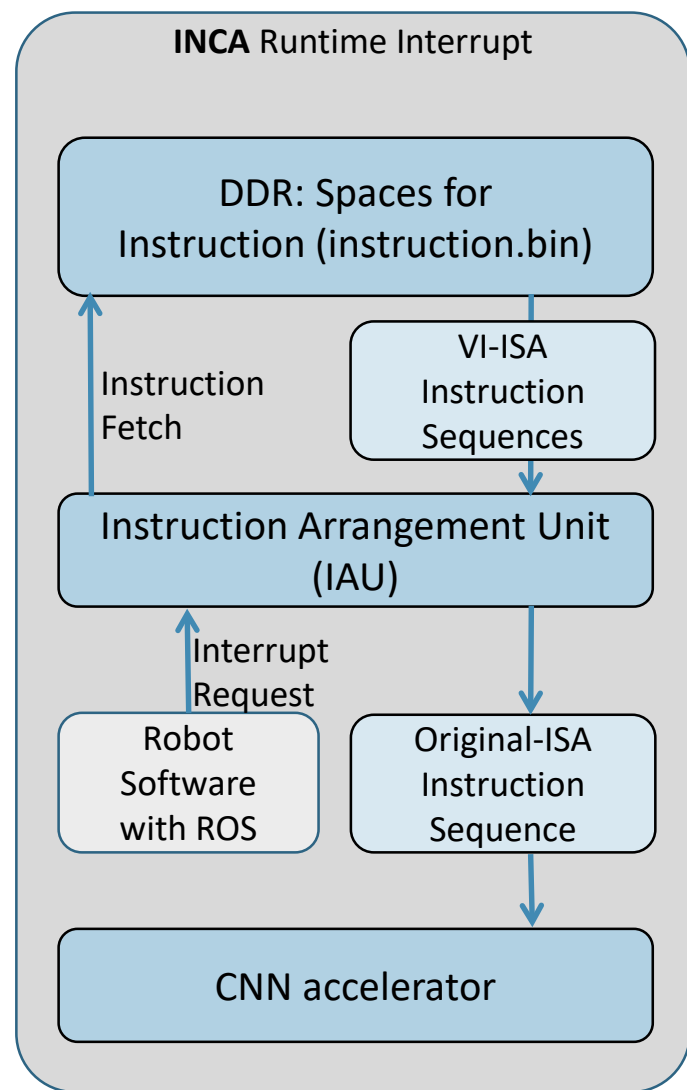(a) At **compilation** step, INCA locates the interrupt location, adds virtual instructions, and generates the virtual-instruction ISA (VI-ISA) sequence.

(b) At **runtime**, IAU translates the VI-ISA sequence to original ISA sequence executed on the accelerator.