

1a. General Computing System

1. A combination of generic hardware & general purpose operating system (GPOS) for executing a variety of applications.
2. Applications are altered by the user.
3. Performance is the key deciding factor in the selection of the system.
4. Less / not at all tailored towards reduced ~~os~~ operating power requirements.
5. Need not be deterministic in execution behaviour. Response requirements are not time critical.

Embedded System

1. A combination of special purpose hardware embedded OS for executing a specific set of applications.
2. Application is not altered by end-user, firmware is preprogrammed.
3. Application specific requirements like performance, power, memory usage.
4. Highly tailored to take advantage of the power saving modes supported by the hardware & the OS.
5. Execution behaviour is deterministic for certain type of embedded systems like 'Hard Real Time' systems.

1b. Types of Embedded System based on Generation,

First generation! 8 bit microprocessor & 4-bit microcontroller like 8085 & 8080 was used in it. Hardware circuit was simple. Assembly code is used for developing firmware. Ex! digital telephone keypads, stepper motor.

Second generation! uses 16 bit microprocessor & 8 bit microcontroller. They are more complex & powerful than 1st. Ex! Data acquisition systems like ADC, SCA DA.

Third Generation! uses 32 bit microprocessor & 16 bit microcontroller. Domain specific processor & controllers are used. Ex! Robotics.

Fourth generation! uses 64 bit ~~and~~ microprocessor & 32 bit microcontroller. The concept of system on chip, multi-core processors evolved. Highly complex & very powerful. Ex! Smart phones.

Types of Embedded System based on Complexity & performance

1. Small Scale! Embedded Systems which are simple in application needs & the performance parameters are not too critical. They are built around low cost & performance 8 or 16 bit microprocessors / controllers. Ex! Electronic toy.

2. Medium Scale! Embedded System which are slightly complex in hardware & firmware requirements. Medium-Scale Embedded System are usually built

around medium performance

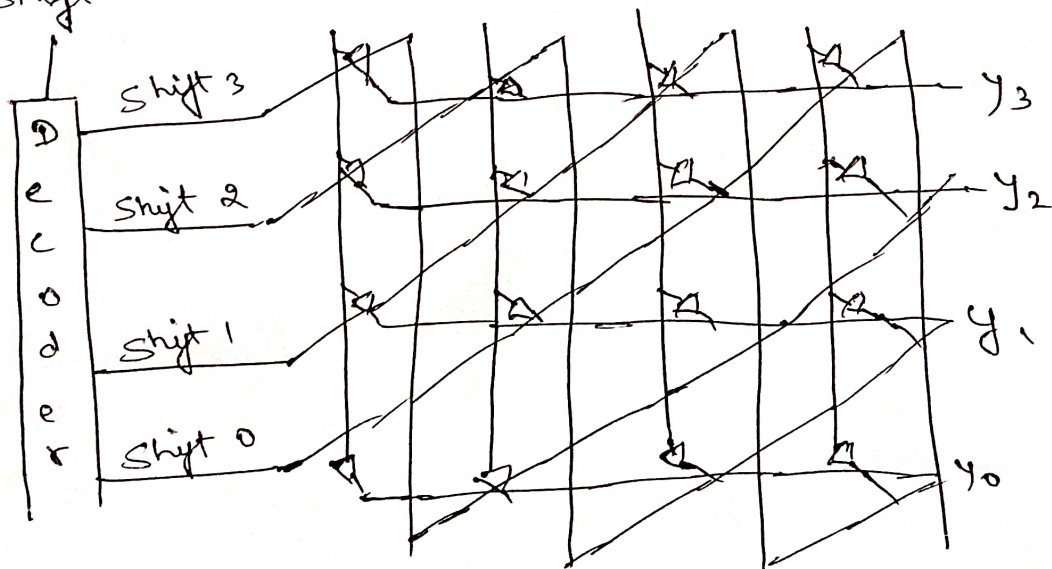
Large Scale :- or complex Embedded System, which involve highly complex hardware & firmware. They are used in mission critical applications demanding high performance.

40.

A ~~basic~~ Barrel Shifter is a digital circuit that can shift a data word by a specified number of bits without the use of any sequential logic, only combinational logic, i.e. it inherently provides a binary operation.

A barrel shifter is often used to shift and rotate bits. ~~in a~~ A barrel shifter is often implemented as a cascade of parallel 2×1 multiplexers. For an 8 bit parallel shifter, two intermediate signals are used which shift by four & two bits or passes the same data based on the value of $S[2]$ & $S[1]$.

Shift count



4 bit cross bar barrel shifter.

21b.

Swap instruction:-

SWP - Swap a word between memory and a register.

SWPB - Swap a byte between memory and a register.

This instruction is a special case of a load-store instruction. It is an atomic operation. It reads and writes a location in same bus operation. It also prevents other instructions from accessing the bus until swap is completed.

Coprocessor instructions:-

Coprocessor instructions are used to extend instruction set. This include data processing, Register transfer & Memory transfer.

CDP - Coprocessor Data Processing.

MRC / MCP - Coprocessor Register transfer.

LDC - Coprocessor Memory transfer.