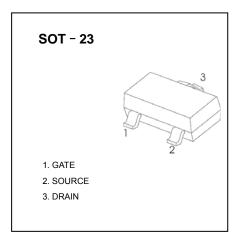
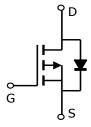


#### Features

- V<sub>DS</sub> (V) = -30V
- ID = -4.2 A
- RDS(ON) < 52m  $\Omega$  (VGS = -10V)
- lacktriangle RDS(ON) < 70m  $\Omega$  (VGS = -4.5V)





### ■ Absolute Maximum Ratings Ta = 25°C

Parameter	Symbol	Rating	Unit	
Drain-Source Voltage	VDS	-30	V	
Gate-Source Voltage	Vgs	±20	V	
Continuous Drain Current Ta = 25 ℃	lo	-4.2		
Ta = 70 ℃	טו	-3.5	А	
Pulsed Drain Current	Ірм	-20	,	
Power Dissipation Ta = 25°C	PD	1.4	W	
Ta = 70°C	רט	1	VV	
Thermal Resistance.Junction- to-Ambient $t \le 10s$	RthJA	90		
Steady State	IXIIJA	125	°C/W	
Thermal Resistance.Junction- to-Lead	RthJL	60		
Junction Temperature	TJ	150	°C	
Storage Temperature Range	Tstg	-55 to 150		



# UMW AO3407A P-Channel Enhancement MOSFET

#### ■ Electrical Characteristics Ta = 25°C

Parameter	Symbol	Test Conditions	Min	Тур	Max	Unit
Drain-Source Breakdown Voltage	VDSS	ID=-250 µ A, VGS=0V	-30			V
Zara Cata Valtaga Brain Correct	l- a a	Vps=-24V, Vgs=0V			-1	μ <b>А</b>
Zero Gate Voltage Drain Current	IDSS	VDS=-24V, VGS=0V, TJ=55℃			-5	
Gate-Body leakage current	Igss	V <sub>DS</sub> =0V, V <sub>GS</sub> =±20V			±100	nA
Gate Threshold Voltage	VGS(th)	VDS=VGS ID=-250 μ A	-1	-1.8	-3	V
Static Drain-Source On-Resistance	Ros on	Vgs=-10V, ID=-4.1A			52	mΩ
		VGS=-4.5V, ID=-3A			70	
On state drain current	ID(ON)	VGS=-4.5V, VDS=-5V	-10			Α
Forward Transconductance	gFS	VDS=-5V, ID=-4A	5.5	8.2		S
Input Capacitance	Ciss	Vgs=0V, Vbs=-15V, f=1MHz		700		pF
Output Capacitance	Coss			120		
Reverse Transfer Capacitance	Crss			75		
Gate resistance	Rg	VGS=0V, VDS=0V, f=1MHz		10		Ω
Total Gate Charge	Qg	Vgs=-4.5V, Vps=-15V, Ip=-4A		14.3		nC
Gate Source Charge	Qgs			7		
Gate Drain Charge	Qgd			3.1		
Turn-On DelayTime	td(on)	Vgs=-10V, Vds=-15V, Rl=3.6 $\Omega$ ,Rgen=3 $\Omega$		8.6		ns
Turn-On Rise Time	tr			5		
Turn-Off DelayTime	td(off)			28.2		
Turn-Off Fall Time	<b>t</b> f			13.5		
Body Diode Reverse Recovery Time	trr	IF=-4A, di/dt=100A/ μ s		27		
Body Diode Reverse Recovery Charge	Qrr			15		nC
Maximum Body-Diode Continuous Current	Is				-2.2	Α
Diode Forward Voltage	VsD	Is=-1A,VGS=0V		-0.77	-1	V



#### ■ Typical Characterisitics

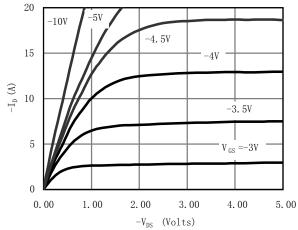


Figure 1: On-Region Characteristics

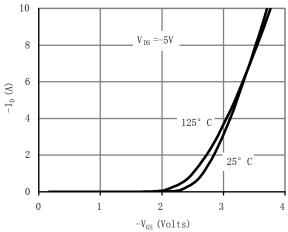


Figure 2: Transfer Characteristics

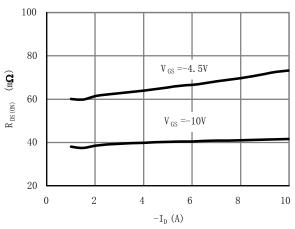


Figure 3: On-Resistance vs. Drain Current and Gate Voltage

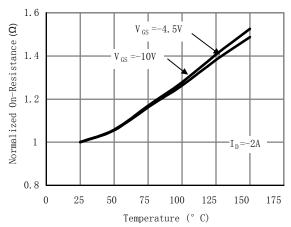


Figure 4: On-Resistance vs. Junction Temperature

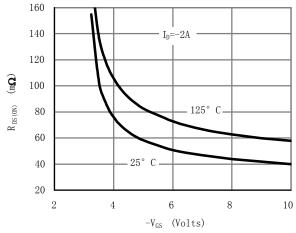


Figure 5: On-Resistance vs. Gate-Source Voltage  $\,$ 

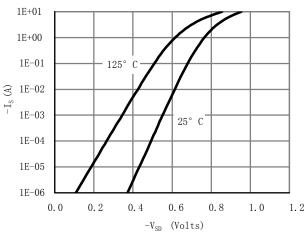


Figure 6: Body-Diode Characteristics



#### ■ Typical Characterisitics

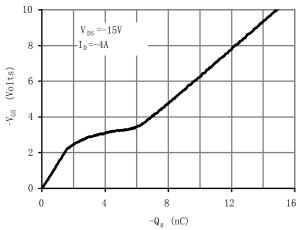


Figure 7: Gate-Charge Characteristics

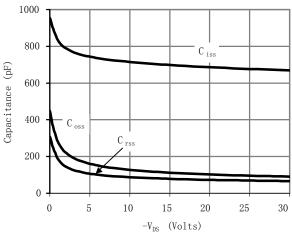


Figure 8: Capacitance Characteristics

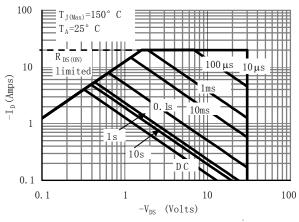


Figure 9: Maximum Forward Biased Safe Operating Area (Note E)

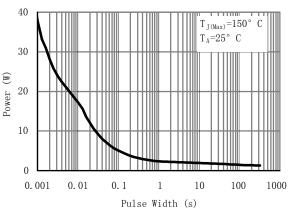


Figure 10: Single Pulse Power Rating Junction-to-Ambient (Note E)

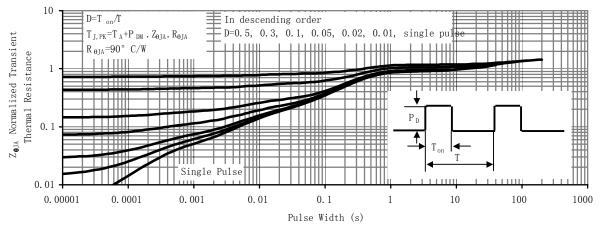
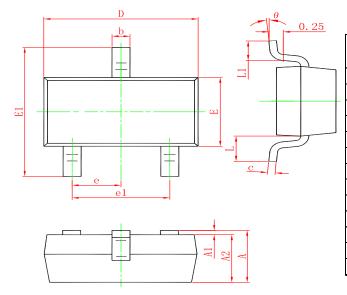


Figure 11: Normalized Maximum Transient Thermal Impedance

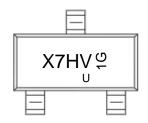


### **SOT-23 PACKAGE OUTLINE DIMENSIONS**



Cumbal	Dimensions In Millimeters		Dimensions In Inches		
Symbol	Min.	Max.	Min.	Max.	
Α	0.900	1.150	0.035	0.045	
A1	0.000	0.100	0.000	0.004	
A2	0.900	1.050	0.035	0.041	
b	0.300	0.500	0.012	0.020	
С	0.080	0.150	0.003	0.006	
D	2.800	3.000	0.110	0.118	
E	1.200	1.400	0.047	0.055	
E1	2.250	2.550	0.089	0.100	
е	0.950	TYP.	0.037 TYP.		
e1	1.800	2.000	0.071	0.079	
L	0.550 REF.		0.022 REF.		
L1	0.300	0.500	0.012	0.020	
θ	0°	8°	0°	8°	

## Marking



## Ordering information

Order code	Package	Baseqty	Deliverymode
UMW AO3407A	SOT-23	3000	Tape and reel