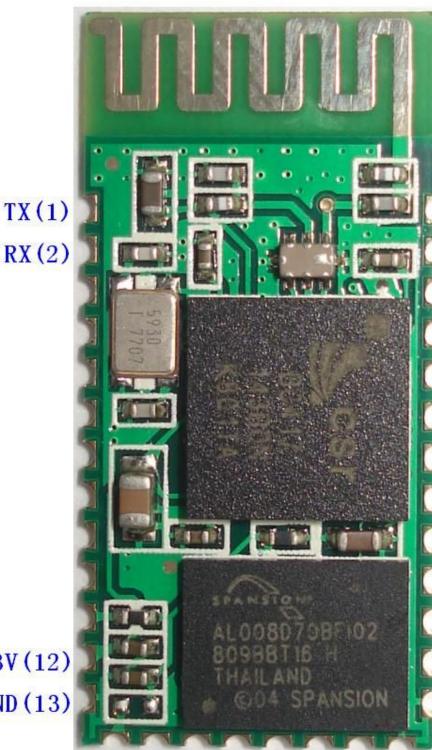
目 录

- 1. 产品图片
- 2. 特征
- 3. 引脚定义和功能
- 4. 产品参数和规格
- 5. 参考原理图
- 6. 调试设备
- 7. 测试数据规格
- 8. 测试方案图表
- 9. AT 指令集

1. 产品图片



KEY (26)

LED (24)

3. 3V (12)

GND (13)

2. 特征

● 无线收发

- ▶ 灵敏度(误码率)达到 -80dBm
- ▶ -4 -> 6dBm 功率可调输出

● 性能概要 完整的蓝牙解决方案

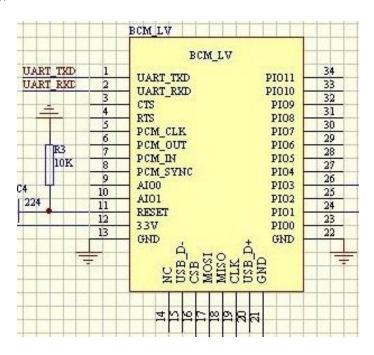
- ▶ 蓝牙 2.0 带 EDR, 2Mbps-3Mbps 调制度
- ▶ 内置 2.4GHz 天线, 用户无需调试天线
- ▶ 外置 8Mbit FLASH
- ▶ 低电压 3.3V 工作(3.1V~4.2V)配对时 30~40MA 波动,配对完毕通信 8MA
- ▶ 可选 PIO 控制
- ▶ 标准 HCI 端口(UART or USB)
- > USB 协议: Full Speed USB1.1, Compliant With 2.0
- ▶ 模块可以作为 SMD 贴片工艺
- ➤ RoHS 制程
- ▶ 引脚半孔工艺
- ▶ 数字 2.4GHz 无线收发射
- ➤ CSR BC04 蓝牙芯片技术
- ▶ 自适应跳频技术
- ➤ 体积小,(27mm×13mm×2mm)
- ▶ 简单的外围设计电路
- ▶ 蓝牙 Class 2 功率级别
- ▶ 存储温度: -40 至+85 度, 工作温度: -25 至+75 度
- ▶ 协波干扰: 2.4MHz, 发射功率 3 dBm
- ▶ 误码率: 0, 但会在传输链路产生信号衰变, 才有误码, 如 RS232 和 TTL 线路处理线路 中
- 低功耗
- 高性能无线收发系统
- 低成本
- 应用领域

- ▶ 蓝牙车载免提
- ➤ 蓝牙 GPS
- ➤ 蓝牙 PCMCIA, USB Dongle
- ▶ 蓝牙数据传送

软件

> CSR

3. 引脚定义和功能



本原理图与实物脚序一致

PIN Name	PIN#	Pad type	Description	Note
GND	13 21 22	VSS	Ground pot	
1V8	14	VDD	Integrated 1.8V (+) supply with On-chip linear regulator output within 1.7-1.9V	
VCC	12	3.3V		
AIO0	9	Bi-Directional	Programmable input/output line	
AIO1	10	Bi-Directional	Programmable input/output line	
PIO0 23 Bi-Directional RX EN			Programmable input/output line, control output for LNA(if fitted)	

PIO1 24 Bi-Directional TX EN control output for PA(if fitted) PIO2 25 Bi-Directional Programmable input/output line PIO3 26 Bi-Directional Programmable input/output line PIO4 27 Bi-Directional Programmable input/output line PIO5 28 Bi-Directional Programmable input/output line PIO6 29 Bi-Directional Programmable input/output line PIO7 30 Bi-Directional Programmable input/output line PIO8 31 Bi-Directional Programmable input/output line PIO9 32 Bi-Directional Programmable input/output line PIO10 33 Bi-Directional Programmable input/output line PIO11 34 Bi-Directional Programmable input/output line PIO11 34 Bi-Directional Programmable input/output line PIO11 34 Bi-Directional Programmable input/output line PIO12 35 Bi-Directional Programmable input/output line PIO13 Bi-Directional Programmable input/output line PIO14 36 Bi-Directional Programmable input/output line PIO15 37 Bi-Directional Programmable input/output line PIO16 38 Bi-Directional Programmable input/output line PIO17 30 Bi-Directional Programmable input/output line PIO18 Bi-Directional Programmable input/output line PIO19 32 Bi-Directional Programmable input/output line PIO19 34 Bi-Directional Programmable input/output line PIO10 35 Bi-Directional Programmable input/output line PIO10 36 Bi-Directional Programmable input/output line PIO10 37 Bi-Directional Programmable input/output line PIO10 30 Bi-Direct			I		
PIO2 25 Bi-Directional Programmable input/output line PIO3 26 Bi-Directional Programmable input/output line PIO4 27 Bi-Directional Programmable input/output line PIO5 28 Bi-Directional Programmable input/output line PIO6 29 Bi-Directional Programmable input/output line PIO7 30 Bi-Directional Programmable input/output line PIO8 31 Bi-Directional Programmable input/output line PIO9 32 Bi-Directional Programmable input/output line PIO10 33 Bi-Directional Programmable input/output line PIO11 34 Bi-Directional Programmable input/output line PIO11 34 Bi-Directional Programmable input/output line PIO11 34 Bi-Directional Programmable input/output line CMOS Input with weak internal pull-down CMOS output, tri-stable with weak internal pull-down CMOS input with weak internal pull-down CMOS output, Tri-stable with weak internal pull-down CMOS input with weak internal pull-que CMOS input with weak intern	PIO1	24	Bi-Directional		
PIO3 26 Bi-Directional Programmable input/output line PIO4 27 Bi-Directional Programmable input/output line PIO5 28 Bi-Directional Programmable input/output line PIO6 29 Bi-Directional Programmable input/output line PIO7 30 Bi-Directional Programmable input/output line PIO8 31 Bi-Directional Programmable input/output line PIO9 32 Bi-Directional Programmable input/output line PIO10 33 Bi-Directional Programmable input/output line PIO10 33 Bi-Directional Programmable input/output line PIO11 34 Bi-Directional Programmable input/output line PIO11 34 Bi-Directional Programmable input/output line PIO11 34 Bi-Directional Programmable input/output line CMOS Input with weak internal pull-down CMOS output, tri-stable with weak internal pull-up CMOS input with weak internal pull-down CMOS input with weak internal pull-down CMOS output, Tri-stable with weak internal pull-up CMOS input with weak internal pull-up CMOS input with weak internal pull-up CMOS input with weak internal pull-down CMOS input with weak internal pull-up CMOS input with w				control output for PA(if fitted)	
PIO4 27 Bi-Directional Programmable input/output line PIO5 28 Bi-Directional Programmable input/output line PIO6 29 Bi-Directional Programmable input/output line PIO7 30 Bi-Directional Programmable input/output line PIO8 31 Bi-Directional Programmable input/output line PIO9 32 Bi-Directional Programmable input/output line PIO10 33 Bi-Directional Programmable input/output line PIO11 34 Bi-Directional Programmable input/output line PRESETB 11 Weak internal pull-down CMOS Input with Weak internal pull-up CMOS input with Weak internal pull-down UART_CTS 3 Weak internal pull-down CMOS input with Weak internal pull-down CMOS output, Tri-stable with weak internal pull-up CMOS output, Tri-stable with weak internal pull-up CMOS input with Weak internal pull-down SPI_CIK 19 CMOS input with Serial peripheral interface clock	PIO2	25	Bi-Directional	Programmable input/output line	
PIO5 28 Bi-Directional Programmable input/output line PIO6 29 Bi-Directional Programmable input/output line CLK_REQ PIO7 30 Bi-Directional Programmable input/output line CLK_OUT PIO8 31 Bi-Directional Programmable input/output line PIO9 32 Bi-Directional Programmable input/output line PIO10 33 Bi-Directional Programmable input/output line PIO11 34 Bi-Directional Programmable input/output line PRESETB 11 weak intemal pull-down CMOS Input with Weak intemal pull-down CMOS output, tri-stable with weak internal pull-up CMOS input with Weak internal pull-down CMOS input with Weak internal pull-down CMOS output, Tri-stable with weak internal pull-up CMOS input with Weak internal pull-down CMOS input with SPI_CSB 16 CMOS input with Weak internal pull-up SPI_CIK 19 CMOS input with Serial peripheral interface clock	PIO3	26	Bi-Directional	Programmable input/output line	
PIO6 29 Bi-Directional Programmable input/output line PIO7 30 Bi-Directional Programmable input/output line PIO8 31 Bi-Directional Programmable input/output line PIO9 32 Bi-Directional Programmable input/output line PIO10 33 Bi-Directional Programmable input/output line PIO11 34 Bi-Directional Programmable input/output line Progr	PIO4	27	Bi-Directional	Programmable input/output line	
PIO7 30 Bi-Directional Programmable input/output line PIO8 31 Bi-Directional Programmable input/output line PIO9 32 Bi-Directional Programmable input/output line PIO10 33 Bi-Directional Programmable input/output line PIO11 34 Bi-Directional Programmable input/output line PIO11 34 Bi-Directional Programmable input/output line Programmable i	PIO5	28	Bi-Directional	Programmable input/output line	
PIO8 31 Bi-Directional Programmable input/output line PIO9 32 Bi-Directional Programmable input/output line PIO10 33 Bi-Directional Programmable input/output line PIO11 34 Bi-Directional Programmable input/output line PIO11 34 Bi-Directional Programmable input/output line CMOS Input with weak internal pull-down CMOS output, tri-stable with weak internal pull-down CMOS input with Weak internal pull-down CMOS input with Weak internal pull-down CMOS output, Tri-stable with weak internal pull-down CMOS output, Tri-stable with weak internal pull-up CMOS input with Weak internal pull-down CMOS input with Weak internal pull-down CMOS input with Weak internal pull-down CMOS input with Weak internal pull-up CMOS input with Weak internal interface data input CMOS input with Weak internal interface clock	PIO6	29	Bi-Directional	Programmable input/output line	CLK_REQ
PIO9 32 Bi-Directional Programmable input/output line PIO10 33 Bi-Directional Programmable input/output line PIO11 34 Bi-Directional Programmable input/output line PIO11 34 Bi-Directional Programmable input/output line CMOS Input with weak internal pull-down CMOS output, tri-stable with weak internal pull-up CMOS input with weak internal pull-down CMOS input with weak internal pull-down CMOS output, Tri-stable with weak internal pull-down CMOS output, Tri-stable with weak internal pull-up CMOS input with weak internal pull-up CMOS input with weak internal pull-down CMOS input with Serial peripheral interface clock CMOS input with Serial peripheral interface clock	PIO7	30	Bi-Directional	Programmable input/output line	CLK_OUT
PIO10 33 Bi-Directional Programmable input/output line PIO11 34 Bi-Directional Programmable input/output line CMOS Input with weak internal pull-down CMOS output, tri-stable with weak internal pull-up CMOS input with weak internal pull-down CMOS output, Tri-stable with weak internal pull-up CMOS output, Tri-stable with weak internal pull-up CMOS input with weak internal pull-up CMOS input with weak internal pull-up CMOS input with weak internal pull-down CMOS input with weak internal pull-up CMOS input with weak internal pull-up CMOS input with weak internal interface data input pull-down CMOS input with weak internal pull-up CMOS input with weak internal interface, active low CMOS input with weak internal pull-up CMOS input with weak internal interface, active low CMOS input with Serial peripheral interface clock	PIO8	31	Bi-Directional	Programmable input/output line	
PIO11 34 Bi-Directional Programmable input/output line CMOS Input with weak internal pull-down CMOS output, tri-stable with weak internal pull-up CMOS input with weak internal pull-down CMOS output, Tri-stable with weak internal pull-up CMOS output, Tri-stable with weak internal pull-up CMOS input with weak internal pull-up CMOS input with weak internal pull-up CMOS input with weak internal pull-down CMOS input with weak internal pull-up CMOS input with weak internal pull-up CMOS input with weak internal interface data input pull-up CMOS input with weak internal pull-up CMOS input with weak internal interface, active low	PIO9	32	Bi-Directional	Programmable input/output line	
RESETB 11 CMOS Input with weak internal pull-down UART_RTS 4 tri-stable with weak internal pull-up CMOS input with Weak internal pull-up CMOS input with Weak internal pull-down CMOS input with Weak internal pull-down CMOS output, Tri-stable with weak internal pull-up CMOS input with Weak internal pull-up CMOS output, Tri-stable with weak internal pull-up CMOS input with Weak internal pull-up CMOS input with Weak internal pull-down CMOS input with Serial peripheral interface data input Chip select for serial peripheral interface, active low SPI_CLK SPI_CLK 19 CMOS input with Serial peripheral interface clock	PIO10	33	Bi-Directional	Programmable input/output line	
RESETB 11 weak internal pull-down CMOS output, tri-stable with weak internal pull-up CMOS input with weak internal pull-down UART_CTS 3 weak internal pull-down CMOS input with weak internal pull-down CMOS input with weak internal pull-down CMOS output, Tri-stable with weak internal pull-up CMOS input with weak internal pull-up CMOS input with weak internal pull-up CMOS input with weak internal pull-down CMOS input with weak internal interface data input CMOS input with weak internal interface, active low SPI_CIK 19 CMOS input with Serial peripheral interface clock	PIO11	34	Bi-Directional	Programmable input/output line	
UART_RTS 4 tri-stable with weak internal pull-up UART_CTS 3 CMOS input with weak internal pull-down UART_CTS 3 Weak internal pull-down UART_RX 2 Weak internal pull-down UART_TX 1 CMOS input with weak internal pull-down CMOS output, Tri-stable with weak internal pull-up CMOS input with weak internal pull-up CMOS input with weak internal pull-down SPI_CSB 16 CMOS input with weak internal pull-up CMOS input with weak internal pull-down CMOS input with weak internal pull-up CMOS input with weak internal interface, active low SPI_CLK 19 CMOS input with Serial peripheral interface clock			CMOS Input with		
UART_RTS 4 tri-stable with weak internal pull-up CMOS input with weak internal pull-down UART_CTS 3 weak internal pull-down CMOS input with weak internal pull-down CMOS output, Tri-stable with weak internal pull-up CMOS input with weak internal pull-down CMOS input with weak internal pull-down CMOS input with weak internal pull-down CMOS input with weak internal pull-up CMOS input with weak internal pull-up CMOS input with weak internal interface data input CMOS input with weak internal pull-up CMOS input with weak internal interface, active low SPI_CLK 19 CMOS input with Serial peripheral interface clock	RESETB	11	weak intemal		
UART_RTS 4 tri-stable with weak internal pull-up CMOS input with weak internal pull-down UART_CTS 3 weak internal pull-down CMOS input with weak internal pull-down CMOS output, Tri-stable with weak internal pull-up CMOS input with Weak internal pull-up CMOS input with Weak internal pull-up CMOS input with SPI_MOSI 17 weak internal pull-down CMOS input with Serial peripheral interface clock SPI_CLK 19 CMOS input with Serial peripheral interface clock			pull-down		
UART_CTS 3 weak internal pull-up UART_CTS 3 weak internal pull-down CMOS input with weak internal pull-down UART_RX 2 weak internal pull-down CMOS output, Tri-stable with weak internal pull-up CMOS input with Wart Data output UART Data output SPI_MOSI 17 weak internal pull-down CMOS input with weak internal interface data input CMOS input with weak internal interface, active low SPI_CLK 19 CMOS input with Serial peripheral interface clock	UART_RTS	4	CMOS output,		
UART_CTS 3 weak internal pull-down UART_RX 2 weak internal pull-down CMOS input with weak internal pull-down CMOS output, Tri-stable with weak internal pull-up CMOS input with SPI_MOSI 17 weak internal pull-down CMOS input with weak internal pull-up CMOS input with weak internal interface data input pull-down CMOS input with weak internal pull-up CMOS input with weak internal interface, active low SPI_CLK 19 CMOS input with Serial peripheral interface clock			tri-stable with weak	UART request to send, active low	
UART_CTS 3 weak internal pull-down CMOS input with weak internal pull-down UART_TX 2 weak internal pull-down CMOS output, Tri-stable with weak internal pull-up CMOS input with weak internal pull-down SPI_MOSI 17 weak internal pull-down CMOS input with weak internal pull-up CMOS input with weak internal pull-up CMOS input with weak internal pull-up CMOS input with Serial peripheral interface clock			internal pull-up		
pull-down CMOS input with weak internal pull-down CMOS output, Tri-stable with weak internal pull-up CMOS input with Weak internal pull-up CMOS input with SPI_MOSI SPI_CSB 16 CMOS input with Weak internal pull-down CMOS input with Weak internal pull-down CMOS input with Weak internal pull-down CMOS input with Weak internal pull-up CMOS input with Weak internal interface data input pull-up CMOS input with Weak internal interface, active low Serial peripheral interface clock		3	CMOS input with		
UART_RX 2 CMOS input with weak internal pull-down UART_TX 1 CMOS output, Tri-stable with weak internal pull-up CMOS input with weak internal pull-up CMOS input with weak internal pull-down SPI_CSB 16 CMOS input with weak internal pull-up CMOS input with weak internal pull-down CMOS input with weak internal pull-up CMOS input with weak internal pull-up CMOS input with Serial peripheral interface clock SPI_CLK 19 CMOS input with Serial peripheral interface clock	UART_CTS		weak internal	UART clear to send, active low	
UART_RX 2 weak internal pull-down CMOS output, Tri-stable with weak internal pull-up CMOS input with weak internal pull-down SPI_MOSI 17 weak internal pull-down CMOS input with weak internal pull-down CMOS input with weak internal pull-down CMOS input with weak internal pull-up CMOS input with weak internal interface, active low SPI_CLK 19 CMOS input with Serial peripheral interface clock			pull-down		
DUART_TX 1 CMOS output, Tri-stable with weak internal pull-up CMOS input with SPI_MOSI SPI_CSB 16 CMOS input with weak internal pull-down CMOS input with weak internal pull-up CMOS input with weak internal pull-up CMOS input with weak internal pull-up CMOS input with Serial peripheral interface clock Serial peripheral interface clock		2	CMOS input with		
UART_TX 1 CMOS output, Tri-stable with weak internal pull-up CMOS input with weak internal pull-down SPI_CSB 16 CMOS input with weak internal pull-down CMOS input with weak internal pull-up Chip select for serial peripheral interface, active low CMOS input with Serial peripheral interface clock SPI_CLK 19 CMOS input with Serial peripheral interface clock	UART_RX		weak internal	UART Data input	
UART_TX 1 Tri-stable with weak internal pull-up CMOS input with weak internal pull-down SPI_CSB 16 CMOS input with weak internal pull-up CMOS input with weak internal interface data input pull-down CMOS input with weak internal pull-up CMOS input with weak internal interface, active low SPI_CLK 19 CMOS input with Serial peripheral interface clock			pull-down		
UART_TX 1 weak internal pull-up CMOS input with weak internal pull-down SPI_CSB 16 CMOS input with weak internal pull-up CMOS input with weak internal interface, active low CMOS input with weak internal pull-up CMOS input with Serial peripheral interface, active low SPI_CLK 19 CMOS input with		1	CMOS output,		
weak internal pull-up CMOS input with weak internal pull-down SPI_CSB 16 CMOS input with weak internal pull-down CMOS input with weak internal pull-up CMOS input with Serial peripheral interface, active low CMOS input with Serial peripheral interface clock	LIADT TV		Tri-stable with	HADED	
SPI_CSB 16 CMOS input with weak internal pull-down CMOS input with weak internal pull-up CMOS input with weak internal interface, active low CMOS input with Serial peripheral interface clock SPI_CLK 19 CMOS input with Serial peripheral interface clock	UARI_IX		weak internal	UART Data output	
SPI_CSB 16 weak internal pull-down CMOS input with weak internal pull-up CMOS input with SPI_CSB 16 CMOS input with SPI_CSB 16 CMOS input with Serial peripheral interface, active low SPI_CLK 19 CMOS input with Serial peripheral interface clock			pull-up		
pull-down CMOS input with weak internal pull-up CMOS input with weak internal interface, active low CMOS input with Serial peripheral interface clock	SPI_MOSI	[17	CMOS input with		
SPI_CSB 16 CMOS input with weak internal pull-up Chip select for serial peripheral interface, active low CMOS input with Serial peripheral interface clock			weak internal	Serial peripheral interface data input	
SPI_CSB 16 weak internal pull-up Chip select for serial peripheral interface, active low SPI_CLK 19 CMOS input with Serial peripheral interface clock			pull-down		
SPI_CSB 16 weak internal interface, active low pull-up CMOS input with Serial peripheral interface clock	SPI_CSB	3 16	CMOS input with	China alay 6	
pull-up CMOS input with Serial peripheral interface clock			weak internal	• • •	
SPLCLK 19 Serial peripheral interface clock			pull-up	interface, active low	
SPI_CLK 19 Serial peripheral interface clock weak internal	CDI CLIZ	10	CMOS input with	Carriel a ariel and l'acceptant la	
	SPI_CLK	19	weak internal	Serial peripheral interface clock	

		pull-down		
SPI_MISO	18	CMOS input with weak internal pull-down	Serial peripheral interface data Output	
USB	15	Bi-Directional		
USB_+	20	Bi-Directional		
1.8V	14		可以外部供电 1.8V	默认为内部 供电 1.8V
PCM_CLK	5	Bi-Directional		
PCM_OUT	6	CMOS output		
PCM_IN	7	CMOS Input		
PCM_SYNC	8	Bi-Directional		

4. 产品参数和规格

LINVOR BLUE T



www. linvor.com

CSR,BC417143B V 2.0

2006/09/6

蓝牙 RF 模块

- 1. 采用 CSR BC4 +8M FLASH 方案
- 2. 具有 PIO0-PIO11、AIO0、AIO1、 USB、PCM、UART 及 SPI 接口, 模块内置 8MFLASH,功能强大, 用户可定制软件,适用于各种蓝牙 设备,内置 RF 天线,便于调试。

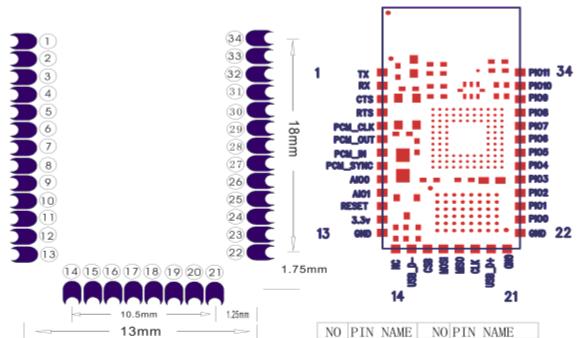
蓝牙协议版本	Bluetooth Specification V2.0 With EDR
USB 协议	Full Speed USB V1.1
USB Protocol	Compliant With USB V2.0
頻率	2.4Ghz ISM band
调制方式	GFSK(Gaussian Frequency Shift Keying)
发射功率	-4 ->4 dBm, Class 2
灵敏度	≤-80dBm at 0.1% BER
通讯速率	Asynchronous:2Mbps(Max)
供电电源	3.3V
工作温度	-20~+55 Centigrade
封装尺寸	27mmX13mmX2mm

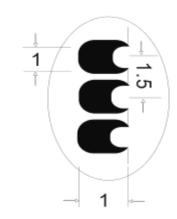
LINVOR BLUE T

www. linvor.com

LV-BC-2.0

单位: mm

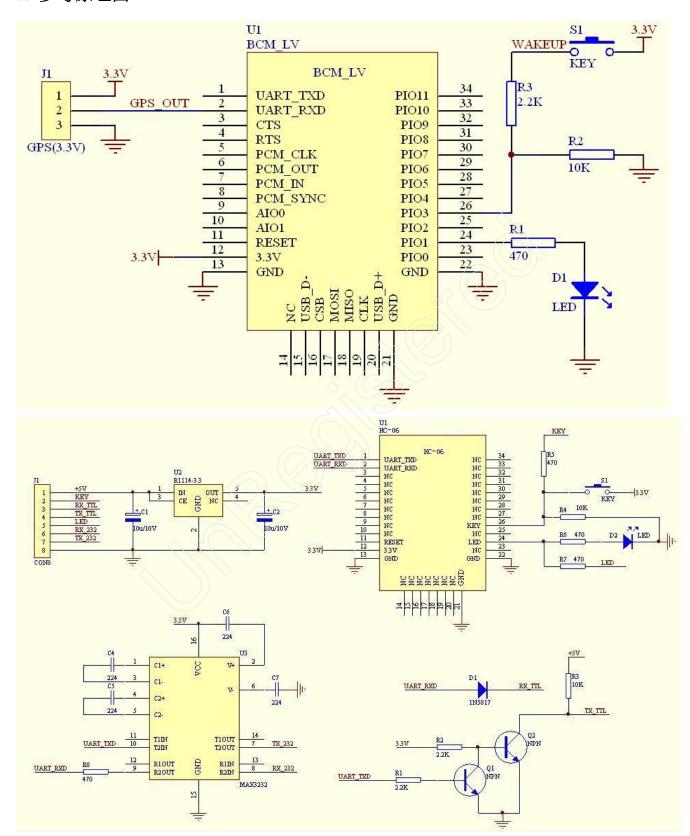




PCB Layout 请参考实物

NO	PIN NAME	NO	PIN NAME
1	TX		USB D+
2	RX		GND
3	CTS		GND
4	RTS	23	PI00
5	PCM_CLK	24	PI01
6	PCM OUT	25	PI02
7	PCM IN	26	PI03
8	PCM SYNC	27	PIO4
9	A100	28	PI05
10	AIO1	29	PI06
11	RESET	30	PI07
12	3. 3V	31	PI08
13	GND	32	PI09
14	NC	33	PI010
15	USB D-	34	PI011
16	CSB		
17	MOSI		
18	MISO		
19	CLK		

5. 参考原理图



对于 HC-04, HC-06 主机而言,WAKEUP 按下后会放弃记忆,重新搜索新的从机,如果不放弃记忆,主机将一直搜索上一次配对过的从机,直到搜到并配对成功为止,HC-04/06 的主机有个特性就是记忆最后一次配对过的从机。WAKEUP 对于从机而言没有意义。

6. 调试设备

6.1 仪器设备

电脑、 硬件、3G 频率计(SP3386)、 3.15V DC 电源、屏蔽房、 MT8850A (or MT8852B)、 蓝牙测试盒。

6.2 软件

7. 测试数据规格

		Test Condition 25℃ RH 65%			
		Min	Typ	Max	Unit
<u>1.</u>	Carrier Freq. (ISM Band)	2.4		2.4835	MHz
2.	RF O/P Power	-6	2	4	dBm
3.	Step size of Power control	2		8	dB
<u>4.</u>	Freq. Offset (Typical Carrier freq.)	-75		75	KHz
<u>5.</u>	Carrier Freq. drift (Hopping on, drift rate/50uS)	-20		20	KHz
	1 slot packet	-25		25	KHz
	3 slot packet	-40		-40	KHz
6.	Average Freq. Deviations (Hopping off, modulation	ı) 140		175	KHz
	Freq. Deviation	115			KHz
	Ratio of Freq. Deviation	0.8			
<u>7.</u>	Receive Sensitivity @< 0.1% BER(Bit error rate)-83			dBm

8. 测试方案图表

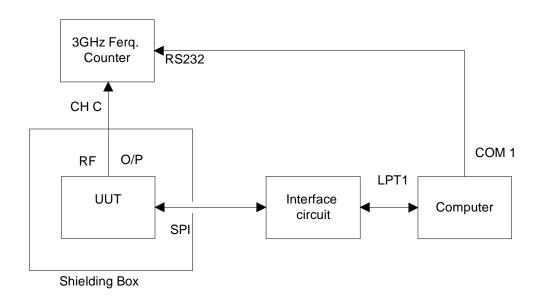


Fig 1. Programming and Freq. Alignment

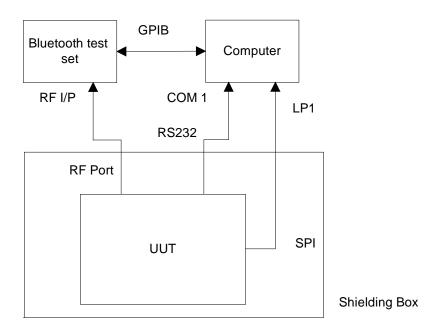


Fig 2 RF parameter Test Procedure

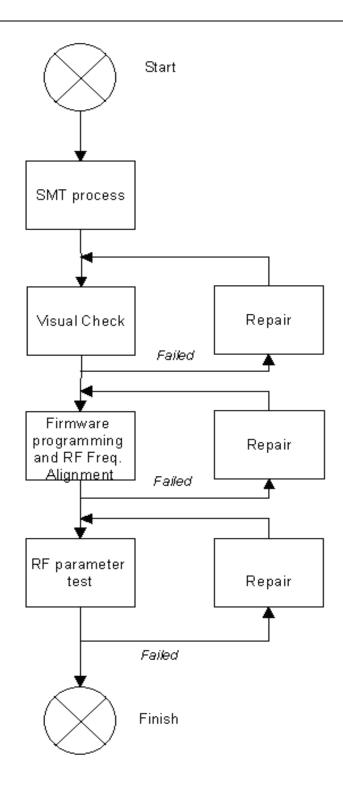


Fig 3 Assemble/Alignment/Testing Flow Chart

9. AT 指令集

进入 AT 指令的方法: 给模块上电,不配对的情况下,就是 AT 模式了。指令间隔 1S 左右。出厂参数: 波特率 9600N81,名字 linvor,密码 1234

1、测试通讯

发送: AT(返回OK,一秒左右发一次)

返回: OK

2、改蓝牙串口通讯波特率

发送: AT+BAUD1

返回: OK1200

发送: AT+BAUD2

返回: OK2400

.....

1-----1200

2-----2400

3-----4800

4------9600 (默认就是这个设置)

5-----19200

6-----38400

7-----57600

8-----115200

9-----230400

A-----460800

B-----921600

C-----1382400

- ▶ 设置超过 115200 后用电脑无法使用,要用单片机编程于高于 115200 才能使用此波特率和重新发 AT 命令设低波特率
- ▶ 用AT 命令设好波特率后,下次上电使用不需再设,可以掉电保存波特率。

3、改蓝牙名称

发送: AT+NAMEname

返回: OKname

参数 name: 所要设置的当前名称,即蓝牙被搜索到的名称。20 个字符以内。

例: 发送 AT+NAMEbill_gates

返回 OKname

这时蓝牙名称改为 bill_gates

参数可以掉电保存,只需修改一次。PDA 端刷新服务可以看到更改后的蓝牙名称,名字不可超过 20 个字符。

4、改蓝牙配对密码

发送: AT+PINxxxx

返回: OKsetpin

参数 xxxx: 所要设置的配对密码, 4 个数字, 此命令可用于从机或主机。从机是适配器或手机弹出要求输入配对密码窗口时, 手工输入此参数就可以连接从机。主蓝牙模块搜索从机后, 如果密码正确, 则会自动配对, 主模块除了可以连接配对从模块外, 其他产品包含从模块的时候也可以连接配对, 比如含蓝牙的数码相机, 蓝牙 GPS, 蓝牙串口打印机, 等等, 特别地, 蓝牙 GPS 为典型例子

例: 发送: AT+PIN8888

返回: OKsetpin

这时蓝牙配对密码改为8888,模块在出厂时的默认配对密码是1234。

参数可以掉电保存,只需修改一次。

5、无校验设置指令: (V1.5 版后支持)

AT+PN (默认就是这个设置)

6、偶校验设置指令: (V1.5 版后支持)

AT+PE

7、奇校验设置指令: (V1.5 版后支持)

AT+PO

8、获取 AT 指令版本命令: AT+VERSION

返回 LinvorV1.n 则为正品