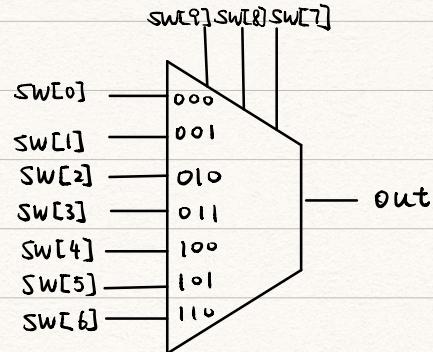


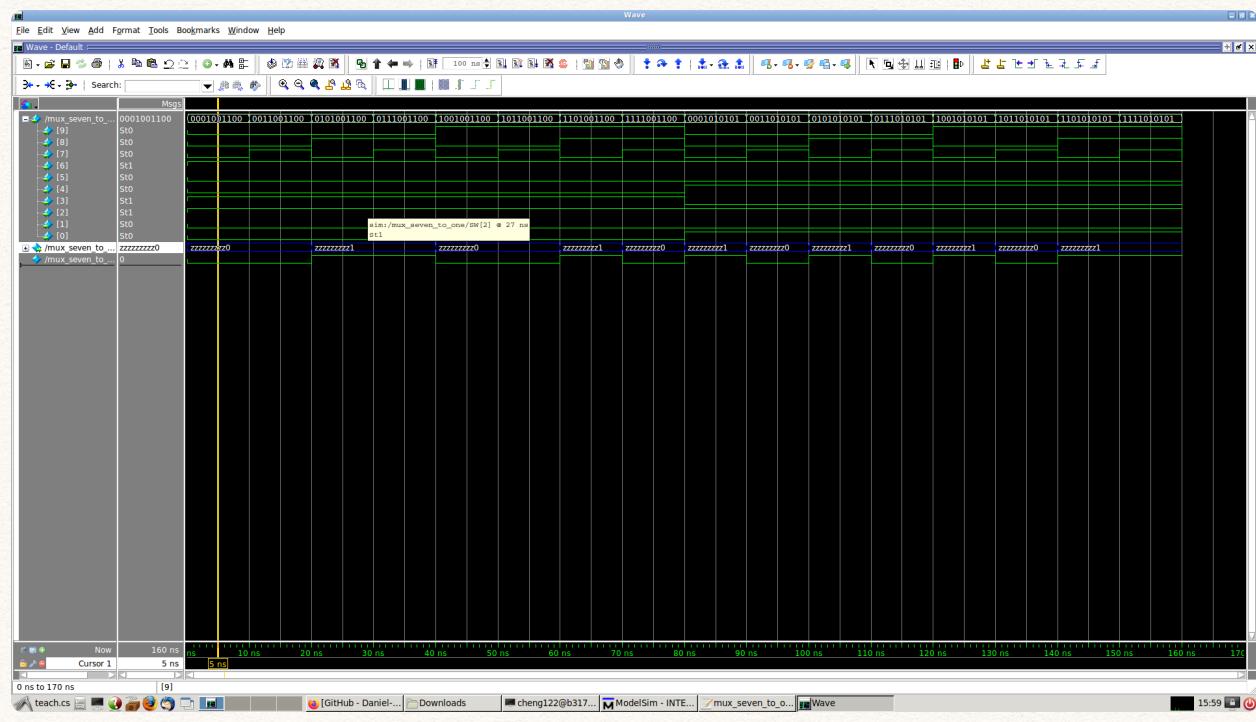
5.1 Draw a schematic showing your code structure with all wires, inputs and outputs labeled.



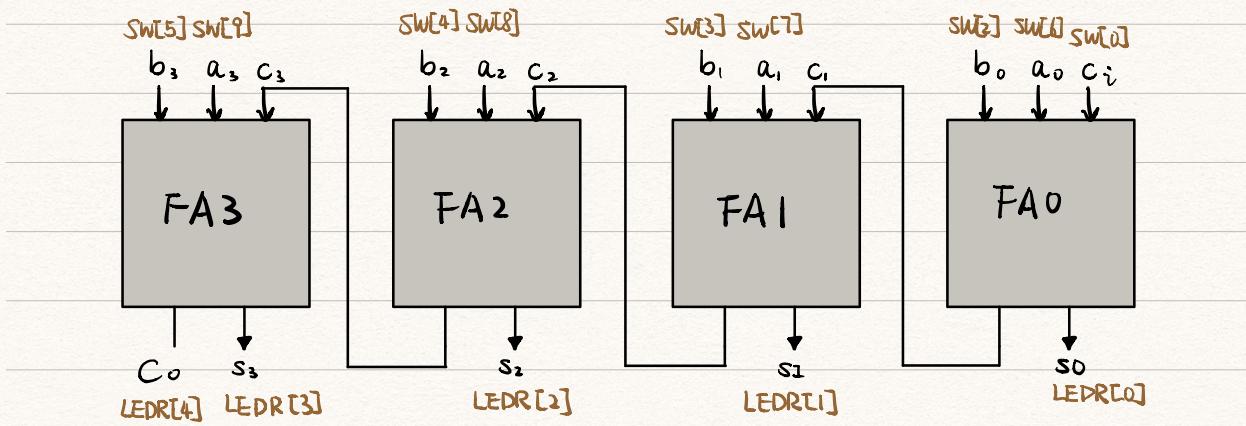
5.2 Write Verilog code for a 7-1 multiplexer based on the template provided above.

see mux-seven-to-one.v

5.3 Simulate your code with ModelSim



6.1 Draw a schematic showing your code structure.



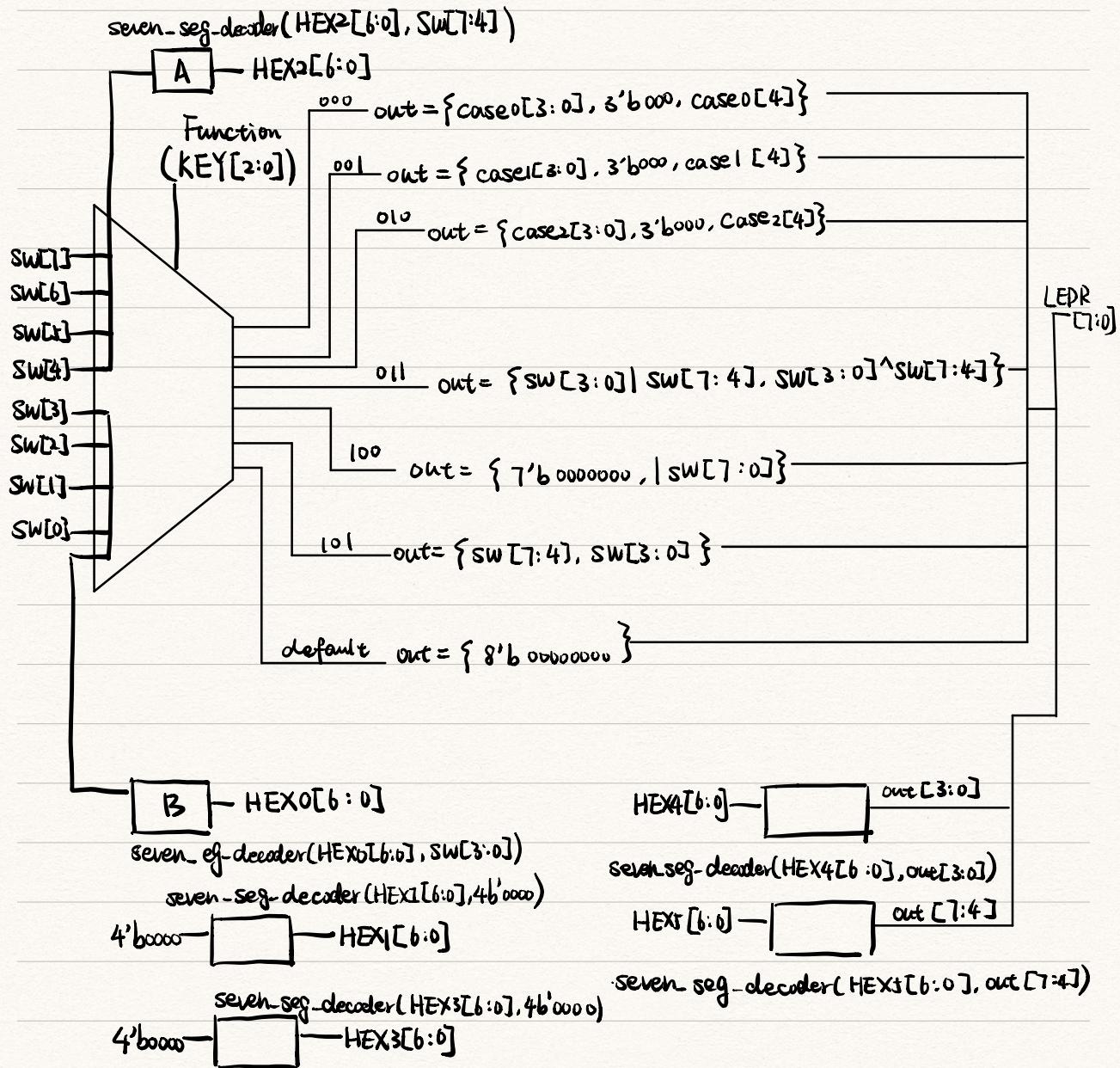
6.2 Write Verilog code for 4 bit ripple- carry adder.

See `ripple_carry_adder.v`

6.3 Simulate your code with ModelSim



7.1 Draw schematic



7.2 Write verilog code.

see ALU.v

7.3 Simulate your code on ModelSim

