

ECSE 323 Schedule - Fall 2015

Date	Instructor	Topic
8-Sep	WG	Introduction
10-Sep	WG	Introduction / Logic Minimization
15-Sep	JC	Introduction to VHDL
17-Sep	JC	Introduction to VHDL
22-Sep	WG	Logic Minimization
24-Sep	WG	CMOS circuits
29-Sep	WG	CMOS circuits
1-Oct	WG	Combinational Circuits
6-Oct	WG	Combinational Circuits
8-Oct	JC	Sequential Circuits
13-Oct	JC	Sequential Circuits
15-Oct	JC	VHDL and Design of Sequential Systems
20-Oct	JC	VHDL and Design of Sequential Systems
22-Oct	WG / JC	Midterm
27-Oct	WG	Circuit Implementation Strategies / Logic Arrays
29-Oct	WG	Circuit Implementation Strategies / Logic Arrays
3-Nov	WG	Register and Counter Design
5-Nov	WG	Finite State Machines
10-Nov	WG	Finite State Machines
12-Nov	WG	Finite State Machines
17-Nov	WG	Finite State Machines
19-Nov	WG	Testing of Combinational Circuits
24-Nov	WG	Testing of Combinational Circuits
26-Nov	JC	Case Studies in Digital System Design
1-Dec	JC	Case Studies in Digital System Design
3-Dec	WG	Testing of Sequential Circuits