

PC_Src	nPC
00	PC+4
01	PC+1+sign_ext(offest 00)
10	{PC[31:28],instr_index,00}

MemToReg	
00	ALUout
01	MD
10	PC+8
11	

ALUctrl	
000	+
001	-
010	&

Reg_DST	
00	[20:16]
01	[15:11]
10	GRF31[4:0]

EXTop	EXTout
00	zero_ext
01	signed_ext
10	lui_ext
11	b_ext

bCtrl	judge
001	bgtz
010	beq

ALUSrc	ALUOp2
0	ALUOP2
1	EXTout

RegDst	WA
00	[15:11]
01	[20:16]
10	31