	Arith signal of add signal of sub signal select Arith op(1) signal select Arith op(0)			Logical signal select logical op(1) signal select logical op(0)		comprator		Shifter Signal select shifter op(1) Signal select shifter op(0)		ALU_operation		
SLL	Signal of add	Signal of sub	signal select Arith op(1)	signal select Arith op(0)	signal select logical op(1)  *	signal select logical op(0)  *	Signal select SLT_SEQ_SLTU(1)  *	Signal select SLT,SEQ,SLTU(0)  *	Signal select shifter op(1)  O	Signal select shifter op(0)	ALU operation (1)	ALU operation (0)
SRL	0	0	*	*	*	*	*	*	0	1	1	0
SRA	0	0	*	*	*	*	*	*	1	0	1	0
ROR	0	0	*	*	*	*	*	*	1	1	1	0
ADD	1	0	0	0	*	*	*	*	*	*	0	0
SUB	0	1	0	1	*	*	*	*	*	*	0	0
SLT	0	0	*	*	*	*	0	0	*	*	1	1
SLTU	0	0	*	*	*	*	1	0	*	*	1	1
SEQ	0	0	*	*	*	*	0	1	*	*	1	1
XOR	0	0	*	*	1	1	*	*	*	*	0	1
OR	0	0	*	*	0	1	*	*	*	*	0	1
AND	0	0	*	*	0	0	*	*	*	*	0	1
NOR	0	0	*	*	1	0	*	*	*	*	0	1
MUL	0	0	1	0	*	*	*	*	*	*	0	0
MULU	0	0	1	1	*	*	*	*	*	*	0	0
SLLI	0	0	*	*	*	*	*	*	0	0	1	0
SRLI	0	0	*	*	*	*	*	*	0	1	1	0
SRAI	0	0	*	*	*	*	*	*	1	0	1	0
RORI	0	0	*	*	*	*	*	*	1	1	1	0
ADDI	1	0	0	0	*	*	*	*	*	*	0	0
SLTI	0	0	*	*	*	*	0	0	*	*	1	1
SLTIU	0	0	*	*	*	*	1	0	*	*	1	1
SEQI	0	0	*	*	*	*	0	1	*	*	1	1
XORI	0	0	*	*	1	1	*	*	*	*	0	1
ORI	0	0	*	*	0	1	*	*	*	*	0	1
ANDI	0	0	*	*	0	0	*	*	*	*	0	1
NORI	0	0	*	*	1	0	*	*	*	*	0	1
SET	0	0	*	*	*	*	*	*	*	*	*	*
SSET	0	0	*	*	*	*	*	*	*	*	*	*
JALR	0	0	*	*	*	*	*	*	*	*	*	*
LW	0	0	0	0	*	*	*	*	*	*	0	0
sw	0	0	0	0	*	*	*	*	*	*	0	0
BEQ	0	0	*	*	*	*	*	*	*	*	*	*
BNQ	0	0	*	*	*	*	*	*	*	*	*	*
BLT	0	0	*	*	*	*	*	*	*	*	*	*
BGE	0	0	*	*	*	*	*	*	*	*	*	*
BLTU	0	0	*	*	*	*	*	*	*	*	*	*
BGEU	0	0	*	*	*	*	*	*	*	*	*	*