

# Al On Chip 2023

#### **Environment Lab**

**Advisor: Tsai, Chia-Chi** 

TA: 羅祥睿 (stevensoar303203@gmail.com)

LAB: 92508 & 92526

Email: course.aislab@gmail.com

### **Outline**



Environment Setup

Change your passwd

Introduction to Linux commands

How to run Verilog code?

• 如何看波形

### **Outline**



Environment Setup

Change your passwd

Introduction to Linux commands

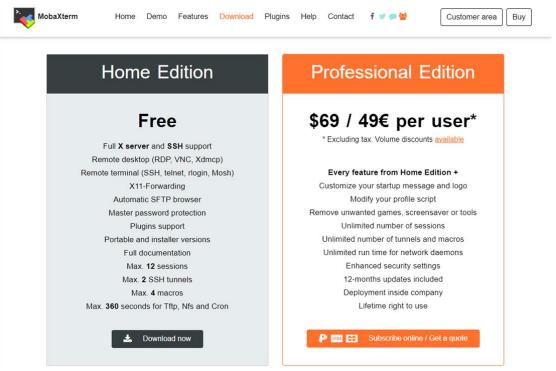
How to run Verilog code?

• 如何看波形

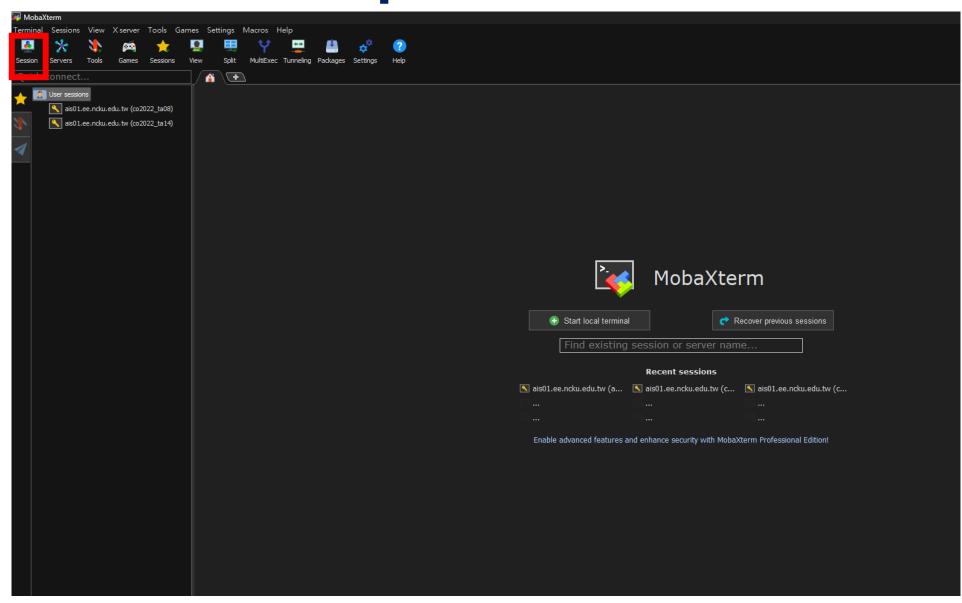


- Introduction to MobaXterm
  - MobaXterm is a free software for remote computing.
  - It provides a graphical user interface and command line shell for the server
- Download
  - MobaXterm download

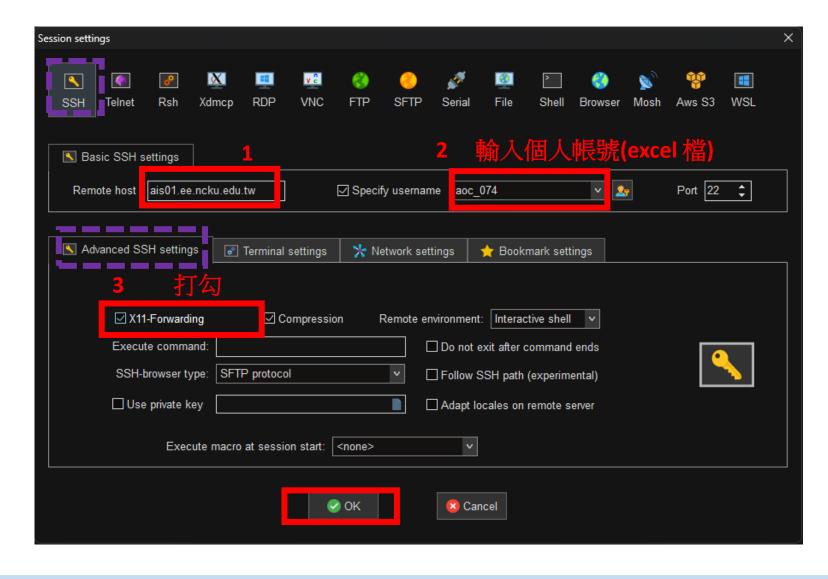




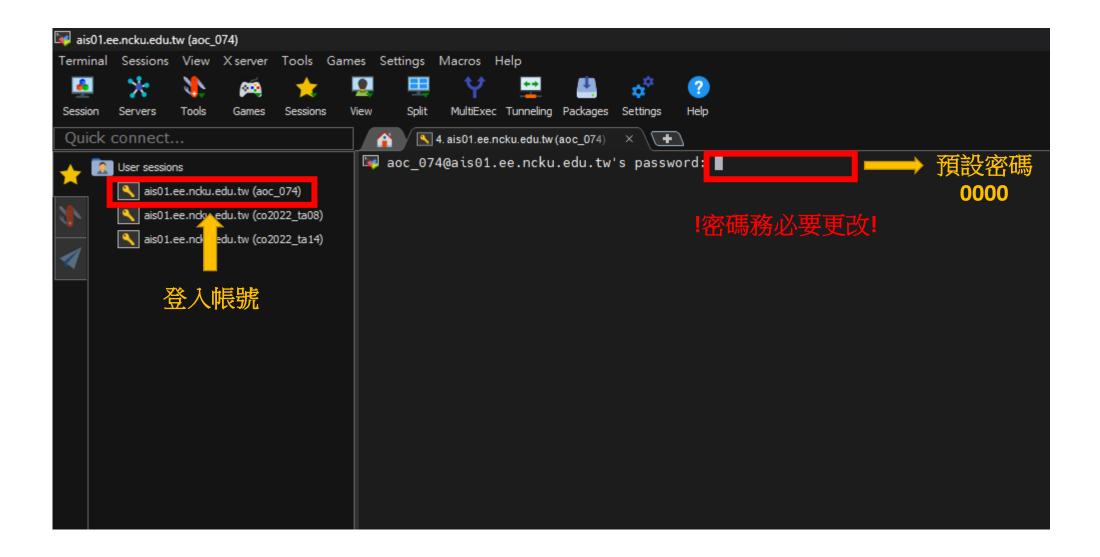












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Introduction to Linux commands

How to run Verilog code?

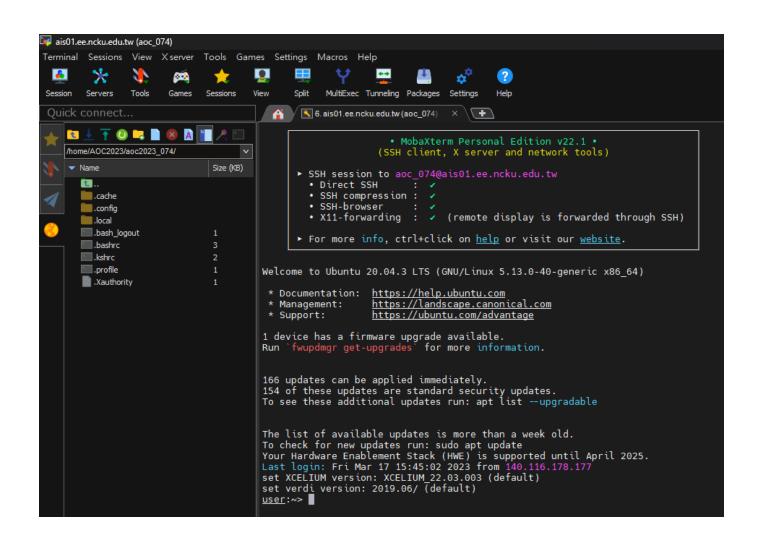
• 如何看波形

## Change your passwd (重要)



• 第一次登入密碼: 0000

• 登入結果,如右圖



## Change your passwd (重要)



• 修改方式:

輸入指令 passwd

```
6. ais01.ee.ncku.edu.tw (aoc 074)

    MobaXterm Personal Edition v22.1

                     (SSH client, X server and network tools)
      ➤ SSH session to aoc_074@ais01.ee.ncku.edu.tw

    Direct SSH

    SSH compression: 

    SSH-browser

    X11-forwarding : 

            (remote display is forwarded through SSH)

      ➤ For more info, ctrl+click on help or visit our website.
Welcome to Ubuntu 20.04.3 LTS (GNU/Linux 5.13.0-40-generic x86 64)
 * Documentation:
                    https://help.ubuntu.com
 * Management:
                    https://landscape.canonical.com
                    https://ubuntu.com/advantage
 * Support:
1 device has a firmware upgrade available.
Run `fwupdmgr get-upgrades` for more information.
166 updates can be applied immediately.
154 of these updates are standard security updates.
To see these additional updates run: apt list --upgradable
The list of available updates is more than a week old.
To check for new updates run: sudo apt update
Your Hardware Enablement Stack (HWE) is supported until April 2025.
Last login: Fri Mar 17 15:45:02 2023 from 140.116.178.177
set XCELIUM version: XCELIUM 22.03.003 (default)
set verdi version: 2019.06/ (default)
<u>user</u>:~> passwd
```

## Change your passwd (重要)



• 修改方式:

Current password 0000

```
6. ais01.ee.ncku.edu.tw (aoc_074)
                       • MobaXterm Personal Edition v22.1 •
                     (SSH client, X server and network tools)
      SSH session to aoc 074@ais01.ee.ncku.edu.tw

    Direct SSH

    SSH compression : 

    SSH-browser

    X11-forwarding : 

            (remote display is forwarded through SSH)

      ▶ For more info, ctrl+click on help or visit our website.
Welcome to Ubuntu 20.04.3 LTS (GNU/Linux 5.13.0-40-generic x86 64)
 * Documentation: https://help.ubuntu.com
 * Management:
                    https://landscape.canonical.com
                    https://ubuntu.com/advantage
 * Support:
1 device has a firmware upgrade available.
Run `fwupdmgr get-upgrades` for more information.
166 updates can be applied immediately.
154 of these updates are standard security updates.
To see these additional updates run: apt list --upgradable
The list of available updates is more than a week old.
To check for new updates run: sudo apt update
Your Hardware Enablement Stack (HWE) is supported until April 2025.
Last login: Fri Mar 17 15:45:02 2023 from 140.116.178.177
set XCELIUM version: XCELIUM 22.03.003 (default)
set verdi version: 2019.06/ (default)
user:~> passwd
Changing password for aoc 074.
Current password:
```

#### **Outline**



Environment Setup

Change your passwd

• Introduction to Linux commands(有需要再回來用就好)

How to run Verilog code?

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pwd (Print Working Directory):

```
<u>user</u>:~> pwd
/home/A0C2023/aoc2023_074
```

Display the full pathname of the current working directory.

• Is (List):

```
user:~> ls
test1.txt test2.txt test_dir1
```

List the file & directory in current working directory.

```
• Is -a (List All) :
```

```
user:~> ls -a
. .bash_history .bashrc .config .local test1.txt test_dir1
.. .bash_logout .cache .kshrc .profile test2.txt .Xauthority
```

List the (all) hidden files & directories in current working directory.



- cd (Change Directory) :
- 1. Change the current working directory
  - cd + Path(relative path)
  - cd ...
  - cd user
  - cd + Path(absolutive path)

user:/home/C02022> cd ..
user:/home> 
user:/home> ls
C02022 user
user:/home> cd user
user:/home/user>

```
user:/usr> cd /home/C02022/
user:/home/C02022> ■
```

- 2. Change directory to previous directory
  - cd -

```
<u>user</u>:/usr> cd /home/C02022/
<u>user</u>:/home/C02022> <mark>cd -</mark>
<u>user</u>:/usr> ■
```



mkdir (Make Directory) :

(e.g. mkdir dirdir)

touch (Create an empty file):

touch + file\_name

user:~> ls
ddd dirdir test1.txt test\_dir1 xyz.txt
user:~> rm -rf test\_dir1/
user:~> ls
ddd dirdir test1.txt xyz.txt
user:~> ls
ddd dirdir test1.txt xyz.txt
user:~> touch abc.txt
user:~> ls
abc.txt\_ddd\_dirdir test1.txt xyz.txt
user:~> clear

ddd dirdir test1.txt xyz.txt
user:~> touch abc.txt
user:~> ls
abc.txt ddd dirdir test1.txt xyz.txt

<u>user</u>:~> mkdir dirdir

(relative or absolutive path)

user:~> ls

user:~> ls

2. co2022\_ta14@140.116.178.181 × \
user:~>

dirdir test1.txt test2.txt test\_dir1

clear :

Clear the terminal screen



- Copy a file (relative or absolutive path)
- cp + file + new\_file\_name
  - cp test1.txt abc.txt
- Copy a file to a directory
- cp + file + directory\_path

  (relative or absolutive path)
  - cp test1.txt dirdir

- Copy a directory
- cp + -rf + directory\_name + new\_directory\_name

  (relative or absolutive path)
  - cp -rf dirdir ddd

```
user:~> ls
dirdir test1.txt test2.txt test_dir1
user:~> cp test1.txt abc.txt
user:~> ls
abc.txt dirdir test1.txt test2.txt test_dir1
```

```
user:~> ls
abc.txt dirdir test1.txt test2.txt test_dir1
user:~> cp test1.txt dirdir
user:~> cd dirdir
user:~/dirdir> ls
test1.txt
```

```
user:~> ls
abc.txt dirdir test1.txt test2.txt test_dir1
user:~> cp -rf dirdir ddd
user:~> ls
abc.txt ddd dirdir test1.txt test2.txt test_dir1
user:~>
```



tab

Autocomplete commands, filenames or folder names.





 $\underline{r}:\sim>$  touch abc.txt

• ↑ , ↓ (up and down arrow key)

Recall previously-entered commands to the command line.

ctrl + c (lt's not a copy function.)

Terminate the current running process

### **Outline**



Environment Setup

Change your passwd

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How to run Verilog code?(重要)

• 如何看波形

## How to run verilog code?



• (必要)Step1—Create/upload folder:

AOClab0

• (必要)Step2—Change Directory:

cd AOClab0

• (非必要)Step3—List the file & directory in current working directory:

Is(檢查要執行的.v檔)

• (必要)Step4—run .v檔: 格式: ncverilog 檔名.v

ncverilog Mux2\_1.v

## How to run verilog code?



• (必要)Step5—run testbench測試檔:

格式: ncverilog 測試檔名.v +access+r

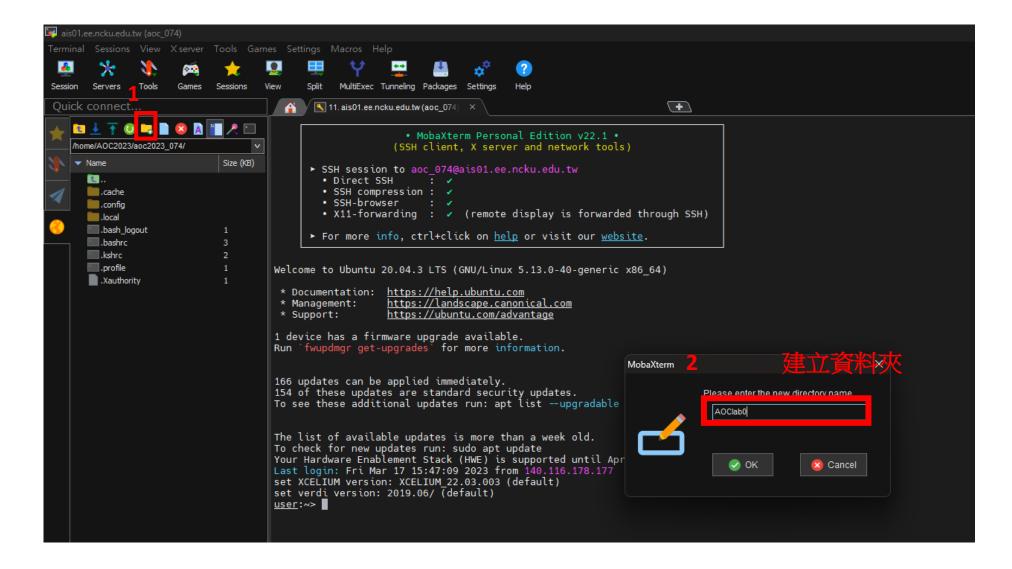
nWave &

ncverilog tb\_Mux2\_1.v +access+r

• (必要)Step6—run Wave:

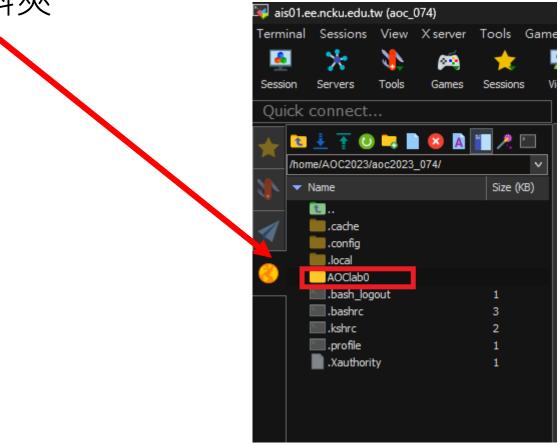
!!!注意注意!!! 有空一格





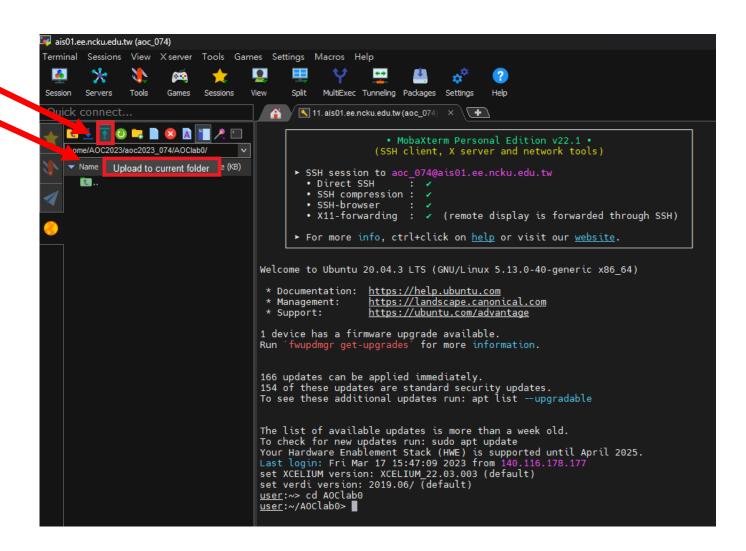


• 確認所建立的資料夾



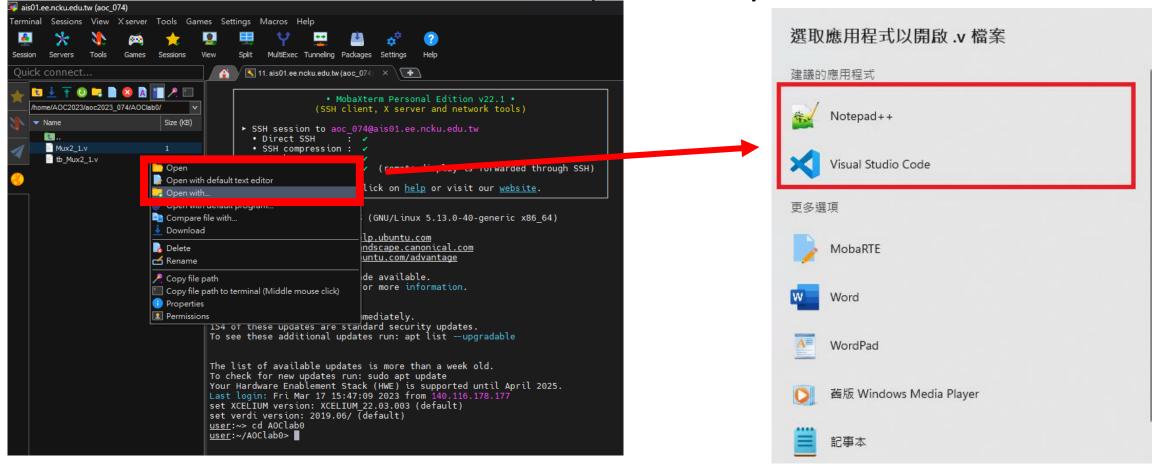


• 上傳.v檔以及測試檔



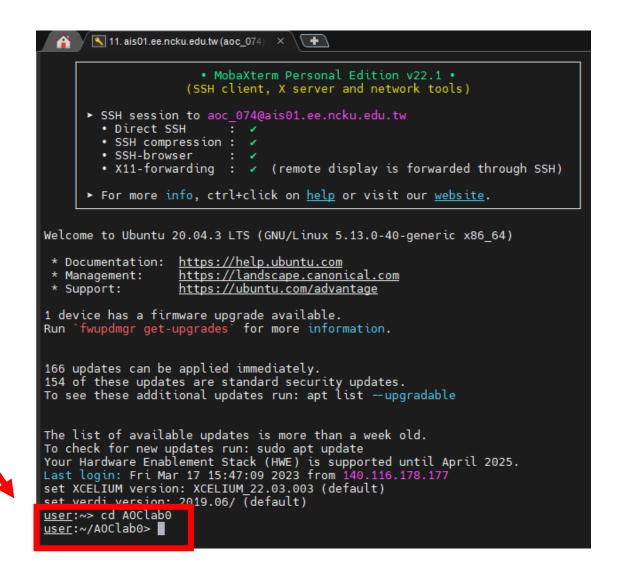


· 依照個人喜好在編輯器上編輯檔案(推薦Notepad++或Visual Studio Code)





• 切換工作目錄



## Step4 (以執行2-to-1 Mux為例)



ncverilog Mux2\_1.v

```
user:~/AOClab0> ncverilog Mux2 1.v
TOOL: xmverilog 22.03-s003: Started on Mar 17, 2023 at 22:02:50 CST
xmverilog(64): 22.03-s003: (c) Copyright 1995-2022 Cadence Design Systems, Inc.
xmverilog: *W,NCEXDEP: Executable (noverilog) is deprecated. Use (xmverilog) instead.
              Caching library 'worklib' ...... Done
       Elaborating the design hierarchy:
       Building instance overlay tables: ..... Done
       Building instance specific data structures.
       Loading native compiled code:
       Design hierarchy summary:
                         Instances Unique
              Modules:
              Vectored wires:
              Cont. assignments: 0
       Writing initial simulation snapshot: worklib.Mux2 1:v
*Verdi* Loading libsscore xcelium171.so
xcelium> source /usr/cad/cadence/XCELIUM/XCELIUM 22.03.003/tools/xcelium/files/xmsimrc
xcelium> run
xmsim: *W,RNQUIE Simulation is complete.
                                                . V檔順利跑過
xcelium> exit
TOOL: xmverilog
                     22.03-s003: Exiting on Mar 17, 2023 at 22:02:51 CST (total: 00:00:01)
user:~/AOClab0>
```

## Step4 (以執行2-to-1 Mux為例)



ncverilog Mux2\_1.v

```
user:~/AOClab0> ncverilog Mux2 1.v
TOOL: xmverilog
                     22.03-5003: Started on Mar 17, 2023 at 22:05:30 CST
xmverilog(64): 22.03-s003: (c) Copyright 1995-2022 Cadence Design Systems, Inc.
xmverilog: *W,NCEXDEP: Executable (noverilog) is deprecated. Use (xmverilog) instead.
Recompiling ... reason: file './Mux2_1.v' is newer than expected.
        expected: Fri Mar 17 21:40:18 2023
        actual: Fri Mar 17 22:05:26 2023
file: Mux2 1.v
                                   在Mux2_1. v檔的第五行、第八欄有errors及warnings
endmodule
xmvlog: *E,EXPSMC (Mux2 1.v,5|8): expecting a semicolon (';') [6.1(IEEE)].
        module worktib.Mux2 1:v
errors 1 warnings: 0
xmvlog: *W,NOTOPL: no top-level unit found, must have recursive instances.
        Total errors/warnings found outside modules and primitives:
               errors: 0, warnings 1
xmverilog: *E,VLGERR: An error occurred during parsing. Review the log file for errors with the code *E and fix those identified problems to proceed. Exiting with code (status 1).
TOOL: xmverilog
                       22.03-s003: Exiting on Mar 17, 2023 at 22:05:30 CST (total: 00:00:00)
user:~/AOClab0>
```

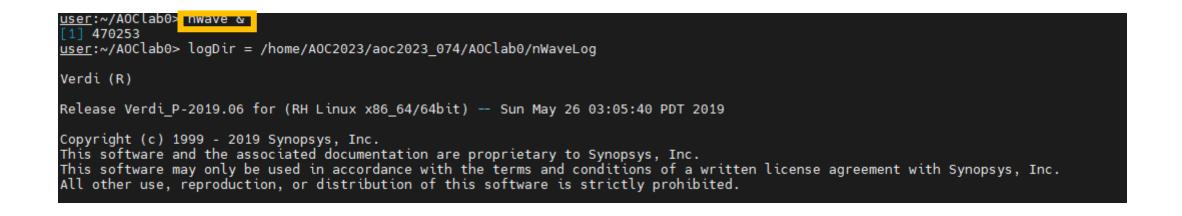


ncverilog tb\_Mux2\_1.v +access+r

```
user:~/AOC ab0> noverilog tb Mux2 1.v +access+r
                                                ar 17, 2023 at 22:14:22 CST
xmverilog(64): 22.03-s003: (c) Copyright 1995-2022 Cadence Design Systems, Inc. xmverilog: w,NCEXDEP: Executable (ncverilog) is deprecated. Use (xmverilog) instead. file: tb_ux2_1.v
        module worklib.tb Mux2 1:v
                errors: 0, warnings: 0
                Caching library 'worklib' ...... Done
       Elaborating the design hierarchy:
       Building instance overlay tables: ..... Done
        Generating native compiled code:
               worklib.Mux2 1:v <0x59ca9e45>
                       streams: 1, words: 208
               worklib.tb Mux2 1:v <0x38857ac0>
                       streams: 7, words: 6441
       Building instance specific data structures.
       Loading native compiled code:
       Design hierarchy summary:
                                 Instances Unique
                Modules:
               Registers:
                Scalar wires:
                Vectored wires:
                Initial blocks:
                Cont. assignments:
                Pseudo assignments:
               Simulation timescale: 10ps
        Writing initial simulation snapshot: worklib.tb Mux2 1:v
_oading snapshot worklib.tb Mux2 1:v ........................ Done
*Verdi* Loading libsscore xcelium171.so
xcelium> source /usr/cad/cadence/XCELIUM/XCELIUM 22.03.003/tools/xcelium/files/xmsimrc
FSDB Dumper for Xcelium, Release Verdi P-2019.06, Linux x86 64/64bit, 05/26/2019
(C) 1996 - 2019 by Synopsys, Inc.
********************
  The simulator version is newer than the FSDB Writer version which
  may cause abnormal behavior, please contact Cadence support for
 *************************
*Verdi* : Create FSDB file 'Mux2 1.fsdb'
*Verdi* : Begin traversing the scopes, layer (0).
*Verdi* : End of traversing.
                  0In0=
                             20.In1=
                                          10.sel=1.out=
                 20In0=
                              5, In1=
                                          10, sel=0, out=
                 40In0=
                            20.In1= 10.sel=0.out=
Simulation complete via $finish(1) at time 140 NS + 0
./tb Mux2 1.v:17 #100 $finish;
xcelium> exit
TOOL: xmverilog
                       22.03-s003: Exiting on Mar 17, 2023 at 22:14:23 CST (total: 00:00:01)
```



• nWave & 波形驗證



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Environment Setup

Change your passwd

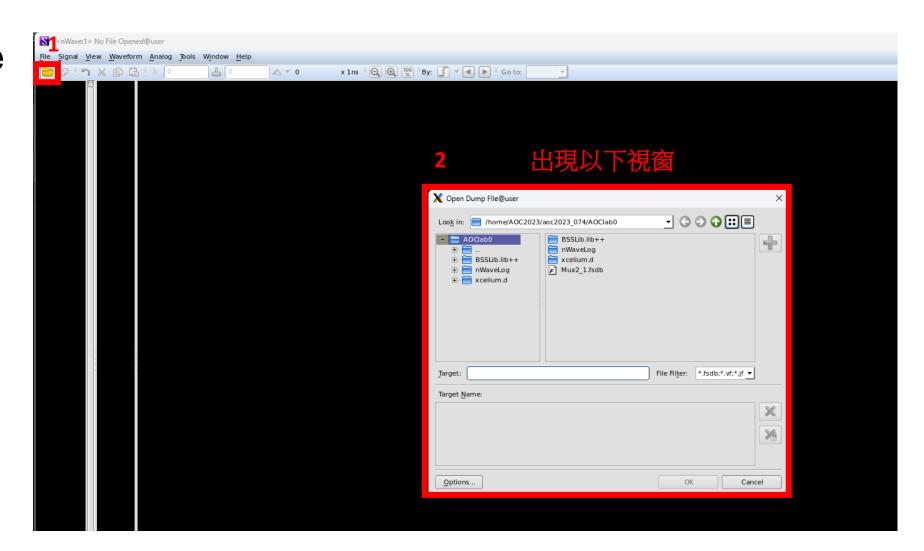
Introduction to Linux commands

How to run Verilog code?

• 如何看波形



• (i) Open File



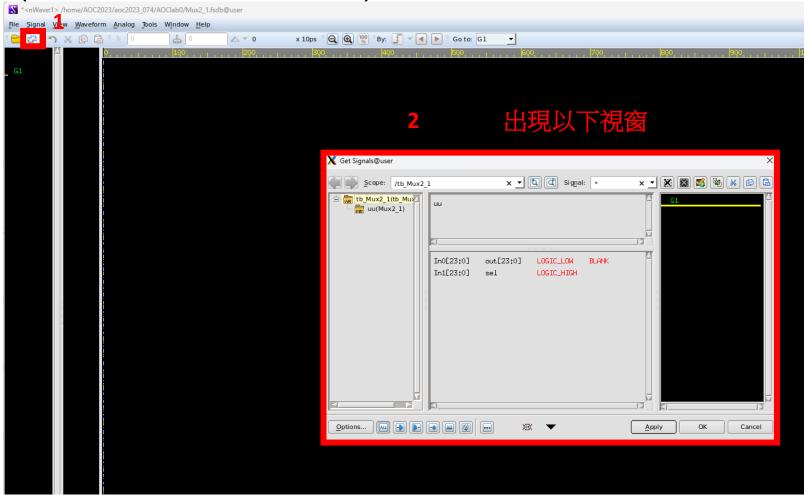


• (ii) Choose Mux2\_1.fsdb



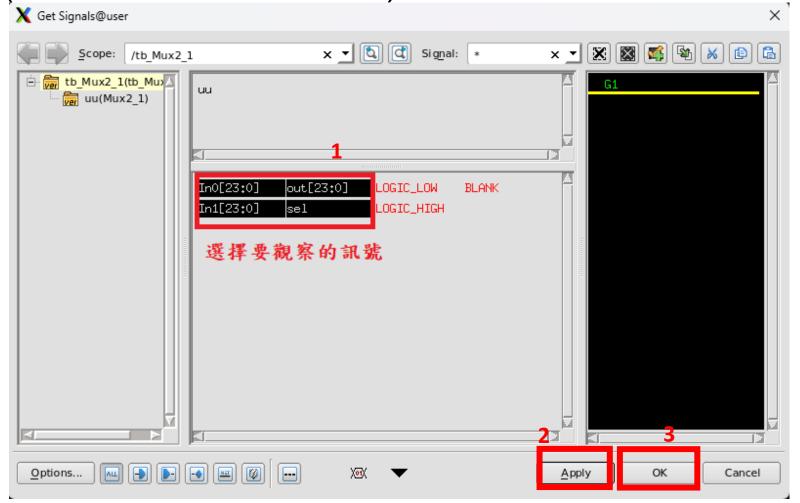


• (iii) Get signals (選擇要看的訊號就好)



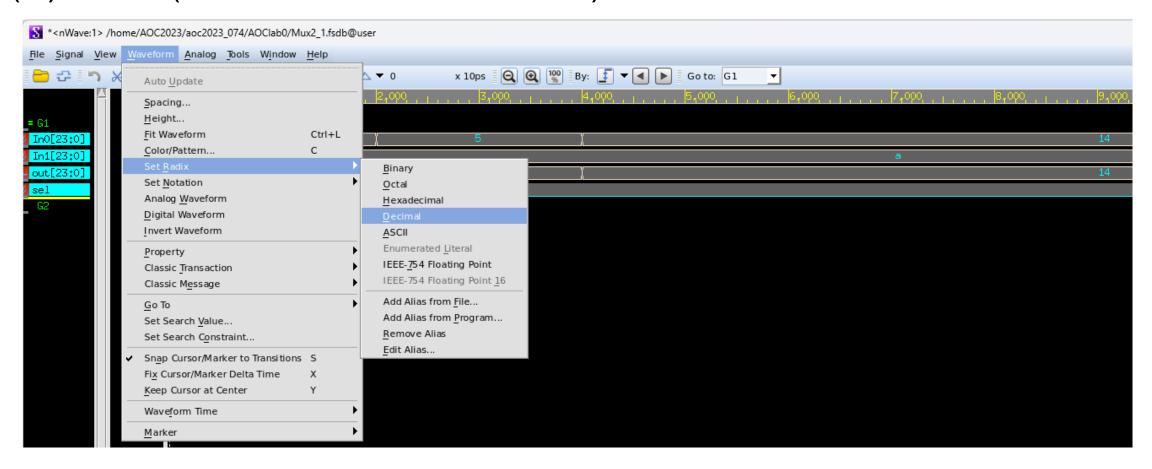


• (iii) Get signals (選擇要觀察的訊號就好)





• (iv) wave (調成十進位方便觀察波形)





• (v) result

