



The image shows a screenshot of a Verilog code editor. The top toolbar contains various icons for editing and simulation. Below the toolbar, the file name 'logicgates.v' is displayed. The main editing area contains the following Verilog code:

```
1 module logicgates(a,b,o1,o2,o3,o4,o5,o6,o7);
2   input a,b;
3   output o1,o2,o3,o4,o5,o6,o7;
4   and(o1,a,b);
5   or(o2,a,b);
6   xor(o3,a,b);
7   nand(o4,a,b);
8   nor(o5,a,b);
9   xnor(o6,a,b);
10  not(o7,a);
11  endmodule
```

The first line of the code is highlighted in blue. The bottom status bar shows the line numbers 267 and 268, and the text 'ab/'.



