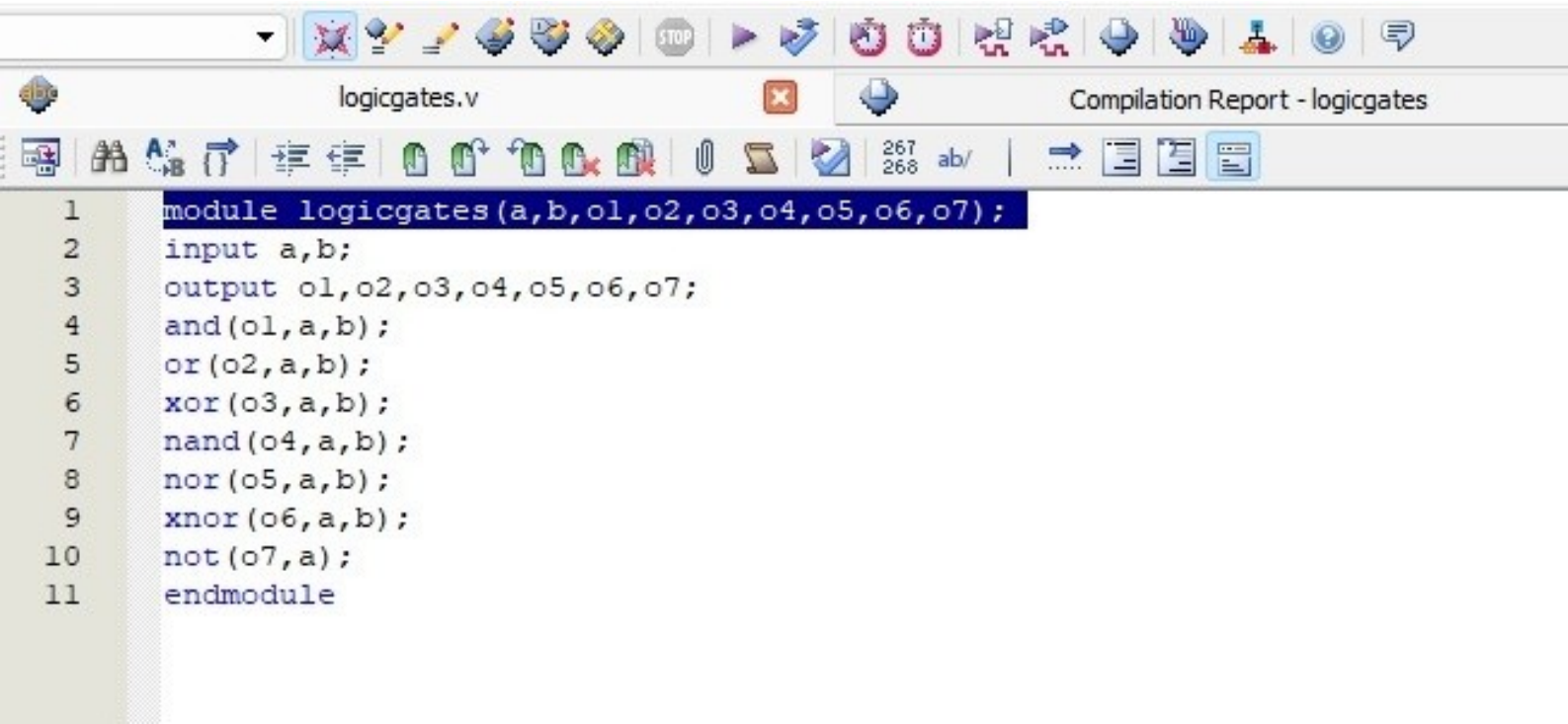
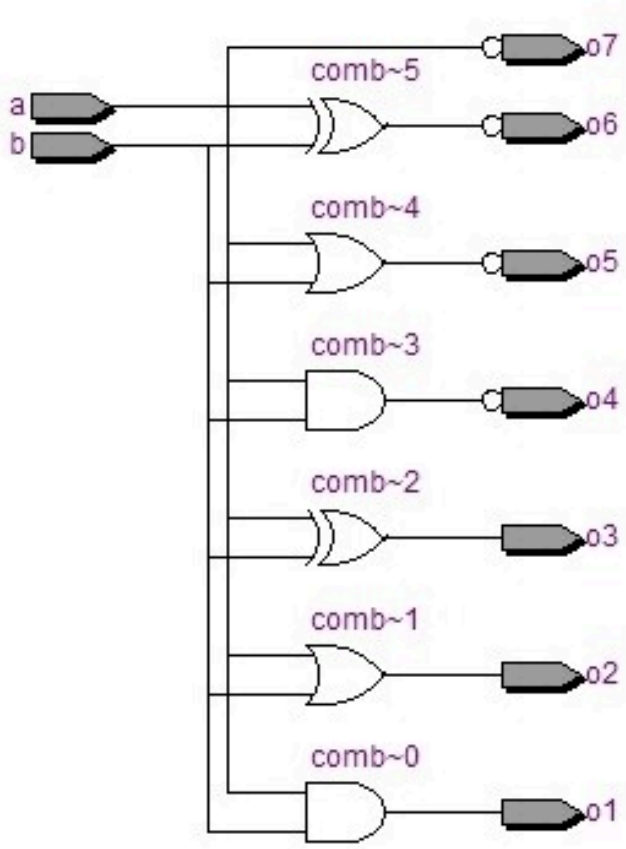
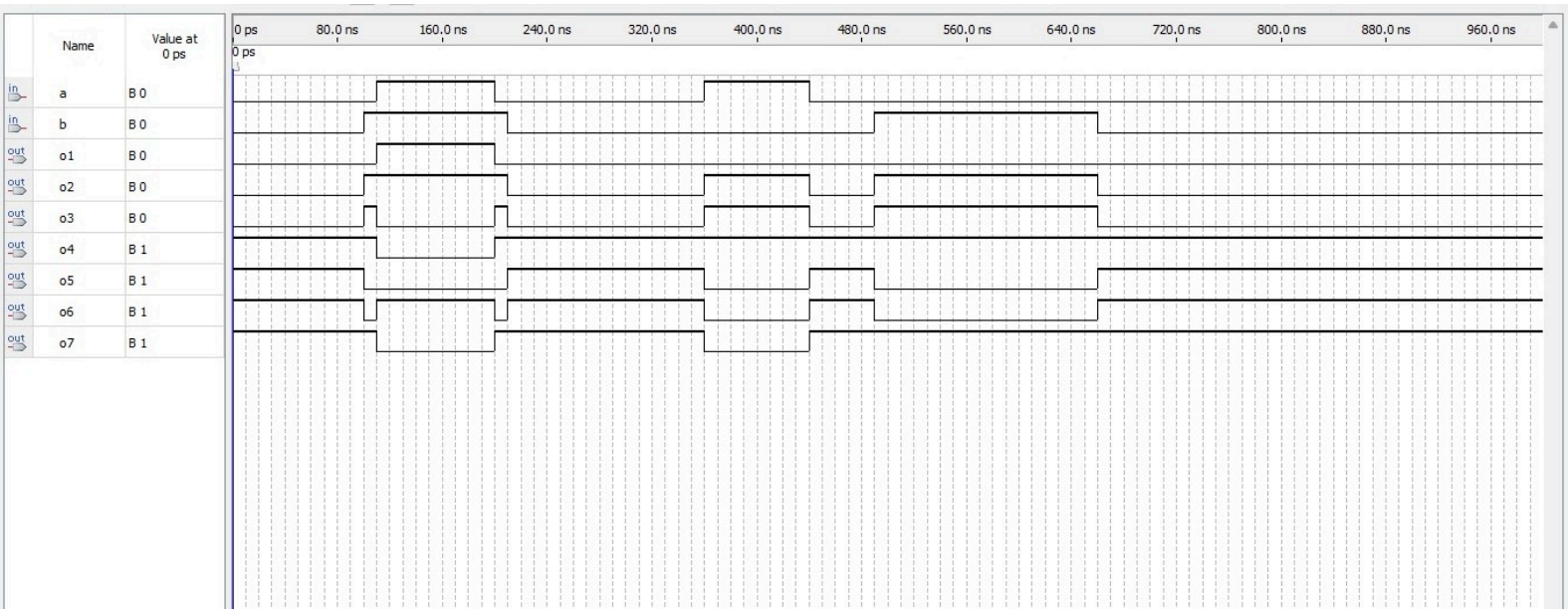

logicgates.v
Compilation Report - logicgates
1 module logicgates(a,b,o1,o2,o3,o4,o5,o6,o7);
2 input a,b;
3 output o1,o2,o3,o4,o5,o6,o7;
4 and(o1,a,b);
5 or(o2,a,b);
6 xor(o3,a,b);
7 nand(o4,a,b);
8 nor(o5,a,b);
9 xnor(o6,a,b);
10 not(o7,a);
11 endmodule


a
b
comb~5
comb~4
comb~3
comb~2
comb~1
comb~0
o7
o6
o5
o4
o3
o2
o1


Name
Value at 0 ps
a
B 0
b
B 0
o1
B 0
o2
B 0
o3
B 0
o4
B 1
o5
B 1
o6
B 1
o7
B 1
0 ps
80.0 ns
160.0 ns
240.0 ns
320.0 ns
400.0 ns
480.0 ns
560.0 ns
640.0 ns
720.0 ns
800.0 ns
880.0 ns
960.0 ns