GNUSim8085 - 8085 Microprocessor Simulator

File Reset Assembler Debug Help

Registers

A	00	
BC	09	00
DE	00	02
HL	21	34
PSW	00	00
PC	42	13
SP	FF	FF
Int-Reg	00	

Flag

S 0
Z 1
AC 0
P 1
C 1

Decimal - Hex Convertion

Decimal: 0 — To Hex
Hex: 0 — To Dec

I/O Ports

0 − + 00
Update Port Value

Memory

8055 − + 08
Update Memory

Load me at

```
1  INX H
2  MOV B, M
3  DCR C
4  LOOP: INX H
5  MOV A, M
6  CMP B
7  JC SKIP
8  MOV B, A
9  SKIP: DCR C
10 JNZ LOOP
11 LXI H, 8500
12 MOV M, B
13 HLT
```

Data | Stack | KeyPad | **Memory** | I/O Ports

Start 8050 OK

Address (Hex)	Address	Data
1F72	8050	5
1F73	8051	2
1F74	8052	7
1F75	8053	4
1F76	8054	9
1F77	8055	8
1F78	8056	0
1F79	8057	0
1F7A	8058	0
1F7B	8059	0
1F7C	8060	0
1F7D	8061	0
1F7E	8062	0
1F7F	8063	0

Line No	Assembler Message
0	Program assembled successfully

Simulator: Idle

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PC	42	13
SP	FF	FF
Int-Reg	00	

Flag

S 0
Z 1
AC 0
P 1
C 1

Decimal - Hex Convertion

Decimal: 0 — To Hex
Hex: 0 — To Dec

I/O Ports

0 − + 00
Update Port Value

Memory

8501 − + 0
Update Memory

Load me at

```
1  INX H
2  MOV B, M
3  DCR C
4  LOOP: INX H
5  MOV A, M
6  CMP B
7  JC SKIP
8  MOV B, A
9  SKIP: DCR C
10 JNZ LOOP
11 LXI H, 8500
12 MOV M, B
13 HLT
```

Data | Stack | KeyPad | **Memory** | I/O Ports

Start 8500 OK

Address (Hex)	Address	Data
2134	8500	9
2135	8501	0
2136	8502	0
2137	8503	0
2138	8504	0
2139	8505	0
213A	8506	0
213B	8507	0
213C	8508	0
213D	8509	0
213E	8510	0
213F	8511	0
2140	8512	0
2141	8513	0

Line No	Assembler Message
0	Program assembled successfully

Simulator: Idle