GNUSim8085 - 8085 Microprocessor Simulator

File Reset Assembler Debug Help

Registers

A	16	
BC	06	04
DE	00	00
HL	80	01
PSW	00	00
PC	42	13
SP	FF	FF
Int-Reg	00	

Flag

S 0
Z 1
AC 1
P 1
C 0

Decimal - Hex Convertion

Decimal: 0 — To Hex
Hex: 0 — To Dec

I/O Ports

0 − + 00
Update Port Value

Memory

32848 − + 14
Update Memory

Load me at

```
1  LDA 8050H
2  ANI 01
3  JZ LOOP1
4  MVI A,11
5  JMP LOOP2
6  LOOP1: MVI A,22
7  LOOP2: STA 8051H
8  HLT
```

Data | Stack | KeyPad | **Memory** | I/O Ports

Start 8050h OK

Address (Hex)	Address	Data
8050	32848	20
8051	32849	22
8052	32850	0
8053	32851	0
8054	32852	0
8055	32853	0
8056	32854	0
8057	32855	0
8058	32856	0
8059	32857	0
805A	32858	0
805B	32859	0
805C	32860	0
805D	32861	0

Line No	Assembler Message
0	Program assembled successfully

Simulator: Idle

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File Reset Assembler Debug Help

Registers

A	0B	
BC	06	04
DE	00	00
HL	80	01
PSW	00	00
PC	42	13
SP	FF	FF
Int-Reg	00	

Flag

S 0
Z 0
AC 1
P 0
C 0

Decimal - Hex Convertion

Decimal: 0 — To Hex
Hex: 0 — To Dec

I/O Ports

0 − + 00
Update Port Value

Memory

32848 − + 13
Update Memory

Load me at

```
1  LDA 8050H
2  ANI 01
3  JZ LOOP1
4  MVI A,11
5  JMP LOOP2
6  LOOP1: MVI A,22
7  LOOP2: STA 8051H
8  HLT
```

Data | Stack | KeyPad | **Memory** | I/O Ports

Start 8050h OK

Address (Hex)	Address	Data
8050	32848	19
8051	32849	11
8052	32850	0
8053	32851	0
8054	32852	0
8055	32853	0
8056	32854	0
8057	32855	0
8058	32856	0
8059	32857	0
805A	32858	0
805B	32859	0
805C	32860	0
805D	32861	0

Line No	Assembler Message
0	Program assembled successfully

Simulator: Idle