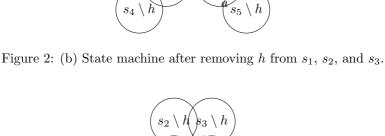


Figure 1: (a) Initial state machine with states s_1 , s_2 , s_3 , s_4 , and s_5 .

 s_3



 s_4

Figure 3: (c) State machine after further simplification.