VE370 Homework 1

Q1

$$\mathbf{CPU\ Time} = \frac{\mathrm{IC} \times \mathrm{CPI}}{\mathrm{Clock\ Rate}} = \frac{100000 \times 1.5}{500\mathrm{MHz}} = \frac{IC_B \times 2.3}{850\mathrm{MHz}}$$

Then from the equation, calculate that $IC_B \approx 110870$.

Q2

$$\begin{aligned} \mathbf{CPU\ Time} &= \frac{\mathrm{IC} \times \mathrm{CPI}}{\mathrm{Clock\ Rate}} = \frac{1100 \times 1 + (200 + 150) \times 7 + 120 \times 3}{2 \times 10^9} = 1.955 \times 10^{-6}\ [s] \\ \mathbf{CPI} &= \frac{\mathrm{CPU\ time} \times \mathrm{Clock\ Rate}}{IC} = \frac{1955 \times 2}{1570} \approx 2.49 \end{aligned}$$

Q3

1)
$$\mathbf{CPU\ Time} = \frac{\mathrm{IC} \times \mathrm{CPI}}{\mathrm{Clock\ Rate}} = \frac{2560 \times 1 + (1280 \times 4) + (256 \times 2)}{3 \times 10^9} = 2.73 \times 10^-6 \ [s]$$

2) **CPU Time** =
$$\frac{1280 \times 1 + (640 \times 6) + (128 \times 2)}{3 \times 10^9} \approx 1.79 \times 10^{-6} \ [s]$$

3) **CPU Time** =
$$\frac{640 \times 1 + (320 \times 8) + (64 \times 2)}{3 \times 10^9} \approx 1.11 \times 10^{-6} \ [s]$$

4) **CPU Time** =
$$\frac{320 \times 1 + (160 \times 10) + (32 \times 2)}{24 \times 10^9} \approx 6.61 \times 10^{-7} \ [s]$$

Q4

$$x = x - y + z - 72$$

```
add $t0, $s0, $s2 # $t0 = x + z
addi $t1, $s2, 72 # $t1 = y + 72
sub $s0, $t0, $t1 # $s0 = $t0 - $t1
```

Q5

B[8] = i + A[j]

```
sll $t2, $t1, 2  # $t2 = $t1 * 4 = 4j
add $t2, $t2, $s5  # $t2 = address of A[j]
lw $t3, 0($t2)  # load from memory to register, $t3 = A[j]
add $t3, $t3, $t0  # $t3 = i + A[j]
sw $t3, 32($s6)  # store the result from the register to memory
```

Q6

Assume a, b, c, d in \$s0, \$s1, \$s2, \$s3 respectively. The address of array A is 0x00000100, such that A[0] = 0x000011f0

The value of \$s0 is 0x0000F2a4

Q7

```
lui $s0, 0x1000 # $s0 = 0x10000000
lb $s2, 2($s0)
```

In \$s2: 0x0000066

Q8

```
slt $t2, $t0, $t1  # signed $t0 < $t1, so $t2 = 1
beq $t2, $0, ELSE  # $t2 != 0, not branch to ELSE
j DONE  # jump to done
ELSE: addi $t2, $0, -2 # not executed
DONE: .....</pre>
```

The value of \$t2 is 1.

Q9

```
positive:
    addi $sp, $sp, -12 # adjust stack to make room for 3 items
                    # store the return address
   sw $ra, 8($sp)
   sw $a1, 4($sp)
                     # store the function arguments
   sw $a0, 0($sp)
   jal addit
   lw $a0, 0($sp) # restore function arguments
   lw $a1, 4($sp)
   lw $ra, 8($sp)
                  # restore return address
   addi $sp, $sp, 12 # pop 3 items from stack
    slt v0, zero, v0 # if (0 < v0), v0 = 1; else v0 = 0
   jr $ra
addit:
    add $t0, $a0, $a1 # parameter variable a, b correspond to the
argument register
    add $v0, $t0, $zero # copy into a return register
    jr $ra
```







