Test Report

Project Information

Customer:	M5Stack	EUT:	test
Model:	—	Mode:	1000.000MHz-3000.000MHz_15s
Engineer:	y	Remark:	首次测试

Test Graph
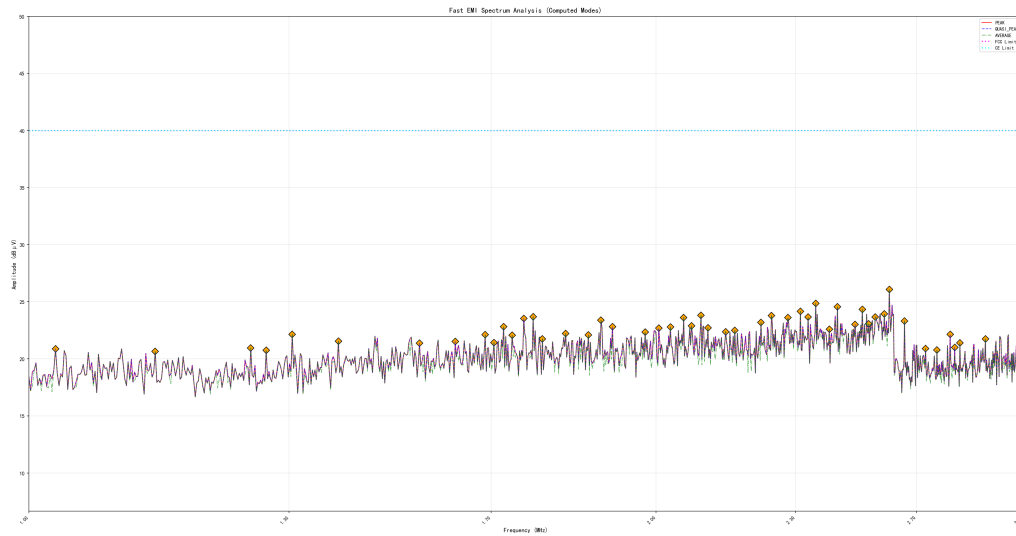
Suspected List

NO.	Freq [MHz]	Amplitude [dBuV]	FCC Limit [dBuV]	FCC Margin [dB]	CE Limit [dBuV]	CE Margin [dB]	Status
1	2588.000	26.09	40.0	-13.91	40.0	-13.91	Pass
2	2386.000	24.86	40.0	-15.14	40.0	-15.14	Pass
3	2444.000	24.55	40.0	-15.45	40.0	-15.45	Pass
4	2512.000	24.34	40.0	-15.66	40.0	-15.66	Pass
5	2346.000	24.16	40.0	-15.84	40.0	-15.84	Pass
6	2574.000	23.95	40.0	-16.05	40.0	-16.05	Pass
7	2102.000	23.82	40.0	-16.18	40.0	-16.18	Pass
8	2272.000	23.79	40.0	-16.21	40.0	-16.21	Pass
9	1746.000	23.70	40.0	-16.30	40.0	-16.30	Pass
10	2548.000	23.67	40.0	-16.33	40.0	-16.33	Pass
11	2366.000	23.66	40.0	-16.34	40.0	-16.34	Pass
12	2062.000	23.63	40.0	-16.37	40.0	-16.37	Pass

AI测试分析报告

未检测到符合条件的异常频点（Fail点及Margin≤2
dB临界点）。所有频点Status均为Pass，且Margin值均≤-13.91 dB（远小于2 dB），无超标或临界风险。