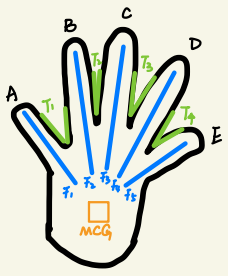
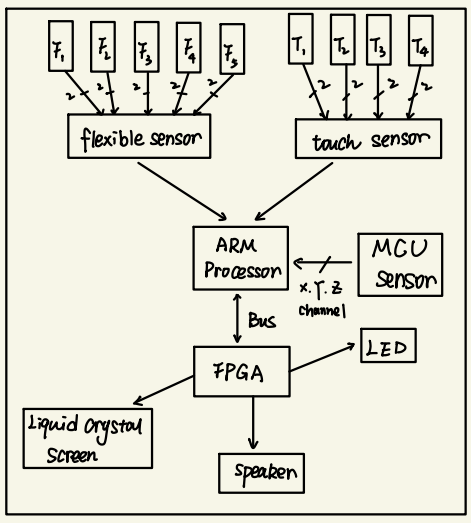
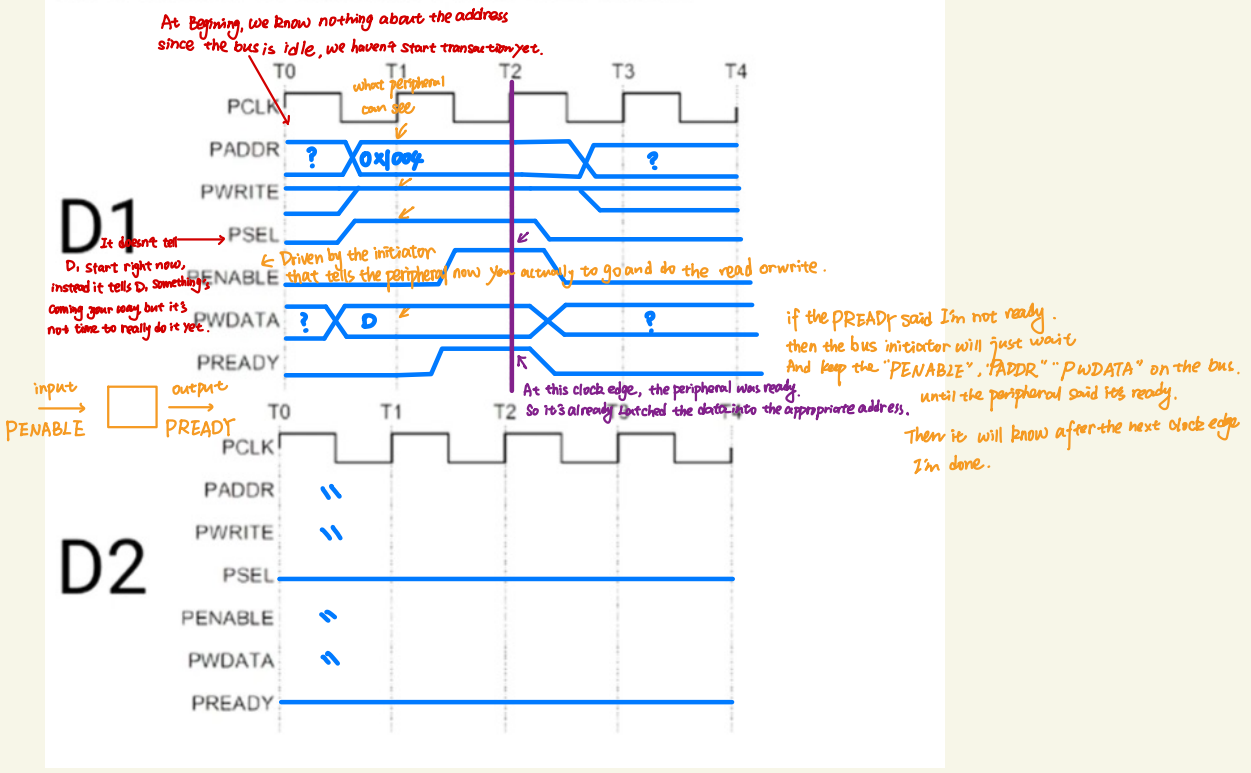
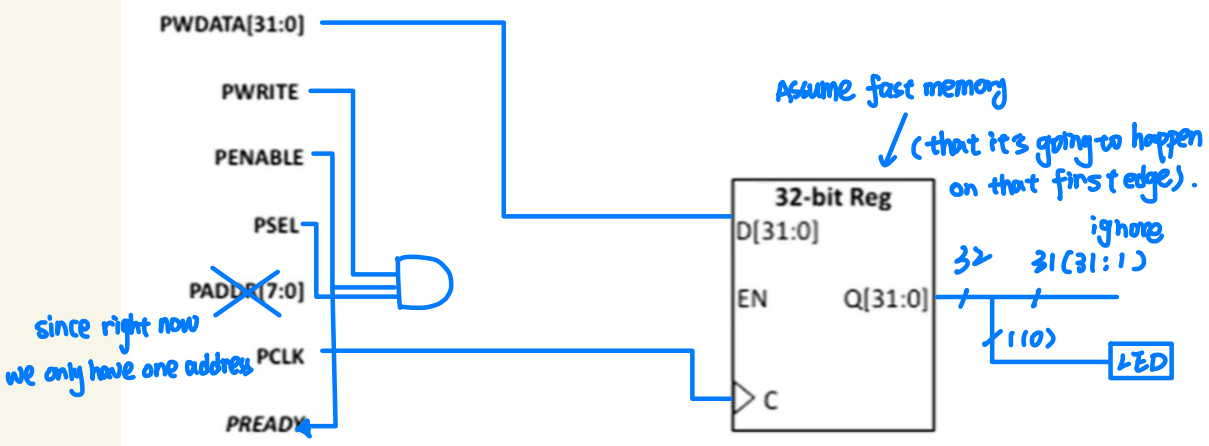
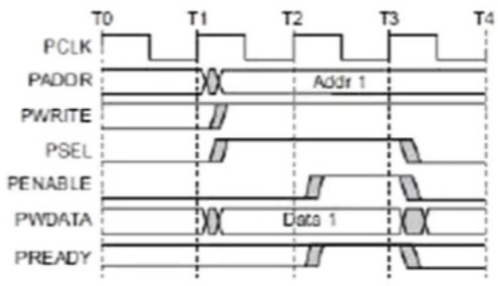




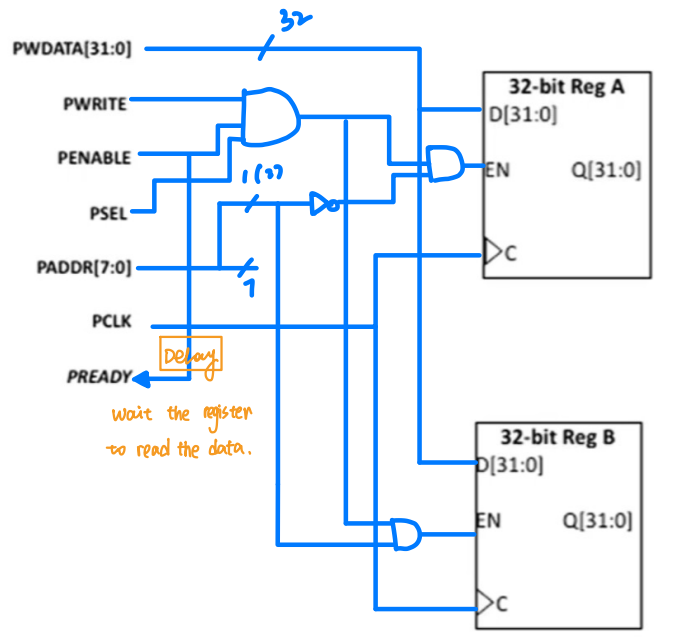
CPU stores to 0x00001004 w.o. stalls



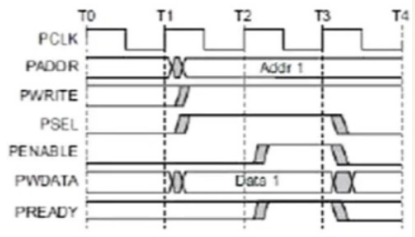




Reg A should be written at address 0x00001000
Reg B should be written at address 0x00001004



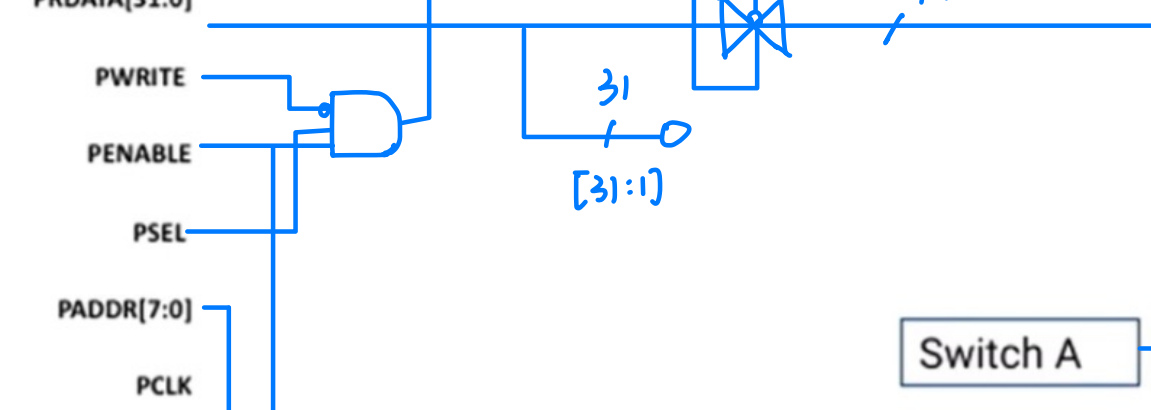
we want the start stay the same, but have the end be later. So we have to actually tell the initiator whatever you doing, keep doing it for a while until I tell you I'm ready.

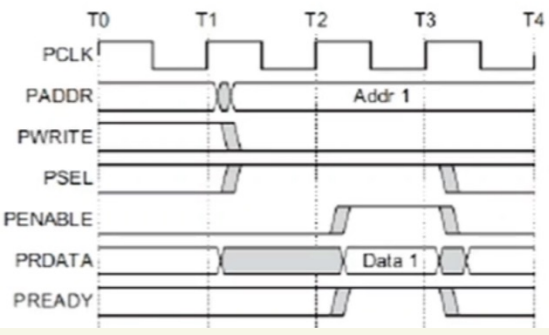


Assuming APB only gets lowest 8 bits of address

Device provides data from switch for any of its addresses

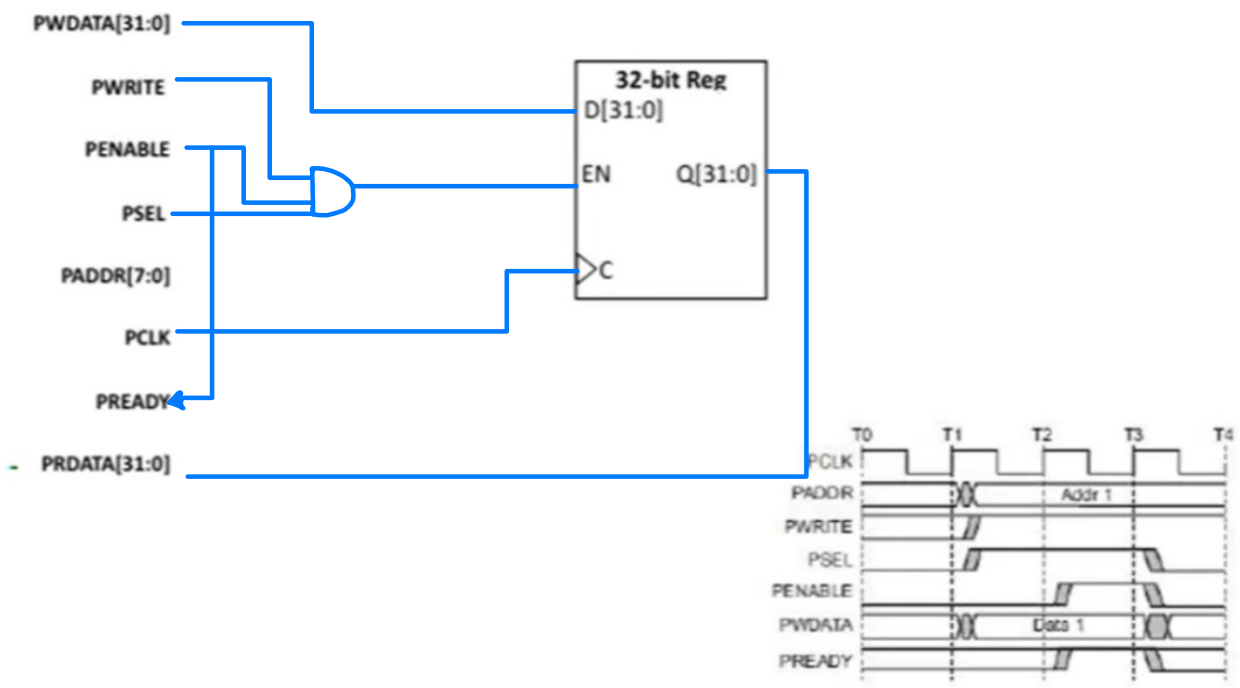
if we just do this, then this transaction will never done. and we need to tell it when we finished.



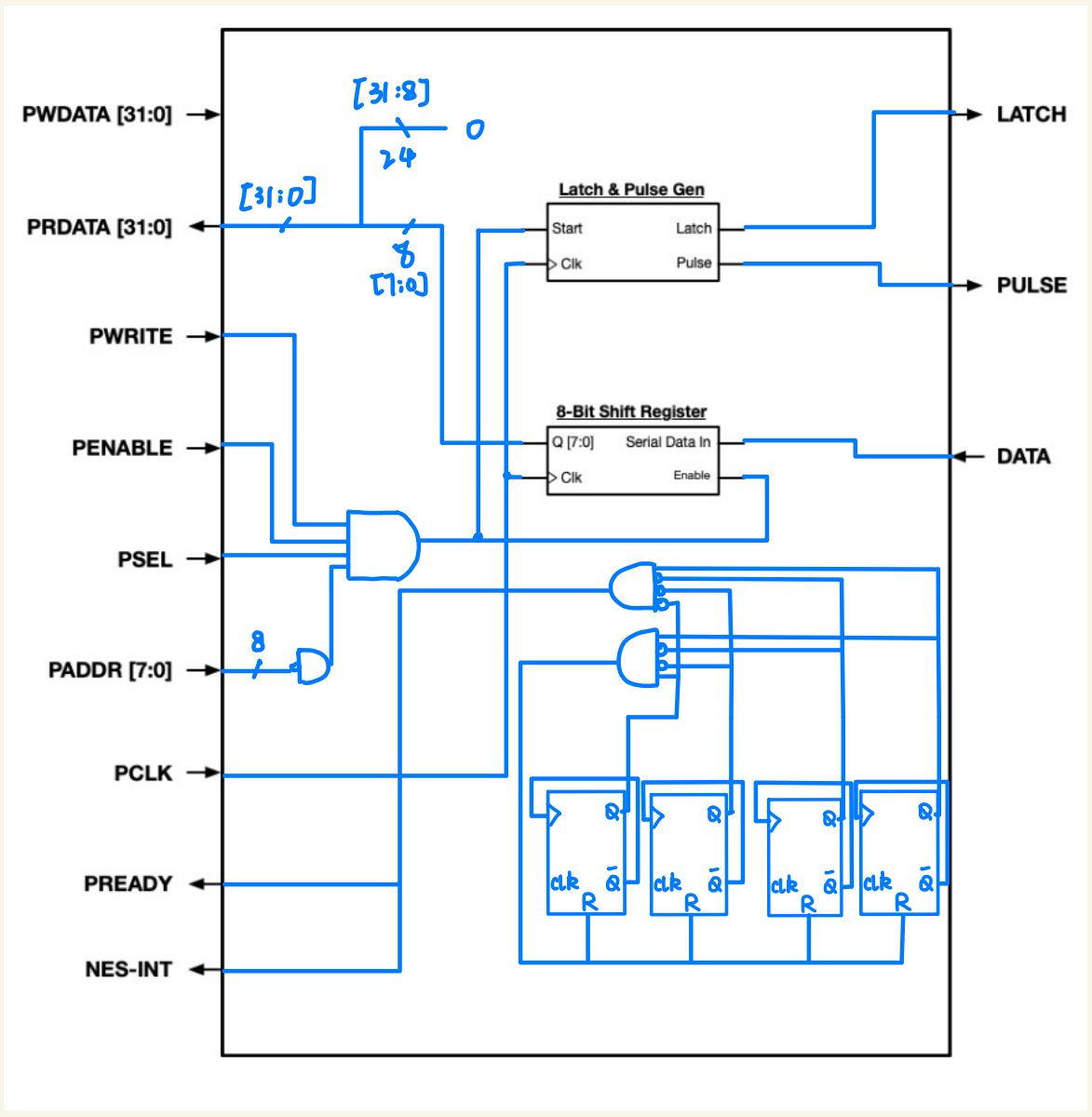


Switch A for 0x00001000, B for 0x00001004

All reads read from register, all writes write

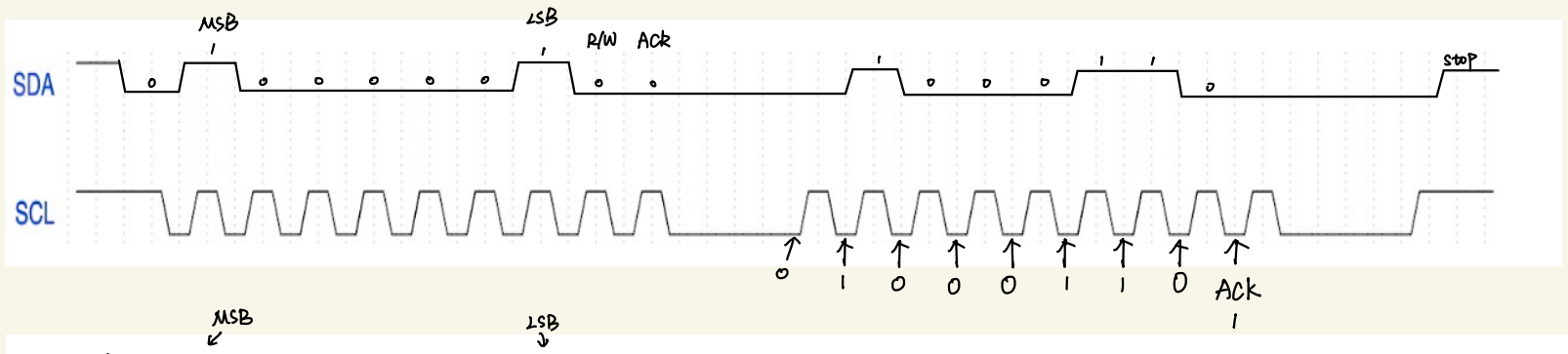


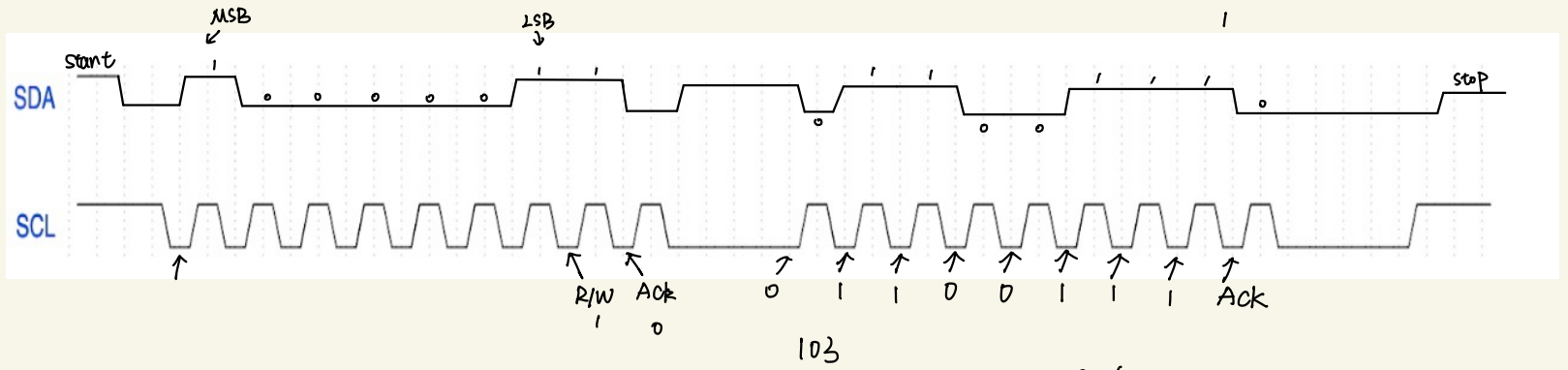
Assuming APB only gets lowest 8 bits of address



Address 0x41 F: 70

0 1 0 0 0 0 0 1





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0 1 1 0 0 1 1 1